



# MASK ROM

☆ New product  
★ Under development

## ■ MASK ROMs

### ◆ Features

- Product lineup covers 11 capacity ranges from 256 k-bit to 128 M-bit.
- Product variations with 3 types of pinout including JEDEC standard EPROM, Mask ROM specific and Flash memory compatible pinout.

### <Page Mode>

Capacity	Bit configuration	Pinout**	Page mode	Model No.	User's No.	Access time (ns) MAX. Cycle time (ns) MIN.	Access time at Page Mode (ns) MAX.	Page size (words)	Current consumption (mA)	Supply Voltage (V)	Package
4M	×8/×16			LH53B4P00D/N	LH-5B4PXX	120	50	4	100	4.5 to 5.5	40DIP/40SOP
				LH53BV8600D/N	LH-5D86XX	100	50	4	85	3.0 to 3.6	42DIP/44SOP
				LH53BV8600T	LH-5D80XX	100	50	4	85	3.0 to 3.6	48TSOP(I)*1
8M	×8/×16		M	LH53BV8P00D/N	LH-5D8PXX	120	50	4	85	3.0 to 3.6	42DIP/44SOP
				LH53BV8P00T	LH-5D8BXX	120	50	4	85	3.0 to 3.6	48TSOP(I)*1
				☆ LH53B8V00D/N	LH-MB8VXX	85	45	4	120	4.5 to 5.5	42DIP/44SOP
				☆ LH53B8600D/N	LH-MB86XX	100	50	4	100	4.5 to 5.5	42DIP/44SOP
				☆ LH53C8600D/N	LH-MC86XX	100	65	8	100	4.5 to 5.5	42DIP/44SOP
				☆ LH53B8P00BD/BN	LH-MB8PXX	120	50	4	100	4.5 to 5.5	42DIP/44SOP
				LH53CV16P00D/N/T	LH-5E7PXX	120	50	8	85	3.0 to 3.6	42DIP/44SOP/48TSOP(I)*1
16M	×8/×16		M	LH53BV16900N/M/T	LH-MD79XX	100	30	4 double	120	3.0 to 3.6	70SSOP/100LQFP/70TSOP(II)*1
	×16/×32			☆ LH53B16600AD/AN	LH-MC76XX	100	50	8	150	4.5 to 5.5	42DIP/44SOP
	×8/×16			☆ LH53C16H00AD/AN	LH-MC7HXX	120	50	8	150	4.5 to 5.5	42DIP/44SOP
				☆ LH53B16P00BD/BN	LH-MB7PXX	120	50	4	120	4.5 to 5.5	42DIP/44SOP
				☆ LH53C16P00BD/BN	LH-MC7PXX	120	60	8	150	4.5 to 5.5	42DIP/44SOP
	×16/×32			LH53B16R00N	LH-5B7RXX	120	50	4 double	180	4.5 to 5.5	70SSOP
	32M			×8/×16		M	LH53CV32800N	LH-ME58XX	100	30	8
LH53CV32800T		LH-ME53XX	100				30	8	120	3.0 to 3.6	48TSOP(I)*1
LH53CV32P00N		LH-ME5GXX	120				50	8	100	3.0 to 3.6	44SOP
LH53CV32P00T		LH-ME5BXX	120				50	8	100	3.0 to 3.6	48TSOP(I)*1
☆ LH53BV32000N/T		LH-MD50XX	80				25	4 double	120	3.15 to 3.45	70SSOP/70TSOP(II)*1
LH53BV32900AN/AT		LH-MD5UXX	100				30	4 double	120	3.0 to 3.6	70SSOP/70TSOP(II)*1
LH53BV32700N		LH-MD57XX	100				50	4 double	100	3.0 to 3.6	70SSOP
64M	×16/×32		M	LH53BV32R00N/M	LH-MD5RXX	120	50	4 double	100	3.0 to 3.6	70SSOP/100LQFP
				LH53C326000N	LH-MC56XX	100	50	8	170	4.5 to 5.5	44SOP
				★ LH53BV64900N/T	LH-MD09XX	100	30	4 double	120	3.0 to 3.6	70SSOP/70TSOP(II)*1
128M	×16/×32		M	★ LH53BV64R00N/T	LH-MD0RXX	120	50	4 double	100	3.0 to 3.6	70SSOP/70TSOP(II)*1
				★ LH53BV128900T	LH-MD19XX	100	30	4 double	120	3.0 to 3.6	70TSOP(II)*1
				★ LH53BV128R00	LH-MD1RXX	120	50	4 double	100	3.0 to 3.6	70TSOP(II)*1

※ M: Mask ROM specific pinout \*1 Normal bend

### <Mask ROM>

Capacity	Bit configuration	Pinout**	Model No.	User's No.	Access time (ns) MAX. Cycle time (ns) MIN.	Current consumption (mA) MAX.	Supply voltage (V)	Package
256k	×8	J	LH53259D/N/T	LH-5359XX	150	25	4.5 to 5.5	28DIP/28SOP/28TSOP(I)*1
512k	×8	J	LH53517D/N/T/TR/U	LH-5317XX	150	30	4.5 to 5.5	28DIP/28SOP/28TSOP(I)*1/28TSOP(II)*2/32QFJ
1M	×8	M	LH531000BN-S	LH-531MXX	500	18	2.6 to 3.6	28SOP
		M	LH531000BD/BN	LH-531GXX	150	25	4.5 to 5.5	28DIP/28SOP
		J	LH53V1R00N/T	LH-5V1RXX	120	30	2.7 to 3.6	32SOP/32TSOP(I)*1
		J	LH531V00D/N/T/U	LH-531VXX	100	50	4.5 to 5.5	32DIP/32SOP/32TSOP(I)*1/32QFJ
		J	LH530800AD/AN/AU	LH-531HXX	150	35	4.5 to 5.5	32DIP/32SOP/32QFJ
		J	LH531024D/N/U	LH-531CXX	100	75	4.5 to 5.5	40DIP/40SOP/44QFJ
2M	×8	J	☆ LH53V2R00AN/AT	LH-5V2HXX	120	35	2.7 to 3.6	32SOP/32TSOP(I)*1
		J	☆ LH53V2Y00N/E	LH-5V2YXX	450	25	1.8 to 3.6	32SOP/32TSOP(I)*1, *3
		J	LH532100BD/BN/BT/BS/BSR/BU-1	LH-532KXX	120	50	4.5 to 5.5	32DIP/32SOP/32TSOP(II)*1/32TSOP(III)*2/32QFJ
	×16	J	LH532100BD/BN/BT/BS/BSR/BU	LH-532KXX	150	50	4.5 to 5.5	32DIP/32SOP/32TSOP(II)*1/32TSOP(III)*2/32QFJ
		J	LH532048D/N/U	LH-532CXX	100	75	4.5 to 5.5	40DIP/40SOP/44QFJ
		M	☆ LH53V2P00AN/AT	LH-5V2GXX	120	35	2.7 to 3.6	40SOP/48TSOP(I)*1
		M	LH532600D/N/T/TR	LH-5326XX	100	75	4.5 to 5.5	40DIP/40SOP/48TSOP(I)*1/48TSOP(II)*2
×8/×16	M	LH532000BD/BN/BT/BTR-1	LH-532GXX	120	50	4.5 to 5.5	40DIP/40SOP/48TSOP(I)*1/48TSOP(II)*2	
	M	LH532000BD/BN/BT/BTR	LH-532GXX	150	50	4.5 to 5.5	40DIP/40SOP/48TSOP(I)*1/48TSOP(II)*2	

※ J: JEDEC standard EPROM pinout M: Mask ROM specific pinout QFJ = PLCC \*1 Normal bend \*2 Reverse bend \*3 Nominal dimensions: 8 x 13.4 mm

Memories