# **DMC9610M**

### Silicon NPN epitaxial planar type

For digital circuits
DMC5610M in SSMini5 type package

#### ■ Features

- $\bullet$  Low collector-emitter saturation voltage  $V_{\text{CE(sat)}}$
- Contributes to miniaturization of sets, reduction of component count.
- Eco-friendly Halogen-free package

#### ■ Basic Part Number

Dual DRC2123J (Common emitter)

#### ■ Packaging

Embossed type (Thermo-compression sealing): 8000 pcs / reel (standard)

#### ■ Absolute Maximum Ratings $T_a = 25$ °C

| Parameter                             | Symbol           | Rating      | Unit |  |
|---------------------------------------|------------------|-------------|------|--|
| Collector-base voltage (Emitter open) | V <sub>CBO</sub> | 50          | V    |  |
| Collector-emitter voltage (Base open) | V <sub>CEO</sub> | 50          | V    |  |
| Collector current                     | $I_{C}$          | 100         | mA   |  |
| Total power dissipation               | P <sub>T</sub>   | 125         | mW   |  |
| Junction temperature                  | $T_j$            | 150         | °C   |  |
| Storage temperature                   | T <sub>stg</sub> | -55 to +150 | °C   |  |

#### ■ Package

Code

SSMini5-F4-B

• Pin Name

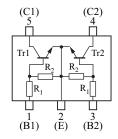
1: Base (Tr1) 4: Collector (Tr2)

2: Emitter (Common) 5: Collector (Tr1)

3: Base (Tr2)

#### ■ Marking Symbol: S2

#### ■ Internal Connection



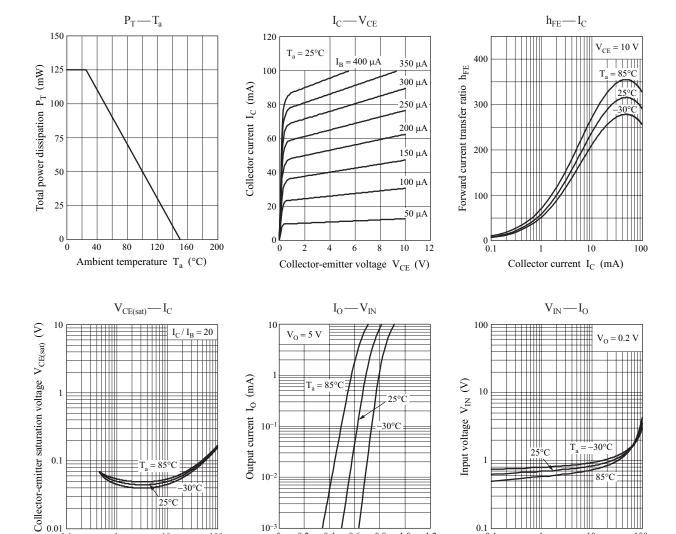
| Resistance | $R_1$          | 2.2 | 1-0 |  |
|------------|----------------|-----|-----|--|
| value      | R <sub>2</sub> | 47  | K22 |  |

#### ■ Electrical Characteristics $T_a = 25$ °C±3°C

| Parameter                                    | Symbol               | Conditions                                      | Min   | Тур   | Max   | Unit |
|--|----------------------|---|-------|-------|-------|------|
| Collector-base voltage (Emitter open)        | V <sub>CBO</sub>     | $I_C = 10 \mu A, I_E = 0$                       | 50    |       |       | V    |
| Collector-emitter voltage (Base open)        | V <sub>CEO</sub>     | $I_{\rm C} = 2 \text{ mA}, I_{\rm B} = 0$       | 50    |       |       | V    |
| Collector-base cutoff current (Emitter open) | $I_{CBO}$            | $V_{CB} = 50 \text{ V}, I_{E} = 0$              |       |       | 0.1   | μΑ   |
| Collector-emitter cutoff current (Base open) | I <sub>CEO</sub>     | $V_{CE} = 50 \text{ V}, I_{B} = 0$              |       |       | 0.5   | μΑ   |
| Emitter-base cutoff current (Collector open) | $I_{EBO}$            | $V_{EB} = 6 \text{ V}, I_C = 0$                 |       |       | 0.2   | mA   |
| Forward current transfer ratio               | $h_{\mathrm{FE}}$    | $V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$   | 80    |       |       | _    |
| Collector-emitter saturation voltage         | V <sub>CE(sat)</sub> | $I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$     |       |       | 0.25  | V    |
| Input voltage (ON)                           | V <sub>I(on)</sub>   | $V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$  | 1.2   |       |       | V    |
| Input voltage (OFF)                          | V <sub>I(off)</sub>  | $V_{CE} = 5 \text{ V}, I_{C} = 100 \mu\text{A}$ |       |       | 0.4   | V    |
| Input resistance                             | $R_1$                |   | -30%  | 2.2   | +30%  | kΩ   |
| Resistance ratio                             | $R_1/R_2$            |   | 0.037 | 0.047 | 0.057 | _    |

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

**Panasonic DMC9610M** 



0.1

10

Output current I<sub>O</sub> (mA)

100

 $10^{-2}$ 

10-3 - 0

0.2 0.4 0.6 0.8

Input voltage V<sub>IN</sub> (V)

Ver. CED 2

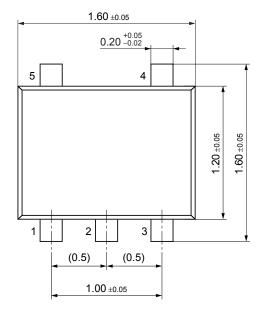
10

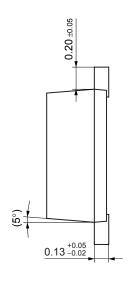
Collector current  $I_C$  (mA)

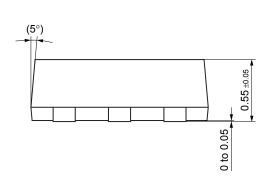
100

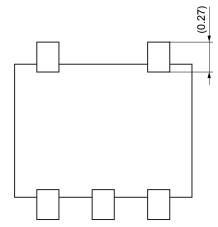
## SSMini5-F4-B

Unit: mm









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