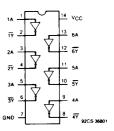
CD54HC04/3A CD54HCT04/3A

Hex Inverter

The RCA-CD54HC04 and CD54HCT04 hex inverters utilize silicon-gate CMOS technology to achieve operating speeds similar to LSTTL gates with the low power consumption of standard CMOS integrated circuits. All devices have the ability to drive 10 LSTTL loads. The 54HCT logic family is functionally as well as pin compatible with the standard 54LS logic family.



Package Specifications

See Section 11, Fig. 10

FUNCTIONAL DIAGRAM

Static Electrical Characteristics (Limits with black dots (•) are tested 100%)

		TEST CONDITIONS						_		
					V _{IN}					
			HC	/HCT		НС	HCT	LIMITS		UNITS
CHARACTERISTICS		V _{DD}	V _o	lo	V _{cc} or GND	V _{IL}	V _{IL} or V _{IH}	MIN.	MAX.	
Quiescent	25°C	6	_	_	6, 0				2•	
Device Current	-55°C +125°C	6	_	_	6, 0	_	_	_	40•	μΑ

The complete static electrical test specification consists of the above by-type static tests combined with the standard static tests in the beginning of this section.

HCT INPUT LOADING TABLE

INPUT	UNIT LOAD*					
All	1.2					

*Unit load is ΔI_{CC} limit specified in Static Characteristics Chart, e.g., 360 μA max. @ 25° C.

Switching Speed (Limits with black dots (•) are tested 100%.) SWITCHING CHARACTERISTICS (C_L = 50 pF, Input t, t_i = 6 ns)

	SYMBOL	TEST	LIMITS								
CHARACTERISTIC		CONDITIONS V _{CC} V	25° C				-55°C to +125°C				
			нс		нст		54HC		54HCT		UNITS
			Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	
Propagation Delay Input to Output	t _{PLH}	2 4.5 6	=	85 17• 14		19• —	 - -	130 26• 22	_ _ _	29• —	ns
Transition Times	t _{TLH}	2 4.5 6	_ 	75 15 13	_	_ 15 _	_	110 22 19		_ 22 _	lis
Input Capacitance	C ₁		_	10		10	_	10		10	pF

Burn-In Test-Circuit Connections (Use Static II for /3A burn-in and Dynamic for Life Test.)

		STATIC BURN-I	N I	STATIC BURN-IN II				
Static	OPEN	GROUND	V _{cc} (6V)	OPEN	GROUND	V _{cc} (6V)		
CD54HC/HCT04	2,4,6,8,10,	1,3,5,7,9,11,13	14	2,4,6,8,10,	7	1,3,5,9,11,13,14		
	12			12				
_					OSCILLATOR			
Dynamic	OPEN	GROUND	1/2 V _{cc} (3V)	V _{cc} (6V)	50 kHz	25 kHz		
CD54HC/HCT04	_	7	2,4,6,8,10,12	14	1,3,5,7,9,11,13	<u> </u>		

NOTE: Each pin except Vcc and Gnd will have a resistor of 2k-47k ohms.