

DOUT3 II 1

DOUT2 II 3

ROUT2 15

DIN2

DIN1 7

RIN1

GND

V_{CC}

C1+

V+

C1-

ROUT1

RIN2 Π 4

П2

6

Π8

9

10

11

12

13

14

DOUT1

DB. DW. OR PW PACKAGE

(TOP VIEW)

28 DOUT4

ROUT3

27 RIN3

25 SHDN

24 🛛 EN

23 🛛 RIN4

21 DIN4

20 DIN3

18 RIN5

17 V-

15

16 C2-

C2+

22 ROUT4

19 ROUT5

26

FEATURES

- ESD Protection for RS-232 Bus Pins
 ±15-kV Human-Body Model (HBM)
- Meets or Exceeds the Requirements of TIA/EIA-232-F and ITU v.28 Standards
- Operates at 5-V V_{CC} Supply
- Four Drivers and Five Receivers
- Operates up to 120 kbit/s
- Low Supply Current in Shutdown Mode . . . 15 μA Typ
- External Capacitors . . . 4 × 0.1 F
- Designed to Be Interchangeable With Industry Standard '213 Devices
- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II

APPLICATIONS

- Battery-Powered Systems
- PDAs
- Notebooks
- Laptops
- Palmtop PCs
- Hand-Held Equipment

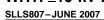
DESCRIPTION/ ORDER INFORMATION

The TRS213 device consists of four line drivers, five line receivers, and a dual charge-pump circuit with \pm 15-kV ESD protection pin to pin (serial-port connection pins, including GND). The device meets the requirements of TIA/EIA-232-F and provides the electrical interface between an asynchronous communication controller and the serial-port connector. The charge pump and four small external capacitors allow operation from a single 5-V supply. The devices operate at data signaling rates up to 120 kbit/s and a maximum of 30-V/µs driver output slew rate.

The TRS213 has an active-low shutdown (SHDN) and an active-high enable control (EN). In shutdown mode, the charge pumps are turned off, V+ is pulled down to V_{CC} , V– is pulled to GND, and the transmitter outputs are disabled. This reduces supply current typically to 1 μ A. Two receivers of the TRS213 are active during shutdown.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.





ORDERING INFORMATION

T _A	PA	CKAGE ⁽¹⁾⁽²⁾	ORDERABLE PART NUMBER	TOP-SIDE MARKING	
	SOIC - DW	Tube of 20	TRS213CDW	TREAMO	
	50IC - DW	Reel of 1000	TRS213CDWR	TRS213C	
°C to 70°C	SSOP – DB	Tube of 50 TRS213CDB		TRS213C	
	550P - DB	Reel of 2000	TRS213CDBR	1852130	
	TSSOP – PW	Tape and reel	TRS213CPWR	TRS213C	
	SOIC - DW	Tube of 20	TRS213IDW	- TRS213I	
	30IC - DW	Reel of 1000	TRS213IDWR	1832131	
–40°C to 85°C	SSOP – DB	Tube of 50	TRS213IDB	TRS213I	
	330P - DB	Reel of 2000	TRS213IDBR	1852131	
	TSSOP – PW	Tape and reel	TRS213IPWR	TRS213I	

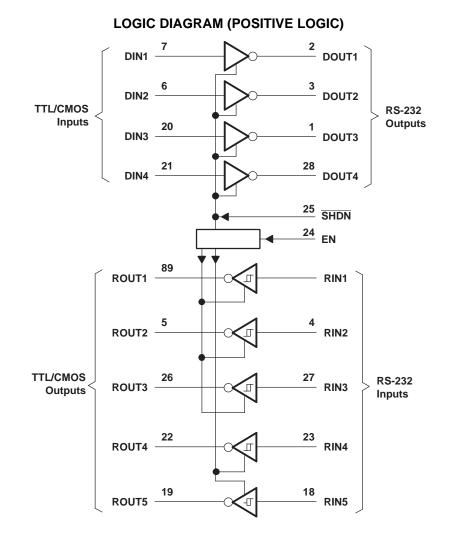
(1) Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

(2) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI website at www.ti.com.

INPU	JTS	DRIVER	REC	EIVER	DEVICE STATUS
SHDN	EN	D1–D4	R1–R3	R4–R5	DEVICE STATUS
L	L	Z	Z	Z	Shutdown
L	Н	Z	Z	Active ⁽¹⁾	Shutdown
Н	L	All active	Z	Z	Normal operation
Н	Н	All active	Active	Active	Normal operation

FUNCTION TABLE

(1) See the $V_{\text{IT+}}$ and $V_{\text{IT-}}$ change in the Electrical Characteristics table.



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Absolute Maximum Ratings⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
V _{CC}	Supply voltage range		-0.3	6	V
V+	Positive charge-pump voltage range ⁽²⁾		V _{CC} – 0.3	14	V
V–	Negative charge-pump voltage range ⁽²⁾	charge-pump voltage range ⁽²⁾		-14	V
N/		Drivers	-0.3	V+ + 0.3	
VI	Input voltage range	Receivers		±30	V
Vo		Drivers	V0.3	±30 V+ + 0.3 V _{CC} + 0.3	
	Output voltage range	Receivers		V _{CC} + 0.3	V
DOUT	Short-circuit duration			Continuous	
		DB package		62	
θ_{JA}	Package thermal impedance ⁽³⁾⁽⁴⁾	DW package		46	C°/W
		PW package			
TJ	Operating virtual junction temperature	· · · ·		150	C°
T _{stg}	Storage temperature range		-65	150	C°

(1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) All voltages are with respect to network GND.

(3) Maximum power dissipation is a function of $T_J(max), \theta_{JA}$, and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) - T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability. (4) The package thermal impedance is calculated in accordance with JESD 51-7.

Recommended Operating Conditions⁽¹⁾

See Figure 4

			MIN	NOM	MAX	UNIT
	Supply voltage		4.5	5	5.5	V
V	Driver high-level input voltage	DIN	2			V
VIH	Control high-level input voltage	EN, SHDN	2.4			v
VIL	Driver and control low-level input voltage	DIN, EN, SHDN			0.8	V
v	Driver and control input voltage	DIN, EN, SHDN	0		5.5	V
VI	Receiver input voltage	RIN	-30		30	v
т	Operating free air temperature	TRS213C	0		70	°C
I A	Operating free-air temperature TRS213I		-40		85	

(1) Test conditions are C1–C4 = 0.1 μF at V_{CC} = 5 V \pm 0.5 V.

Electrical Characteristics⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

PARAMETER		Г	MIN	TYP ⁽²⁾	MAX	UNIT	
I _{CC}	Supply current	No load,	See Figure 6		14	20	mA
I _{SHDN}	Shutdown supply current	$T_A = 25^{\circ}C$,	See Figure 1		15	50	μA

(1) Test conditions are C1–C4 = 0.1 μF at V_{CC} = 5 V \pm 0.5 V.

(2) All typical values are at $V_{CC} = 5$ V, and $T_A = 25^{\circ}C$.

DRIVER SECTION

Electrical Characteristics⁽¹⁾

over operating free-air temperature range (unless otherwise noted) (see Figure 4)

	PARAMETER	TEST CONDI	TIONS	MIN	TYP ⁽²⁾	MAX	UNIT
V _{OH}	High-level output voltage	DOUT at $R_L = 3 k\Omega$ to GNE)	5	9		V
V _{OL}	Low-level output voltage	DOUT at $R_L = 3 k\Omega$ to GNI	C	-5	-9		V
I _{IH}	Control high-level input current	EN, <u>SHDN</u> = 5 V			3	10	μA
	Driver low-level input current	DIN = 0 V			-15	-200	
Ι _{ΙL}	Control low-level input current	EN, $\overline{\text{SHDN}} = 0 \text{ V}$			-3	-10	μA
$I_{OS}^{(3)}$	Short-circuit output current	$V_{CC} = 5.5 V,$	$V_0 = 0 V$		±10	±60	mA
r _o	Output resistance	V_{CC} , V+, and V- = 0 V,	$V_0 = \pm 2 V$	300			Ω

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 5 V ± 0.5 V

(2) All typical values are at $V_{CC} = 5$ V, and $T_A = 25^{\circ}$ C. (3) Short-circuit durations should be controlled to prevent exceeding the device absolute power dissipation ratings, and not more than one output should be shorted at a time.

Switching Characteristics⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

	PARAMETER	TEST CC	ONDITIONS	MIN	TYP ⁽²⁾	MAX	UNIT
	Maximum data rate	$C_L = 50 \text{ pF}$ to 1000 pF, One DOUT switching,	$R_L = 3 k\Omega$ to 7 k Ω , See Figure 3	120			kbit/s
t _{PLH(D)}	Propagation delay time, low- to high-level output	$C_L = 2500 \text{ pF},$ All drivers loaded,	$R_L = 3 k\Omega$, See Figure 3		2		μs
t _{PHL(D)}	Propagation delay time, high- to low-level output	C _L = 2500 pF, All drivers loaded,	$R_L = 3 k\Omega$, See Figure 3		2		μs
t _{sk(p)}	Pulse skew ⁽³⁾	$C_L = 150 \text{ pF to } 2500 \text{ pF},$ See Figure 3	$R_L = 3 \ k\Omega \ to \ 7 \ k\Omega$,		300		ns
SR(tr)	Slew rate, transition region (see Figure 2)	$C_L = 50 \text{ pF to } 1000 \text{ pF},$ $V_{CC} = 5 \text{ V}$	$R_L = 3 \ k\Omega$ to 7 k Ω ,	3	6	30	V/µs

(1) Test conditions are C1–C4 = 0.1 μ F at V_{CC} = 5 V ± 0.5 V. (2) All typical values are at V_{CC} = 5 V, and T_A = 25°C.

(3) Pulse skew is defined as $(t_{PLH} - t_{PHL})$ of each channel of the same device.

ESD Protection

over operating free-air temperature range (unless otherwise noted)

PIN	TEST CONDITIONS	TYP	UNIT
DOUT	Human-Body Model	±15	kV

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RECEIVER SECTION

Electrical Characteristics⁽¹⁾

over operating free-air temperature range (unless otherwise noted) (see Figure 6)

	PARAMETER	TEST	CONDITIONS	MIN	TYP ⁽²⁾	MAX	UNIT
V _{OH}	High-level output voltage	$I_{OH} = -1 \text{ mA}$			$V_{CC} - 0.4$		V
V _{OL}	Low-level output voltage	I _{OH} = 1.6 mA				0.4	V
V	Positive-going	V _{CC} = 5 V, T _A = 25°C	Active mode		1.7	2.4	V
V _{IT+}	input threshold voltage	$v_{\rm CC} = 5 v, T_{\rm A} = 25 C$	Shutdown mode (R4-R5)		1.5	2.4	v
N	Negative-going		Active mode	0.8	1.2		N/
V _{IT-}	input threshold voltage	$V_{CC} = 5 V, T_A = 25^{\circ}C$	Shutdown mode (R4-R5)	0.6	1.5		v
V _{hys} ⁽³⁾	Input hysteresis (V _{IT+} , V _{IT-})	V _{CC} = 5 V			0.5	1	V
r _l	Input resistance	$V_{CC} = 5 \text{ V}, \text{ T}_{A} = 25^{\circ}\text{C}$		3	5	7	kΩ
	Output leakage current	$EN = 0 V, 0 \le ROUT \le V_{CC}, R1-R3$			±0.05	±10	μA

Switching Characteristics⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

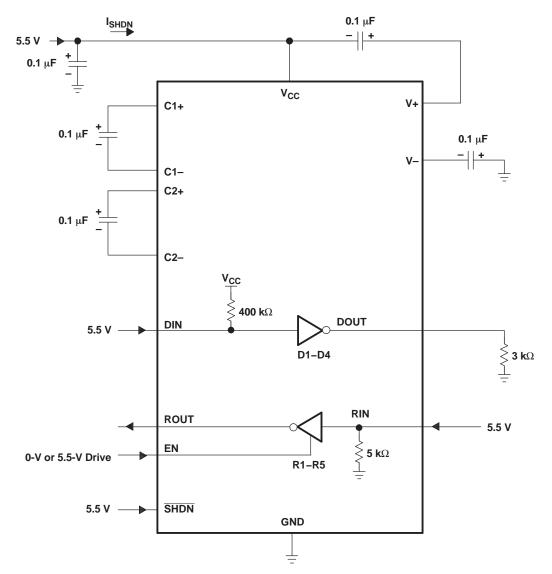
PARAMETER		TEST CONDITIONS				TYP ⁽²⁾	MAX	UNIT
+	Propagation delay time,	$C_{l} = 150 \text{ pF},$	Soo Figuro 4	$\overline{\text{SHDN}} = V_{CC}$		0.5	10	
t _{PLH(R)}	low- to high-level output	$C_{L} = 150 \text{ pr},$	See Figure 4	<u>SHDN</u> = 0 V, R4–R5		4	40	μs
t _{PHL(R)}	Propagation delay time, high- to low-level output	C _L = 150 pF,	See Figure 4			0.5	10	μs
t _{en}	Output enable time	C _L = 150 pF,	See Figure 5			600		ns
t _{dis}	Output disable time	C _L = 150 pF,	See Figure 5			200		ns

ESD Protection

over operating free-air temperature range (unless otherwise noted)

PIN	TEST CONDITIONS	TYP	UNIT
RIN	Human-Body Model	±15	kV

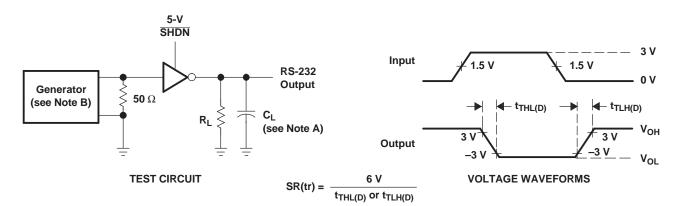
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PARAMETER MEASUREMENT INFORMATION

Figure 1. Shutdown Current Test Circuit

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NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: $Z_0 = 50 \Omega$, 50% duty cycle, $t_f \le 10$ ns. $t_f \le 10$ ns.

Figure 2. Driver Slew Rate

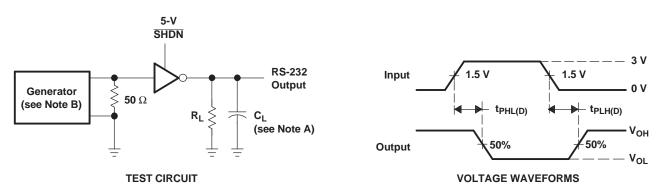
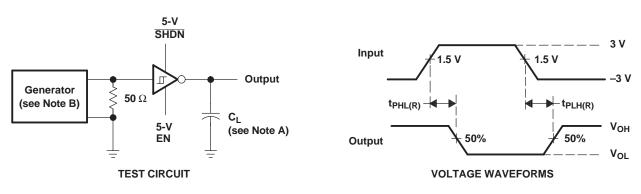




Figure 3. Driver Pulse Skew and Propagation Delay Times

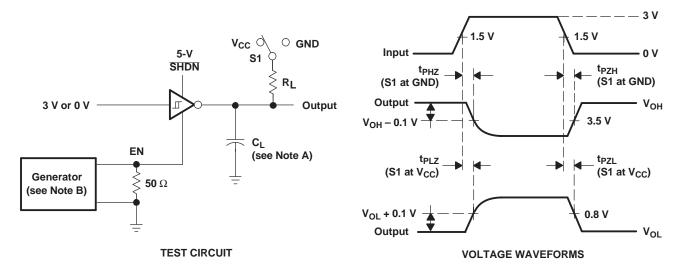


NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: $Z_0 = 50 \Omega$, 50% duty cycle, $t_r \le 10$ ns, $t_f \le 10$ ns.

Figure 4. Receiver Propagation Delay Times

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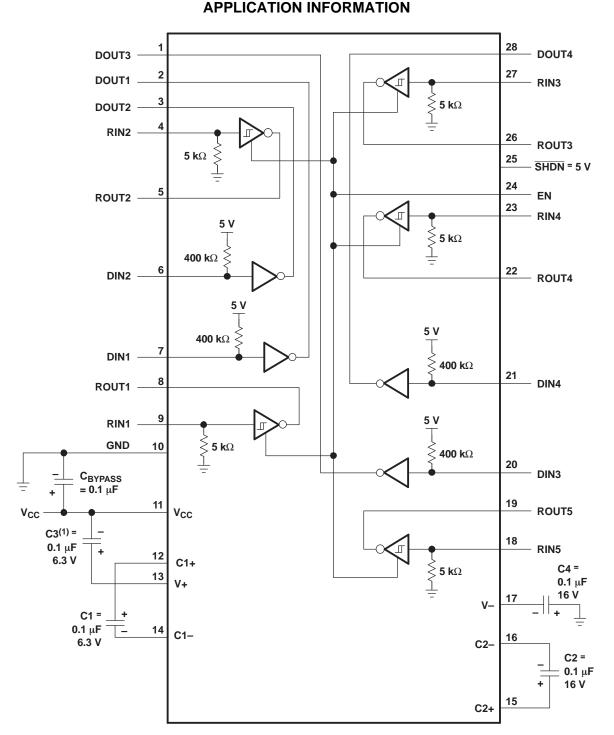


- NOTES: A. C_L includes probe and jig capacitance.
 - B. The pulse generator has the following characteristics: $Z_0 = 50 \Omega$, 50% duty cycle, $t_r \le 10$ ns. $t_f \le 10$ ns.
 - C. t_{PLZ} and t_{PHZ} are the same as t_{dis} .
 - D. t_{PZL} and t_{PZH} are the same as t_{en} .

Figure 5. Receiver Enable and Disable Times

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(1) C3 can be connected to $V_{\mbox{CC}}$ or GND.

NOTES: A. Resistor values shown are nominal.

B. Nonpolarized ceramic capacitors are acceptable. If polarized tantalum or electrolytic capacitors are used, they should be connected as shown.

Figure 6. Typical Operating Circuit and Capacitor Values



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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
TRS213CDB	ACTIVE	SSOP	DB	28		TBD	Call TI	Call TI	
TRS213CDBG4	ACTIVE	SSOP	DB	28		TBD	Call TI	Call TI	
TRS213CDBR	ACTIVE	SSOP	DB	28	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TRS213CDBRG4	ACTIVE	SSOP	DB	28	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TRS213CDW	ACTIVE	SOIC	DW	28		TBD	Call TI	Call TI	
TRS213CDWG4	ACTIVE	SOIC	DW	28		TBD	Call TI	Call TI	
TRS213CDWR	ACTIVE	SOIC	DW	28	1000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TRS213CDWRG4	ACTIVE	SOIC	DW	28	1000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TRS213IDB	ACTIVE	SSOP	DB	28	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TRS213IDBG4	ACTIVE	SSOP	DB	28	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TRS213IDBR	ACTIVE	SSOP	DB	28	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TRS213IDBRG4	ACTIVE	SSOP	DB	28	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TRS213IDW	ACTIVE	SOIC	DW	28	20	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TRS213IDWG4	ACTIVE	SOIC	DW	28	20	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TRS213IDWR	ACTIVE	SOIC	DW	28	1000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	
TRS213IDWRG4	ACTIVE	SOIC	DW	28	1000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	

⁽¹⁾ The marketing status values are defined as follows: **ACTIVE:** Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.



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OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

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Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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PACKAGE MATERIALS INFORMATION

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TAPE AND REEL INFORMATION

REEL DIMENSIONS

Texas Instruments





TAPE AND REEL INFORMATION

TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

All dimensions are nominal												
Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TRS213CDBR	SSOP	DB	28	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1
TRS213CDWR	SOIC	DW	28	1000	330.0	32.4	11.35	18.67	3.1	16.0	32.0	Q1
TRS213IDBR	SSOP	DB	28	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1
TRS213IDWR	SOIC	DW	28	1000	330.0	32.4	11.35	18.67	3.1	16.0	32.0	Q1

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PACKAGE MATERIALS INFORMATION

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*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TRS213CDBR	SSOP	DB	28	2000	367.0	367.0	38.0
TRS213CDWR	SOIC	DW	28	1000	367.0	367.0	55.0
TRS213IDBR	SSOP	DB	28	2000	367.0	367.0	38.0
TRS213IDWR	SOIC	DW	28	1000	367.0	367.0	55.0

DW (R-PDSO-G28)

PLASTIC SMALL OUTLINE



NOTES:

A. All linear dimensions are in inches (millimeters). Dimensioning and tolerancing per ASME Y14.5M-1994.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).

D. Falls within JEDEC MS-013 variation AE.



MECHANICAL DATA

MSSO002E - JANUARY 1995 - REVISED DECEMBER 2001

DB (R-PDSO-G**)

PLASTIC SMALL-OUTLINE

28 PINS SHOWN



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
- D. Falls within JEDEC MO-150



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