



DATA SHEET

PG200~PG2010

GLASS PASSIVATED JUNCTION PLASTIC RECTIFIER

VOLTAGE 50 to 1000 Volts **CURRENT** 2.0 Amperes

DO-15

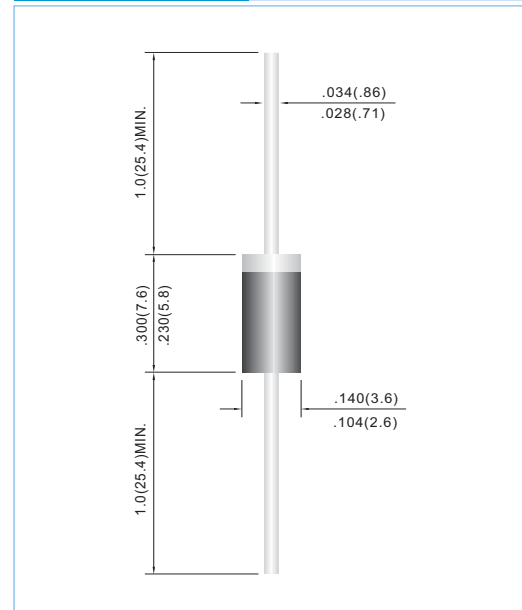
Unit: inch(mm)

FEATURES

- Plastic package has Underwriters Laboratory Flammability Classification 94V-O utilizing Flame Retardant Epoxy Molding Compound.
- Glass passivated junction
- Exceeds environmental standards of MIL-S-19500/228
- Pb free product are available : 99% Sn can meet Rohs environment substance directive request

MECHANICAL DATA

Case: Molded plastic, DO-15
 Terminals: Axial leads, solderable to MIL-STD-202G, Method 208
 Polarity: Color Band denotes cathode end
 Mounting Position: Any
 Weight: 0.015 ounce, 0.4 gram



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified. Resistive or inductive load, 60Hz.

PARAMETER	SYMBOL	PG 200	PG 201	PG 202	PG 204	PG 206	PG 208	PG 2010	UNITS
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Current .375"(9.5mm) lead length at TA=55°C	I _{AV}	2.0							A
Peak Forward Surge Current : 8.3ms single half sine-wave superimposed on rated load (JEDEC method)	I _{FSM}	70							A
Maximum Forward Voltage at 2.0A	V _F	1.1							V
Maximum DC Reverse Current at TA=25°C Rated DC Blocking Voltage TA=100°C	I _R	5.0 50							uA
Typical Junction capacitance (Note 1)	C _J	25							pF
Typical Thermal Resistance (Note 2)	R _{θJA} R _{θJL}	45 28							°C / W
Operating and Storage Temperature Range T _J , T _{STG}	T _J , T _{STG}	-55 TO +150							°C

NOTES: 1. Measured at 1 MHz and applied reverse voltage of 4.0 VDC
 2. Thermal resistance from junction to ambient and from junction to lead length 0.375"(9.5mm) P.C.B. mounted



RATING AND CHARACTERISTIC CURVES

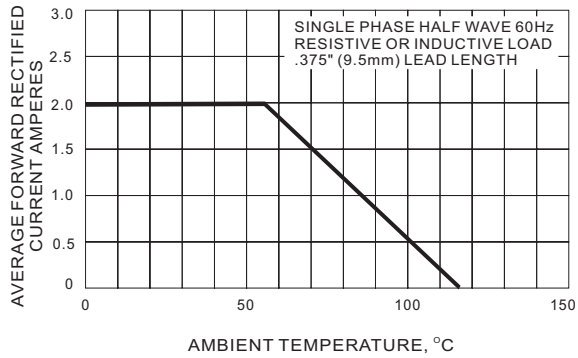


FIG. 1 FORWARD CURRENT DERATING CURVE

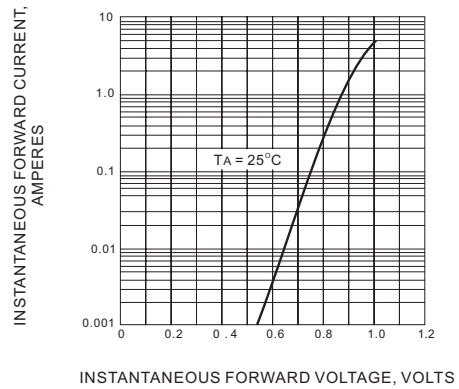


FIG. 2 TYPICAL FORWARD CHARACTERISTICS

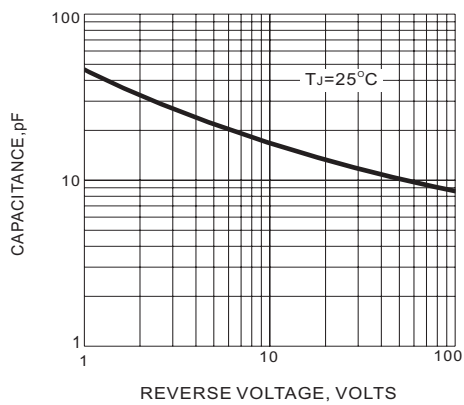


FIG. 3 TYPICAL JUNCTION CAPACITANCE

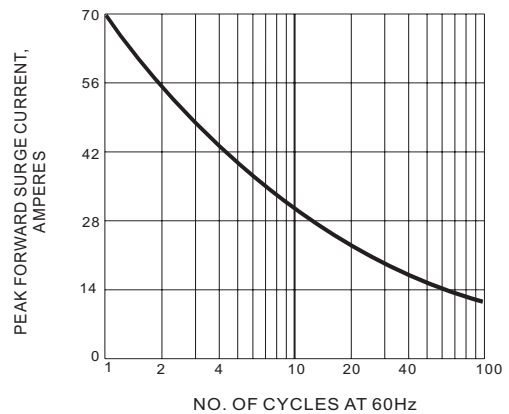


FIG. 4 MAX OVERLADE SURGE CURRENT