

# **DATA SHEET**

# SMP1322 Series: Low Resistance, Plastic Packaged PIN Diodes

# **Applications**

· High-performance wireless switch applications

# **Features**

- Resistance: 0.8  $\Omega$  typical @ 1 mA
- Packages rated MSL1, 260 °C per JEDEC J-STD-020)



Skyworks Green<sup>™</sup> products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green<sup>™</sup>*, document number SQ04-0074.



# Description

The SMP1322 series of plastic packaged, surface mountable PIN diodes is designed for use in high volume switch applications from 10 MHz to more than 10 GHz. The ultra-low resistance of these diodes (1.5  $\Omega$  maximum at 1 mA and 0.5  $\Omega$  typical at 10 mA) makes the SMP1322 series particularly suited to low-loss PIN diode switches in battery operated circuits.

The SMP1322 series is available in a selection of plastic packages and a variety of configurations that include an SC-70, a small footprint SC-79, SOD-323, and a miniature SOD-882.

The SMP1322-016 consists of two diodes in an SOT-143 package configured to enable insertion in a quarter-wave transmit/receive (T/R) switch with no crossover connections.

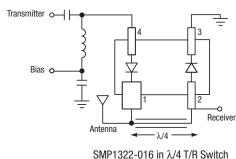
Table 1 describes the various packages and marking of the SMP1322 series.

Table 1. SMP1322 Series Packaging and Marking

Single	Common Anode	Common Cathode	Series Pair	Single	T/R Switch	Single	Single
SOT-23	SOT-23	S0T-23	S0T-23	SOD-323 Green™	S0T-143	SC-79 Green™	SOD-882 Green™
SMP1322-001 Marking: PN1	<b>SMP1322-003</b> Marking: PN9	<b>SMP1322-004</b> Marking: PN3	SMP1322-005 Marking: PN2		SMP1322-016 Marking: PN6		
SMP1322-001LF Green™ Marking: RN1	SMP1322-003LF Green™ Marking: RN9	<b>SMP1322-004LF</b> Green™ Marking: RN3	SMP1322-005LF Green™ Marking: RN2	SMP1322-011LF Marking: RN	SMP1322-016LF Marking: RN6	SMP1322-079LF Marking: Cathode	SMP1322-040LF Marking: T
Ls = 1.5 nH	$L_S = 1.5 \text{ nH}$	$L_{S} = 1.5 \text{ nH}$	Ls = 1.5 nH	$L_{S} = 1.5 \text{ nH}$	$L_{S} = 1.5 \text{ nH}$	$L_{S} = 0.7 \text{ nH}$	Ls = 0.45 nH
			SC-70				
			SMP1322-075LF Green™ Marking: RN2				
			$L_S = 1.4 \text{ nH}$				

Ø

The Pb-free symbol or "LF" in the part number denotes a lead-free, RoHS-compliant package unless otherwise noted as Green<sup>TM</sup>. Tin/lead (Sn/Pb) packaging is not recommended for new designs.



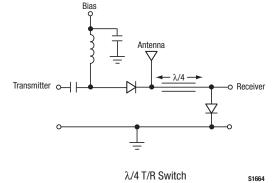


Figure 1. SMP1322-016 Quarter-Wave T/R Switch

# SMP1322-016 T/R Switch

The SMP1322-016 is a low-cost PIN diode unconnected pair specifically designed for low-current drain antenna T/R switches in hand-held wireless systems. In the specifically configured SOT-143 package (see Figure 1), the PIN diodes are oriented to enable connection as a  $\lambda/4$  switch with no external crossover connections.

# **Electrical and Mechanical Specifications**

The absolute maximum ratings of the SMP1322 series are provided in Table 2. Electrical specifications are provided in Table 3. Resistance versus temperature measurements are provided in Table 4.

Typical performance characteristics of the SMP1322 series are illustrated in Figures 2 to 6. Package dimensions are shown in Figures 7 to 17 (odd numbers), and tape and reel dimensions are provided in Figures 8 to 18 (even numbers).

### **Table 2. SMP1322 Series Absolute Maximum Ratings**

Parameter	Symbol	Minimum	Maximum	Units
Reverse voltage	VR		50	V
Power dissipation @ 25 °C lead temperature	PD		250	mW
Storage temperature	Tstg	-65	+150	°C
Operating temperature	TA	-65	+150	°C
Electrostatic discharge:	ESD			
Charged Device Model (CDM), Class 4 Human Body Model (HBM), Class 1B			1000 1000	V V

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

Table 3. SMP1322 Series Electrical Specifications (N	lote 1	I)
(T <sub>A</sub> = +25 °C, Unless Otherwise Noted)		

Parameter	Symbol	Test Condition	Min	Typical	Мах	Units
Reverse current	I <sub>R</sub>	$V_R = 50 V$			10	μA
Capacitance (Note 2)	CT	F = 1 MHz, V = 30 V			1	pF
Resistance	R <sub>S</sub>	F = 100 MHz				
		l = 1 mA l = 10 mA		0.5	1.5	Ω Ω
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA		0.85		V
Carrier lifetime	TI	I <sub>F</sub> = 10 mA		0.4		μs
I region width				8		μm

Note 1: Performance is guaranteed only under the conditions listed in this table.

Note 2: C<sub>T</sub> is 1.15 pF maximum for the SMP1322-016.

**CAUTION**: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

## **Package and Handling Information**

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SMP1322 series is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering.

For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

## Table 4. Resistance vs Temperature @ 500 MHz

lF (mA)	Rs @ -55 °C (Ω)	Rs @ –15 °C (Ω)	Rs @ +25 °C (Ω)	Rs @ +65 °C (Ω)	Rs @ +100 °C (Ω)
0.02	9.5	9.4	9.9	10.5	10.9
0.10	3.0	3.0	3.0	3.3	3.5
0.30	1.5	1.5	1.5	1.6	1.8
0.50	1.1	1.1	1.2	1.2	1.4
1.0	0.922	0.914	0.902	0.963	1.100
10	0.568	0.559	0.533	0.563	0.655
20	0.532	0.520	0.494	0.521	0.610
100	0.483	0.469	0.440	0.464	0.565

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# **Typical Performance Characteristics**

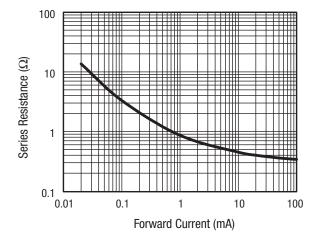


Figure 2. Series Resistance vs Current @ 100 MHz

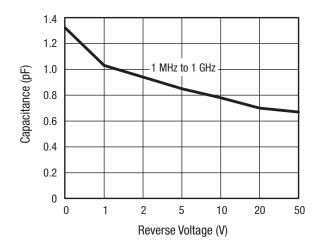


Figure 4. Capacitance vs Reverse Voltage

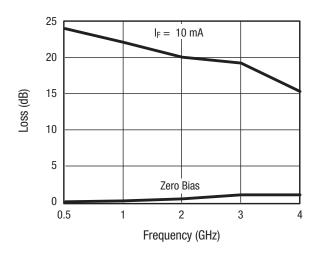
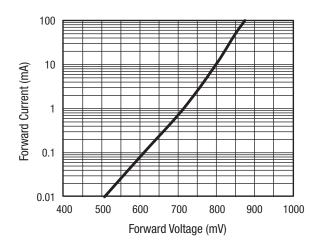
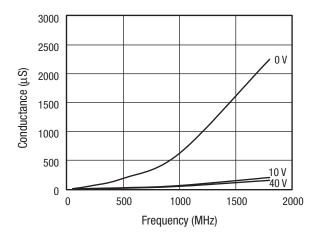


Figure 6. SMP1322-017 Typical SPST Switch Performance



**Figure 3. DC Characteristics** 



**Figure 5. Conductance vs Frequency and Reverse Voltage** 

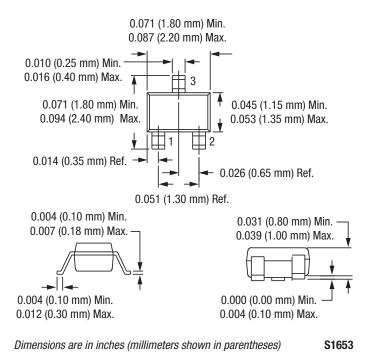
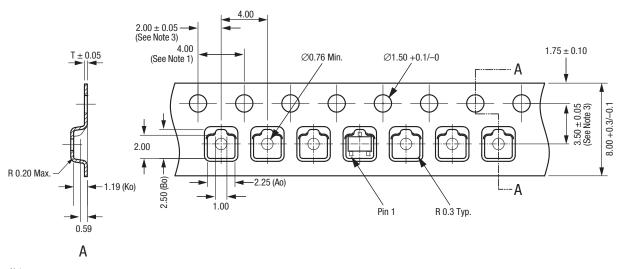


Figure 7. SC-70 Package Dimension Drawing

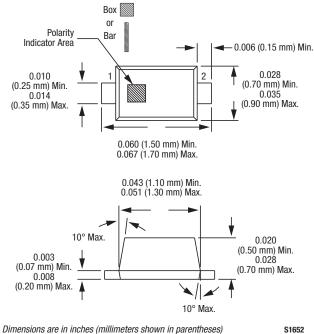


Notes:

- Sprocket hole pitch cumulative tolerance ±0.2.
  Carrier tape: black conductive polystyrene.
- Cocket position relative to sprocket hole measured as true position of pocket, not pocket hole.
  Cover tape material: transparent conductive PSA with 9.2 mm width.
  All measurements are in millimeters.

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## Figure 8. SC-70 Tape and Reel Dimensions



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Figure 9. SC-79 Package Dimension Drawing

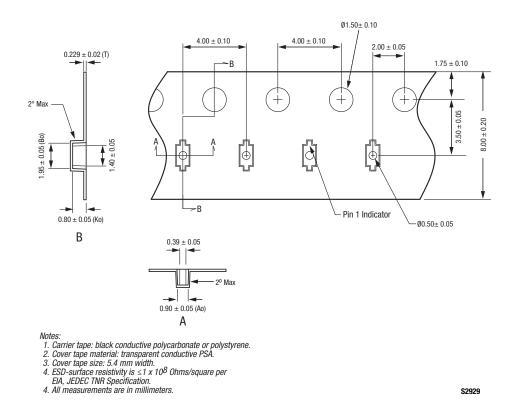
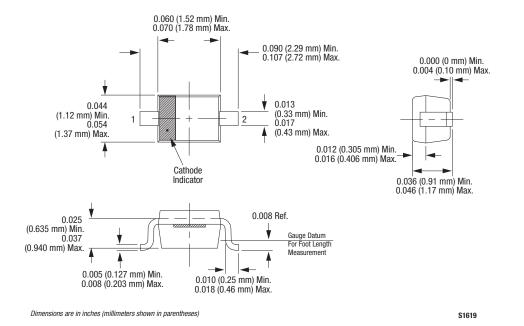
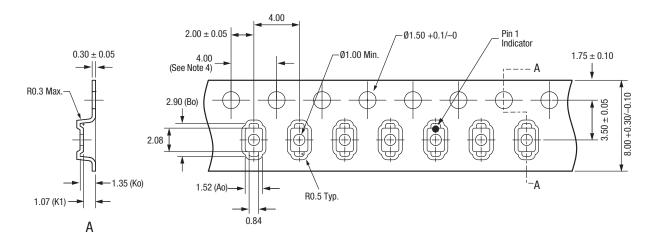


Figure 10. SC-79 Tape and Reel Dimensions







Notes:

1.

1. 2. 3. 4. 5.

Carrier tape: black conductive polystyrene. Cover tape: transparent conductive PSA. Cover tape size: 5.4 mm width. 10 sprocket hole pitch cumulative tolerance: ±0.20 mm. All measurements are in millimeters.

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## Figure 12. SOD-323 Tape and Reel Dimensions

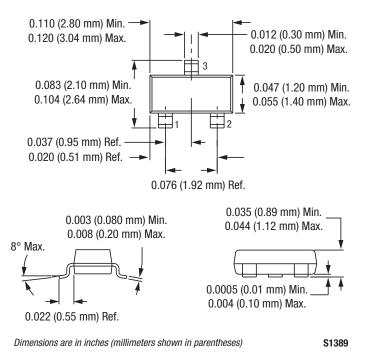
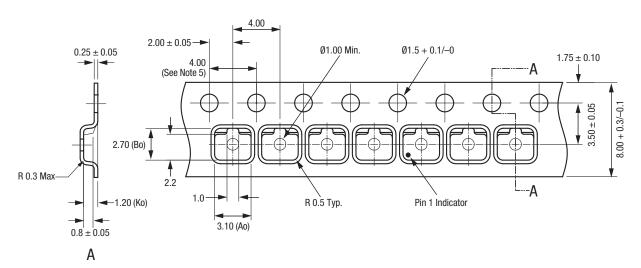


Figure 13. SOT-23 Package Dimension Drawing



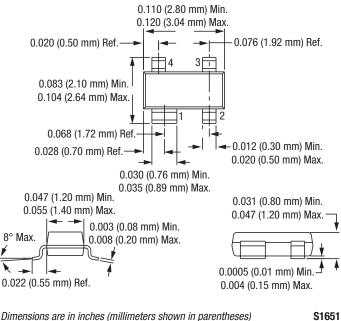
Notes.

otes: 1. Carrier tape: black conductive polycarbonate. 2. Cover tape material: transparent conductive PSA. 3. Cover tape size: 5.40 mm width. 4. Tolerance ±0.10 mm.

For sprocket hole pitch cumulative tolerance: ±0.2 mm.
 All measurements are in millimeters.

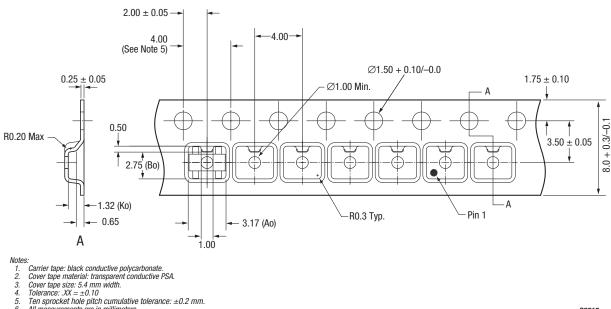
S1684b





Dimensions are in inches (millimeters shown in parentheses)

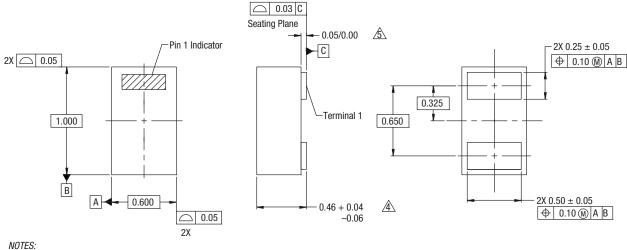
Figure 15. SOT-143 Package Dimension Drawing



3. 4. 5. 6. All measurements are in millimeters

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#### Figure 16. SOT-143 Tape and Reel Dimensions



### 1. All measurements are in millimeters.

- Dimensions and tolerances according to ASME Y14.5M-1994.
  These packages are used principally for discrete devices.
- 4. This dimension includes stand-off height and package body thickness, but does not include attached features, e.g., external heatsink or chip capacitors. An integral heatslug is not considered an attached feature.
- 5. This dimension is primarily terminal plating, but does not include small metal protrusion.

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## Figure 17. SOD-882 Package Dimension Drawing

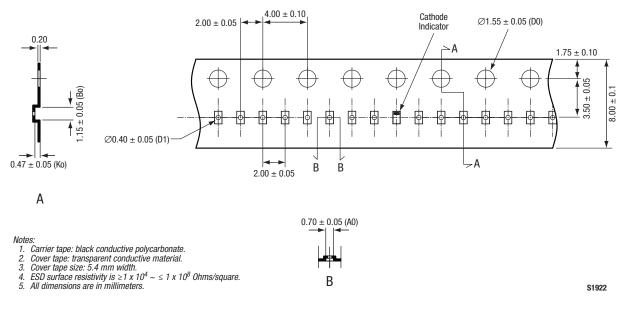


Figure 18. SOD-882 Tape and Reel Dimensions

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