



**EUREKA**

**EK681024A**

# 128K x 8 Bit High-Speed CMOS SRAM

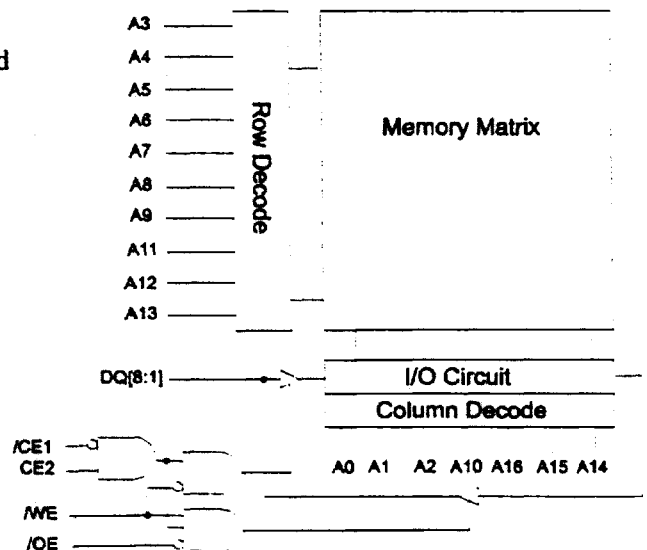
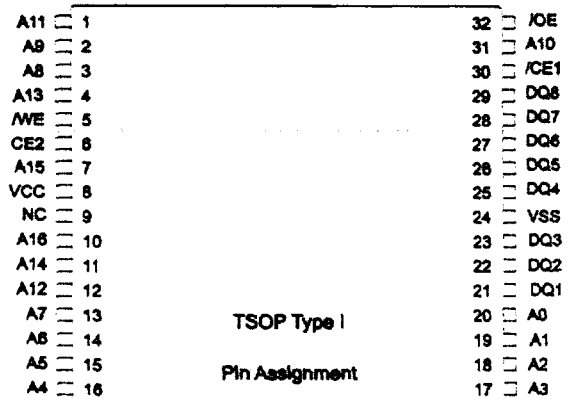
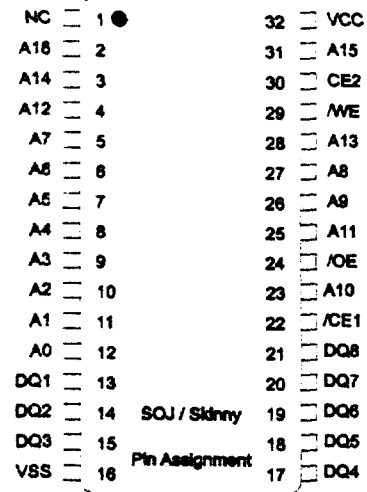
## Features

- Fast Address Access Times  
10ns, 12ns, 15ns
- Fast Output Enable Access Times  
5ns, 6ns, 7ns
- Low Power Consumption  
180mA, 150mA, 130mA
- Single 5V  $\pm 10\%$  Power Supply
- Industry Standard Pin Assignment
- Package Options  
300 mil 32 pin SOJ  
32 pin TSOP Type 1  
300 mil 32 pin PDIP (Skinny)

## Description

The EK681024A from Eureka is a one-megabit density fast static random access memory organized as 131,072 words by 8 bits. It is designed for use in high performance memory applications such as main memory storage and high speed communication buffers. Fabricated using high performance CMOS technology, access times down to 12ns are achieved. Memory expansion by banking is easily accomplished using the chip enable pins /CE1 and CE2.

Symbol	Description
A0-A16	Address Inputs
DQ1-DQ8	Data Inputs/Outputs
/CE1, CE2	Chip Enables
/WE	Write Enable
/OE	Output Enable
Vcc	Power Supply
Vss	Ground





**EK681024A**

## Ordering Information

(Order by Complete Part Number)

**EK 681024A X - Y**

Eureka Prefix

Part Number

10=10ns, 12=12ns, 15=15ns

Package Type

J = 300 mil SOJ

T = TSOP Type I

K = 300 mil PDIP(Skinny)