TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC153AP,TC74HC153AF TC74HC253AP,TC74HC253AF

TC74HC153AP/AF TC74HC253AP/AF Dula 4-Channe Multiplexer

Dual 4-Channel Multiplexer with 3-State Output

The TC74HC153A and TC74HC253A are high speed CMOS DUAL 4-CHANNEL MULTIPLEXERs fabricated with silicon gate C²MOS technology.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

The TC74HC153A has standard outputs, while the TC74HC253A has 3-state outputs.

Input data (1C0 \sim 1C2, 2C0 \sim 2C3) are selected by the two address inputs, A and B.

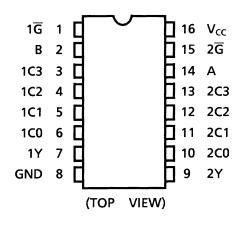
Separate strobe inputs $(1\overline{G}\ ,\ 2\overline{G}\)$ are provided for each of the two four-line sections. They can be used to inhibit the data outputs. The output of the HC153A is set low, and the HC253A output is set to the high impedance state, when the strobe inputs are low.

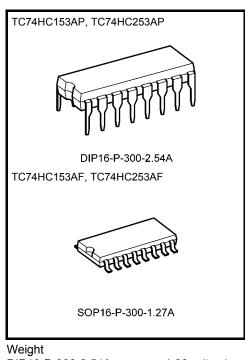
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 12 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \mu A \pmod{at Ta} = 25 \circ C$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min)
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 to 6 V
- Pin and function compatible with 74LS153, 74LS253

Pin Assignment





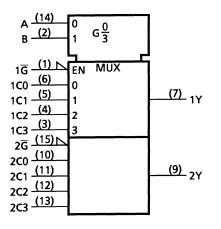
DIP16-P-300-2.54A SOP16-P-300-1.27A

: 1.00 g (typ.) : 0.18 g (typ.)

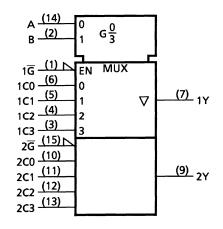
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IEC Logic Symbol

TC74HC153A



TC74HC253A



Truth Table

Select Inputs			Data	Inputs		Strobe	Outputs Y		
В	А	C0	C1	C2	C3	G	HC153A	HC253A	
Х	Х	Х	Х	Х	Х	Н	L	Z	
L	L	L	Х	Х	Х	L	L	L	
L	L	Н	Х	Х	Х	L	Н	Н	
L	Н	Х	L	Х	Х	L	L	L	
L	Н	Х	Н	Х	Х	L	Н	Н	
Н	L	Х	Х	L	Х	L	L	L	
Н	L	Х	Х	Н	Х	L	Н	Н	
Н	Н	Х	Х	Х	L	L	L	L	
Н	Н	Х	Х	Х	Н	L	Н	Н	

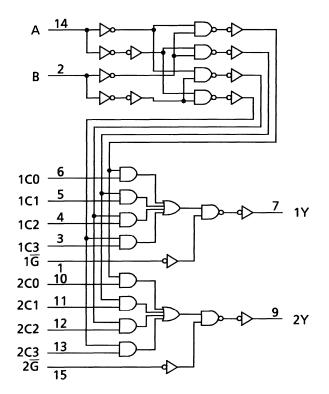
X: Don't care

Z: High impedance

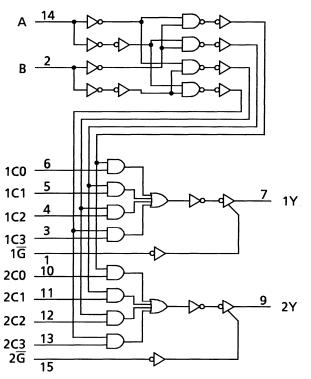
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System Diagram

TC74HC153A



TC74HC253A



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	–0.5 to 7	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	lıк	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65° C. From Ta = 65 to 85° C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0 to 500 (V _{CC} = 4.5 V)	ns
		0 to 400 ($V_{CC} = 6.0 \text{ V}$)	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C)	Ta = -40 to 85°C		Unit
	,			$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
		_		2.0	1.50	_	_	1.50	_	
High-level input voltage	VIH			4.5	3.15		—	3.15	—	V
				6.0	4.20		—	4.20	—	
				2.0	_		0.50	_	0.50	
Low-level input voltage	VIL	—		4.5	—		1.35	—	1.35	V
				6.0	—	_	1.80	—	1.80	
	V _{OH}		I _{OH} = -20 μA	2.0	1.9	2.0	_	1.9	_	
		V _{IN} = V _{IH} or V _{IL}		4.5	4.4	4.5	—	4.4	—	V
High-level output voltage				6.0	5.9	6.0	—	5.9	—	
			I _{OH} = -4 mA	4.5	4.18	4.31	_	4.13	_	
			I _{OH} = -5.2 mA	6.0	5.68	5.80	—	5.63	—	
	V _{OL}	V _{IN} = V _{IH} or V _{IL}		2.0	_	0.0	0.1	_	0.1	
			$I_{OL} = 20 \ \mu A$	4.5	—	0.0	0.1	—	0.1	
Low-level output voltage				6.0	—	0.0	0.1	—	0.1	V
			$I_{OL} = 4 \text{ mA}$	4.5	_	0.17	0.26	_	0.33	
			I _{OL} = 5.2 mA	6.0	—	0.18	0.26	—	0.33	
3-state output	I _{OZ}	$V_{IN} = V_{IH}$ or	V _{IL}	6.0			10.5		15.0	•
off-state current	(Note)	$V_{OUT} = V_{CC}$ or GND		0.0	_		±0.5	—	±5.0	μA
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$ or GND		6.0	_	_	±0.1	_	±1.0	μA
Quiescent supply current	ICC	V _{IN} = V _{CC} or GND		6.0			4.0		40.0	μΑ

Note: TC74HC253A only

AC Characteristics (CL = 15 pF, VCC = 5 V, Ta = 25° C, input: tr = tf = 6 ns)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time		t _{TLH} t _{THL}	_	_	4	8	ns
Propagation delay time (Cn-Y)		t _{pLH} t _{pHL}	—	_	12	19	ns
Propagation delay time (A, B-Y)		t _{pLH} t _{pHL}	_	_	17	26	ns
Propagation delay time $(\overline{G} - Y)$	(Note 1)	^t pLH t _{pHL}	_		8	16	ns
3-state output enable time $(\overline{G} - Y)$	(Note 2)	t _P ZL t _P ZH	$R_L = 1 k\Omega$	_	9	16	ns

Note 1: For TC74HC153A only

Note 2: For TC74HC253A only

AC Characteristics (C_L = 50 pF, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
			$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
	tтLH		2.0	—	30	75	—	95	
Output transition time	t _{THL}	—	4.5	—	8	15	—	19	ns
	THL		6.0	_	7	13	—	16	
Propagation delay	t _{pLH}		2.0	—	48	115		145	
time	t _{pHL}	—	4.5	—	15	23	—	29	ns
(Cn-Y)	φп∟		6.0	_	12	20	—	25	
Propagation delay	t _{pLH}		2.0	—	68	150	—	190	
time	^{ър} сн	—	4.5	—	20	30	—	38	ns
(A, B-Y)	чрнL		6.0	_	16	26		33	
Propagation delay time	t _{pLH}		2.0		31	95		120	
(G - Y)	t _{pHL}		4.5	—	11	19	—	24	ns
(Note 2)	φн∟		6.0		9	16		20	
3-state output enable time	t		2.0	_	36	100	_	125	
(G - Y)	t _{pZL}	$R_L = 1 \ k\Omega$	4.5	—	12	20	—	25	ns
(Note 3)	t _{pZH}		6.0	—	9	17	—	21	
3-state output disable time	+ . –		2.0		22	115	_	145	
(G - Y)	t _{pLZ}	$R_L = 1 \ k\Omega$	4.5	—	13	23	_	29	ns
(Note 3)	t _{pHZ}		6.0	—	11	20	—	25	
Input capacitance	C _{IN}	—		_	5	10		10	pF
Power dissipation	C _{PD}	TC74HC153A			58	_			
capacitance	(Note 1)	TC74HC253A			59				pF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

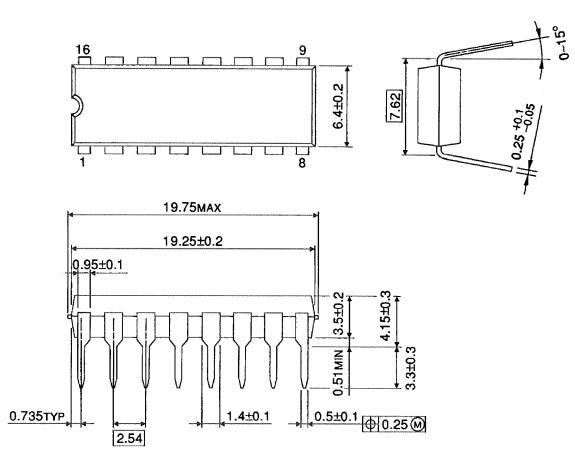
Note 2: For TC74HC153A only

Note 3: For TC74HC253A only

Package Dimensions

DIP16-P-300-2.54A

Unit : mm



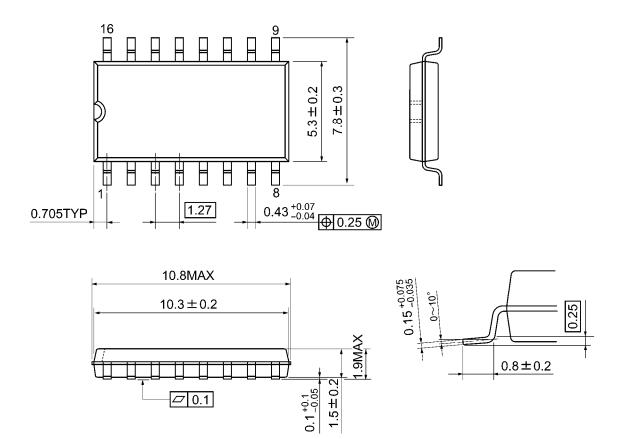
Weight: 1.00 g (typ.)



Package Dimensions

SOP16-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

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