**BLW40** 

#### **FEATURES**

- Emitter-ballasting resistors for an optimum temperature profile
- Gold metallization ensures excellent reliability.

### **DESCRIPTION**

NPN silicon planar epitaxial transistor encapsulated in a 4-lead SOT120 stud envelope with a ceramic cap. It is designed for common emitter, class-B operation in mobile VHF transmitters with a supply voltage of 12.5 V. All leads are isolated from the mounting flange.

#### **PINNING - SOT120**

PIN	DESCRIPTION		
1	collector		
2	emitter		
3	base		
4	emitter		

#### QUICK REFERENCE DATA

RF performance at  $T_{mb} \approx 25$  °C in a common emitter test circuit.

MODE OF	f	V <sub>CE</sub>	P <sub>L</sub> (W)	G <sub>p</sub>	η <sub>ε</sub>
OPERATION	(MHz)	(V)		(dB)	(%)
c.w. class-B	175	12.5	40	> 10	> 55

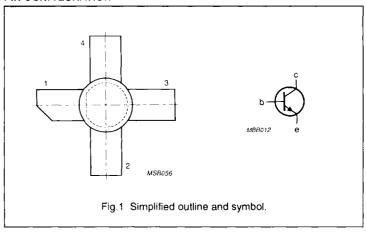
#### WARNING

#### Product and environmental safety - toxic materials

This product contains beryllium oxide. The product is entirely safe provided that the BeO disc is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with the general or domestic waste.

### **PIN CONFIGURATION**

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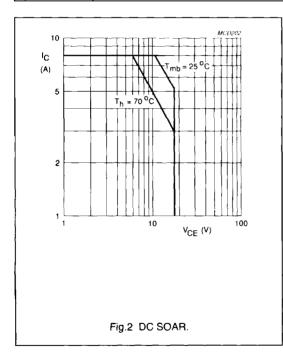


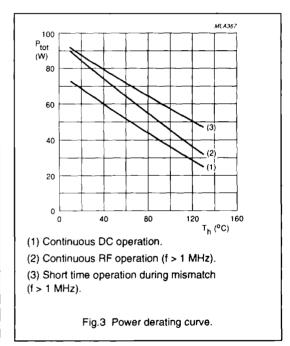
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#### LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	-	36	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	16.5	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	4	V
Ic, Ic(AV)	collector current	DC or average value	[-	8	Α
I <sub>CM</sub>	collector current	peak value f > 1 MHz	-	25	A
P <sub>tot</sub>	total power dissipation	RF operation; f > 1 MHz; T <sub>mb</sub> = 25 °C	-	106	W
T <sub>stg</sub>	storage temperature range		-65	150	°C
T <sub>i</sub>	junction operating temperature		-	200	°C





## THERMAL RESISTANCE

SYMBOL	PARAMETER	MAX.	UNIT
R <sub>th j-mb(RF)</sub>	from junction to mounting base	1.65	κw
R <sub>th mb-h</sub>	from mounting base to heatsink	0.45	K/W

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### CHARACTERISTICS

T, = 25 °C.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	open emitter; I <sub>C</sub> = 50 mA	36	-	-	٧
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	open base; I <sub>C</sub> = 100 mA	16.5	-	<u> </u>	٧
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	open collector; I <sub>E</sub> = 10 mA	4	-	-	٧
I <sub>CES</sub>	collector-emitter leakage current	V <sub>BE</sub> = 0; V <sub>CE</sub> = 20 V	-	-	25	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 5.4 A	15	60	-	
f <sub>T</sub>	transition frequency	$V_{CE} = 12.5 \text{ V};$ $I_{E} = 5 \text{ A}$	-	1.65	[-	GHz
C <sub>c</sub>	collector capacitance	$V_{CB} = 12.5 \text{ V};$ $I_E = I_e = 0;$ $f = 1 \text{ MHz}$	-	105	-	pF
C <sub>re</sub>	feedback capacitance	V <sub>CE</sub> = 12.5 V; I <sub>C</sub> = 0; f = 1 MHz	-	65	-	pF
C <sub>c-s</sub>	collector-stud capacitance	f = 1 MHz	-	2	-	pF

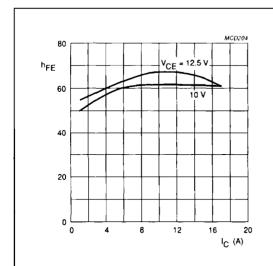


Fig.4 DC current gain as a function of collector current, typical values.

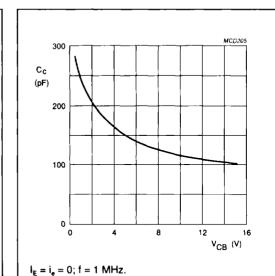
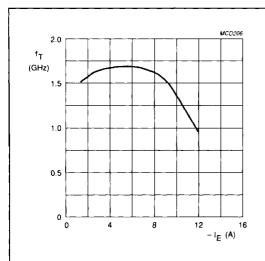


Fig.5 Collector capacitance as a function of collector-base voltage, typical values.

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 $V_{CB} = 12.5 \text{ V}.$ 

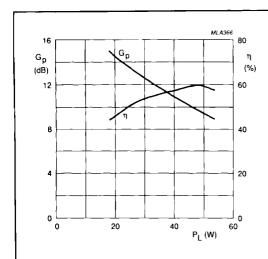
Fig.6 Transition frequency as a function of emitter current, typical values.

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### **APPLICATION INFORMATION**

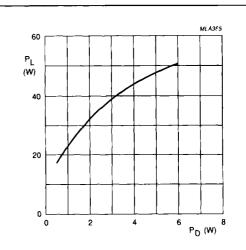
RF performance at  $T_{mb}$  = 25 °C in a common emitter test circuit.

MODE OF OPERATION	f	V <sub>CE</sub>	թ <sub>ւ</sub>	G <sub>p</sub>	η <sub>ε</sub>
	(MHz)	(V)	(W)	(dB)	(%)
c.w. class-B	175	12.5	40	> 10 typ. 11	> 55 typ. 57



Class-B operation;  $V_{CE} = 12.5 \text{ V}$ ; f = 175 MHz.

Fig.7 Gain and efficiency as functions of load power, typical values.



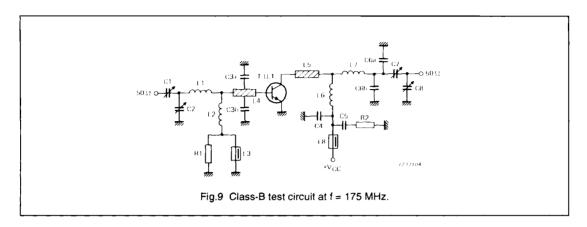
Class-B operation;  $V_{CE} = 12.5 \text{ V}$ ; f = 175 MHz.

Fig.8 Load power as a function of drive power, typical values.

## Ruggedness in class-B operation

The BLW40 is capable of withstanding a load mismatch corresponding to VSWR = 10:1 through all phases at rated output power, up to a supply voltage of 15.5 V, and f = 175 MHz.

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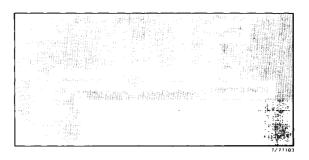
### List of components (see test circuit)

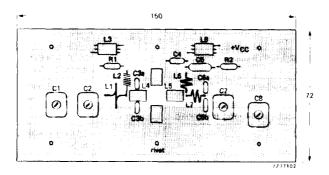
COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE NO.
C1	film dielectric trimmer	2.5 to 20 pF		2222 809 07004
C2, C8	film dielectric trimmer	4 to 40 pF	<del> </del>	2222 809 07008
C3a, C3b	500 V ceramic capacitor	47 pF		1
C4	500 V ceramic capacitor	120 pF	T	
C5	polyester capacitor	100 nF	T	
C6a, C6b	500 V ceramic capacitor	8.2 pF	1	
C7	film dielectric trimmer	5 to 60 pF		2222 809 07011
L1	1 turn enamelled 1.6 mm copper wire		int. dia. 9 mm; leads 2 x 5 mm	
L2	7 turns closely wound enamelled 0.5 mm copper wire	100 nH	int. dia. 3 mm; leads 2 x 5 mm	
L3, L8	grade 3B Ferroxcube wideband HF choke			4312 020 36640
L4, L5	stripline (note 1)		12 mm x 6 mm; note 2	
L6	2 turns enamelled 1.6 mm copper wire		int. dia. 5 mm; length 6 mm; leads 2 x 5 mm	
L7	2 turns enamelled 1.6 mm copper wire		int. dia. 4.5 mm; length 6 mm; leads 2 x 5 mm	
R1	0.25 W carbon resistor	10 Ω, 5%		
R2	0.25 W carbon resistor	4.7 Ω, 5%		

### Notes

- 1. The striplines are mounted on a double copper-clad printed circuit board, with PTFE fibre-glass dielectric, thickness ½6 inch.
- 2. Taps for capacitors C3a and C3b are situated 5 mm from the transistor.

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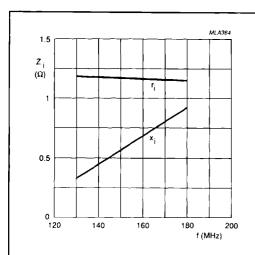




The circuit and components are situated on one side of an epoxy fibre-glass board; the other side is unetched and serves as a ground plane. Earth connections from the component side to the ground plane are made by means of hollow rivets and copper straps under the emitters.

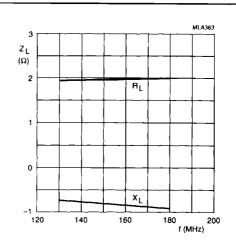
Fig.10 Component layout for 175 MHz class-B test circuit.

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Class-B operation;  $V_{CE} = 12.5 \text{ V}$ ;  $P_L = 40 \text{ W}$ .

Fig.11 Input impedance (series components) as a function of frequency, typical values.



Class-B operation;  $V_{CE} = 12.5 \text{ V}$ ;  $P_{L} = 40 \text{ W}$ .

Fig.12 Load impedance (series components) as a function of frequency, typical values.

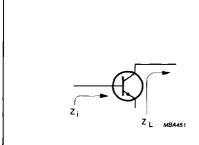
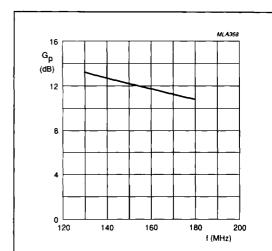


Fig.13 Definition of transistor impedance.



Class-B operation;  $V_{CE}$  = 12.5 V;  $P_L$  = 40 W.

Fig.14 Power gain as a function of frequency, typical values.