

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceed the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-35835
 - Class Q Military
 - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)

• Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

FAIRCHILD

SEMICONDUCTOR

74F2240 Octal Buffer/Line Driver with 25 Ω Series Resistors in the Outputs

General Description

The 74F2240 is an inverting octal buffer and line driver designed to drive capacitive inputs of MOS memory devices, address and clock lines or act as a low undershoot general purpose bus driver.

The 25Ω series resistor in the outputs reduces undershoot and ringing and eliminates the need for external resistors.

Features

3-STATE outputs drive bus lines or buffer memory address registers

January 1995

Revised May 1999

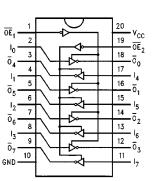
- Outputs sink 12 mA and source 15 mA
- 25Ω series resistors in outputs eliminate the need for external resistors
- Designed to drive the capacitive inputs of MOS devices
- Guaranteed 4000V minimum ESD protection

Ordering Code:

Order Number	Package Number	Package Description				
74F2240SC	M20B	20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide				
74F2240QC	V20A	20-Lead Plastic Lead Chip Carrier (PLCC), JEDEC MO-047, 0.350 Square				

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Truth Table

OE ₁	D _{1n}	O _{1n}	OE ₂	D _{2n}	O _{2n}	
Н	Х	Z	Н	Х	Z	
L	Н	L	L	Н	L	
L	L	Н	L	L	Н	

Unit Loading/Fan Out

Pin	Description	U.L.	Output I _{OH} /I _{OL}		
Names	Description	HIGH/LOW			
$\overline{\text{OE}}_1, \overline{\text{OE}}_2$	3-STATE Output				
	Enable Input	1.0/1.667	20 µA/–1 mA		
	(Active LOW)				
l ₀ - l ₇	Inputs	1.0/1.667	20 µA/-1 mA		
$\overline{O}_0 - \overline{O}_7$	Outputs	750/20	–15 mA/12 mA		

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Absolute Maximum Ratings(Note 1)

Storage Temperature	-65°C to + 150°C
Ambient Temperature under Bias	-55° to $+125^{\circ}C$
Junction Temperature under Bias	$-55^{\circ}C$ to $+150^{\circ}C$
V _{CC} Pin Potential to Ground Pin	-0.5V to +7.0V
Input Voltage (Note 2)	-0.5V to +7.0V
Input Current (Note 2)	-30 mA to +5.0 mA
Voltage Applied to Output	
In HIGH State (with $V_{CC} = 0V$)	
Standard Output	-0.5V to V _{CC}
3-STATE Output	-0.5V to +5.5V
Current Applied to Output	
in LOW State (Max)	twice the rated I _{OL} (mA)
ESD Last Passing Voltage (Min)	4000V

Recommended Operating Conditions

Free Air Ambient Temperature Supply Voltage 0°C to 70°C +4.5V to +5.5V

C Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

DC Electrical Characteristics

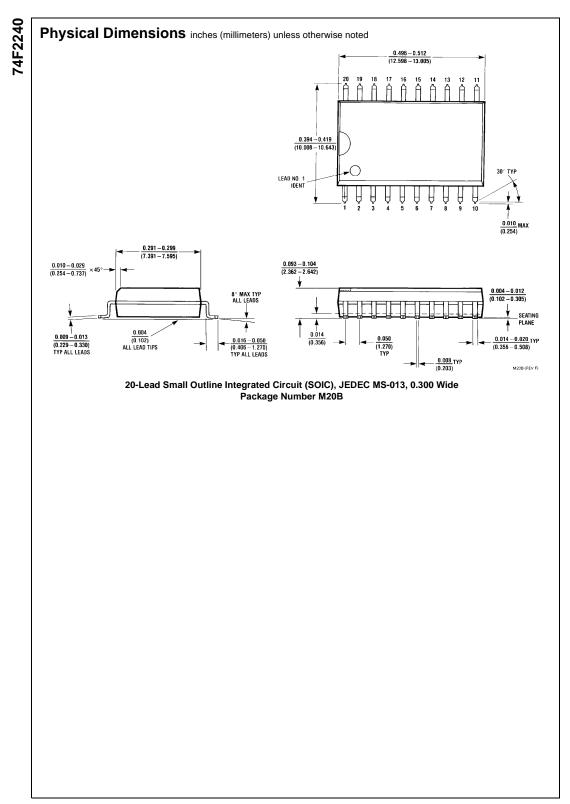
Symbol	Parameter	Min	Тур	Max	Units	V _{cc}	Conditions
VIH	Input HIGH Voltage	2.0			V		Recognized as a HIGH Signal
VIL	Input LOW Voltage			0.8	V		Recognized as a LOW Signal
V _{CD}	Input Clamp Diode Voltage			-1.2	V	Min	I _{IN} = -18 mA
V _{OH}	Output HIGH 10% V _{CC}	2.4			v	Min	$I_{OH} = -3 \text{ mA}$
	Voltage 10% V _{CC}	2.0			v	IVIIII	$I_{OH} = -15 \text{ mA}$
V _{OL}	Output LOW Voltage 10% V _{CC}			0.75	V	Min	I _{OL} = 12 mA
I _{IH}	Input HIGH Current			5.0	μΑ	Max	V _{IN} = 2.7V
I _{BVI}	Input HIGH Current Breakdown Test			7.0	μΑ	Max	$V_{IN} = 7.0V$
ICEX	Output HIGH Leakage Current			50	μA	Max	V _{OUT} = V _{CC}
VID	Input Leakage	4.75			V	0.0	I _{ID} = 1.9 μA
	Test	4.75		0.0	All Other Pins Grounded		
I _{OD}	Output Leakage			3.75	μA	0.0	V _{IOD} = 150 mV
	Circuit Current			5.75	μΛ	0.0	All Other Pins Grounded
I _{IL}	Input LOW		1	1.0	-1.0 mA	Max	$V_{IN} = 0.5V$
	Current			-1.0	mA	IVIAX	$(\overline{OE}_1, \overline{OE}_2, D_n)$
I _{OZH}	Output Leakage Current			50	μA	Max	$V_{OUT} = 2.7V$
I _{OZL}	Output Leakage Current			-50	μA	Max	$V_{OUT} = 0.5V$
los	Output Short-Circuit Current	-100		-225	mA	Max	V _{OUT} = 0V
I _{ZZ}	Bus Drainage Test			500	μΑ	0.0	V _{OUT} = 5.25V
I _{CCH}	Power Supply Current		16	29	mA	Max	V _O = HIGH
I _{CCL}	Power Supply Current		47	75	mA	Max	V _O = LOW
I _{CCZ}	Power Supply Current		45	63	mA	Max	V _O = HIGH Z

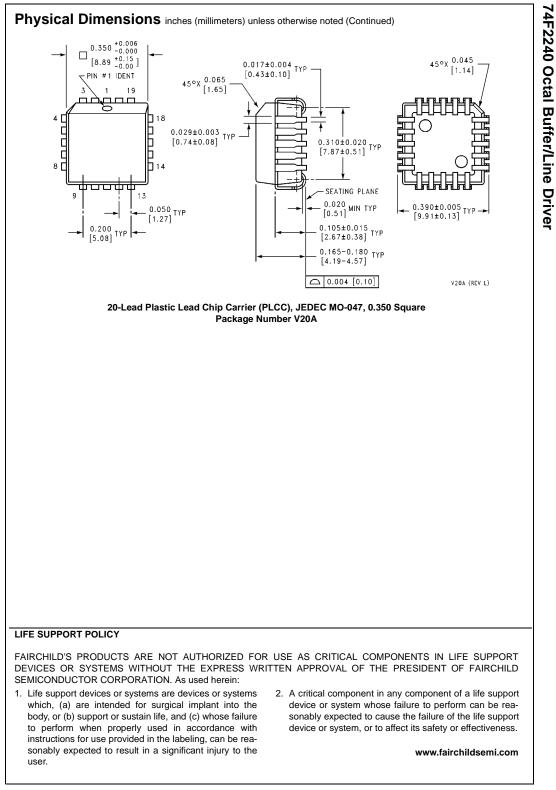
AC Electrical Characteristics

Symbol	Parameter		$T_{A} = +25^{\circ}C$ $V_{CC} = +5.0V$ $C_{L} = 50 \text{ pF}$			$T_{A} = 0^{\circ}C \text{ to } +70^{\circ}C$ $V_{CC} = +5.0V$ $C_{L} = 50 \text{ pF}$	
		Min	Тур	Max	Min	Max	1
t _{PLH}	Propagation Delay	3.0	4.9	7.5	3.0	7.5	ns
t _{PHL}	Data to Output	2.0	3.7	6.0	2.0	6.0	
t _{PZH}	Output Enable Time	2.0	3.9	6.5	2.0	7.0	ns
t _{PZL}		4.0	6.7	9.5	4.0	10.0	
t _{PHZ}	Output Disable Time	2.0	4.1	6.5	2.0	7.0	
t _{PLZ}		2.0	4.9	8.5	2.0	9.5	ns

74F2240

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