

LM119 Dual Voltage Comparator

Product Specification

Military Communications and Industrial Products

DESCRIPTION

The LM119 is a precision high-speed dual comparator fabricated on a single monolithic chip. It is designed to operate over a wide range of supply voltages down to a single 5V logic supply and ground. Further, it has higher gain and lower input currents than devices like the $\mu\text{A}710$. The uncommitted collector of the output stage makes the LM119 compatible with RTL, DTL, and TTL as well as capable of driving lamps and relays at currents up to 25mA.

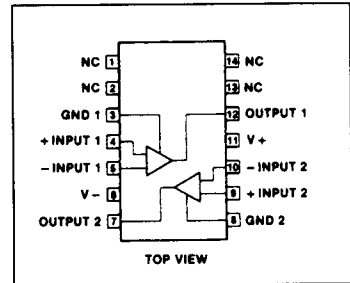
Although designed primarily for applications requiring operation from digital logic supplies, the LM119 is fully specified for power supplies up to $\pm 15\text{V}$. It features faster response than the LM111 at the expense of higher power dissipation.

However, the high-speed, wide operating voltage range and low package count make the LM119 much more versatile than older devices like the $\mu\text{A}711$.

FEATURES

- Two independent comparators
- Operates from a single 5V supply
- Typically 80ns response time at $\pm 15\text{V}$
- Minimum fanout of 3 (each side)
- Maximum Input current of $1\mu\text{A}$ over temperature
- Inputs and outputs can be isolated from system ground

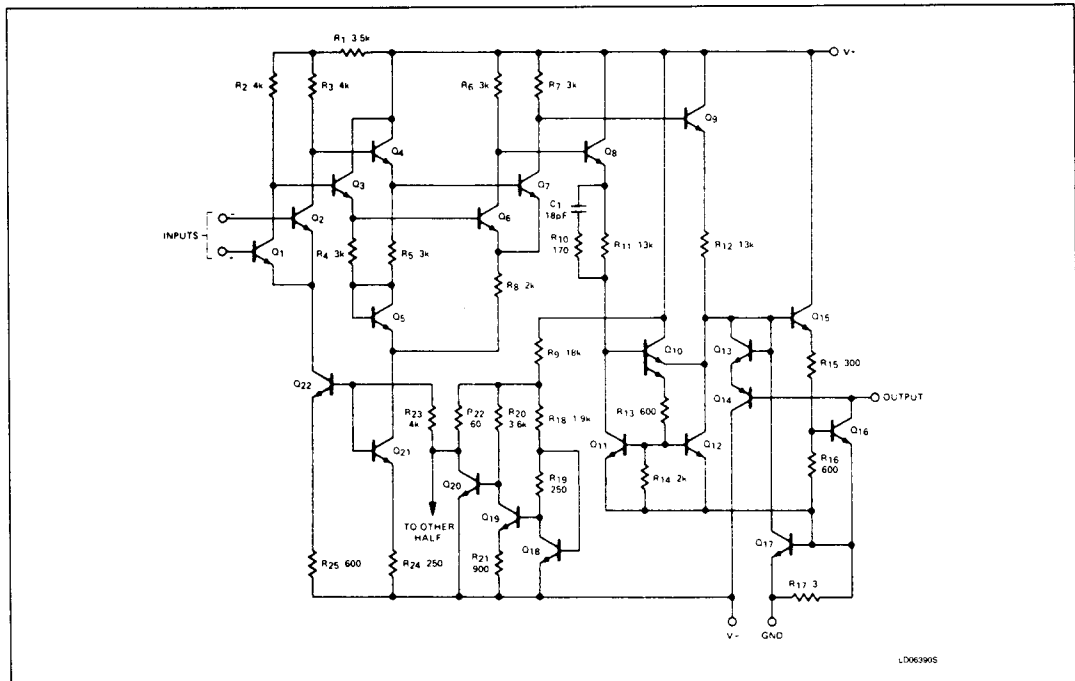
PIN CONFIGURATION



ORDERING INFORMATION

PACKAGES	ORDER CODE
14-Pin Ceramic DIP	LM119/BCA

EQUIVALENT SCHEMATIC



Dual Voltage Comparator

LM119

ABSOLUTE MAXIMUM RATINGS¹

SYMBOL	PARAMETER	RATING	UNIT
V_S	Total supply voltage	36	V
	Output to negative supply voltage	36	V
	Ground to negative supply voltage	25	V
	Ground to positive supply voltage	18	V
	Differential input voltage	± 5	V
V_{IN}	Input voltage ²	± 15	V
	Maximum power dissipation ³	500	mA
	Output short-circuit duration	10	s
T_A	Operating temperature range	-55 to +125	°C
T_{STG}	Storage temperature range	-65 to +150	°C
T_{SOLD}	Lead soldering temperature (10sec max)	300	°C

DC ELECTRICAL CHARACTERISTICS $V_S = \pm 15V$, $-55^\circ C \leq T_A \leq 125^\circ C$, unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	$T_A = +25^\circ C$			$T_A = -55^\circ C, +125^\circ C$		UNIT	
			Min	Typ	Max	Min	Max		
V_{OS}	Input offset voltage ^{4, 5}	$R_S \leq 5k\Omega$		0.7	4.0		7.0	mV	
I_{OS}	Input offset current ^{4, 5}			30	75		100	nA	
I_B	Input bias current ⁵			150	500		1000	nA	
A_V	Voltage gain ⁶		10	40				V/mV	
V_{OL}	Saturation voltage	$V_{IN}^7 \leq -5mV, I_{OUT} = 25mA$ $V_+ \geq 4.5V, V_- = 0$ $V_{IN}^7 \leq -6mV, I_{OUT} = 3.2mA$ $T_A = 125^\circ C$ $T_A = -55^\circ C$		0.75	1.5			V	
				0.23	0.4			V	
							0.4		V
							0.6		V
I_{OH}	Output leakage current	$V_- = 0V, V_{IN} \geq 5mV$ $V_{OUT} = 35V$		0.2	2		10	μA	
V_{IN}	Input voltage range	$V_S = \pm 15V$ $V_+ = 5V, V_- = 0V$	± 12	± 13		± 12		V	
			1		3	1	3	V	
V_{ID}	Differential input voltage				± 5		± 5	V	
I_+	Positive supply current	$V_+ = 5V, V_- = 0V$		4.3				mA	
I_+	Positive supply current	$V_S = \pm 15V$		8.0	11.5			mA	
I_-	Negative supply current	$V_S = \pm 15V$		3.0	4.5			mA	

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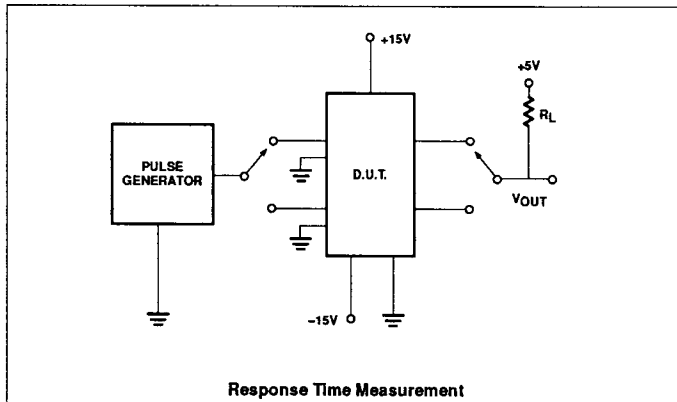
AC ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			Min	Typ	Max	
t_R	Response time ⁸	$V_S = \pm 15V, T_A = 25^\circ C$ $R_L = 500\Omega$ (see test figure)		80		ns

NOTES:

1. Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. Functional operation at these or any other conditions above those indicated in the operational specifications is not implied.
2. For supply voltages less than $\pm 15V$, the absolute maximum rating is equal to the supply voltage.
3. The absolute maximum junction temperature is $150^\circ C$. Device dissipation must be derated as $9.5mW/^\circ C$.
4. V_{OS} , I_{OS} and I_B specifications apply for a supply voltage range of $V_S = \pm 15V$ down to a single $5V$ supply.
5. The offset voltages and offset currents given are the maximum values required to drive the output to within $1V$ of either supply with a $1mA$ load. Thus these parameters define an error band and take into account the worst case effects of voltage gain and input impedance.
6. This parameter is guaranteed, but not tested.
7. V_{IN} value specified is the overdrive applied in addition to the specified V_{OS} value.
8. The response time specified is for a $100mV$ step with $5mV$ overdrive.

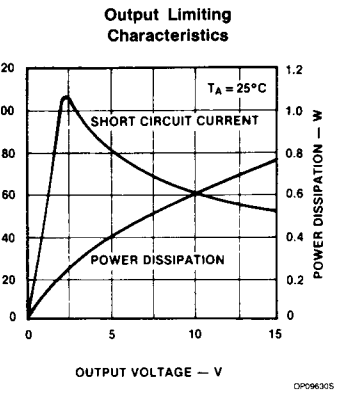
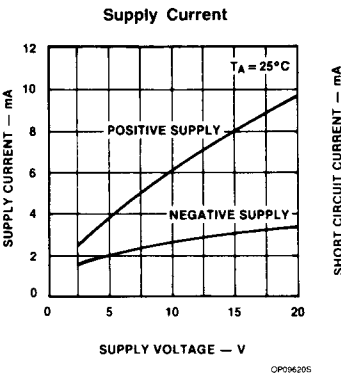
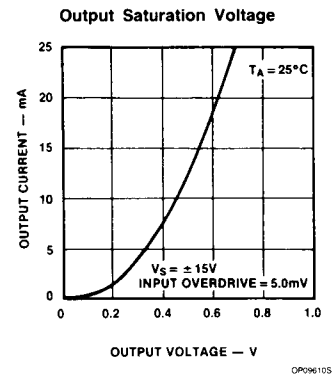
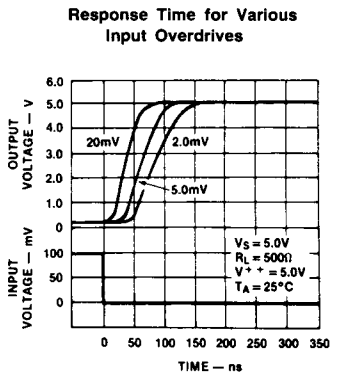
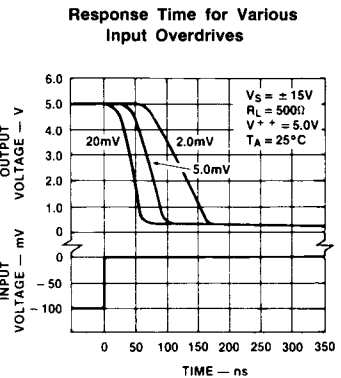
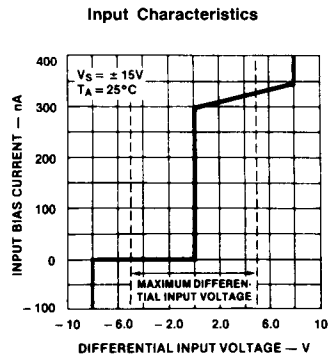
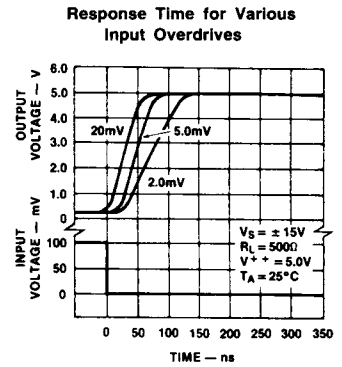
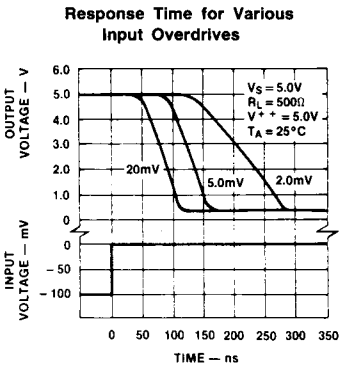
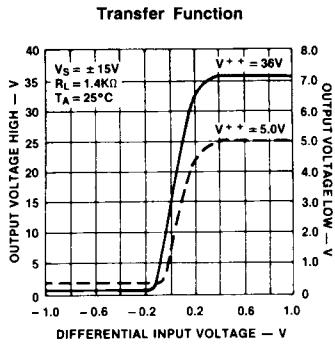
TEST CIRCUIT



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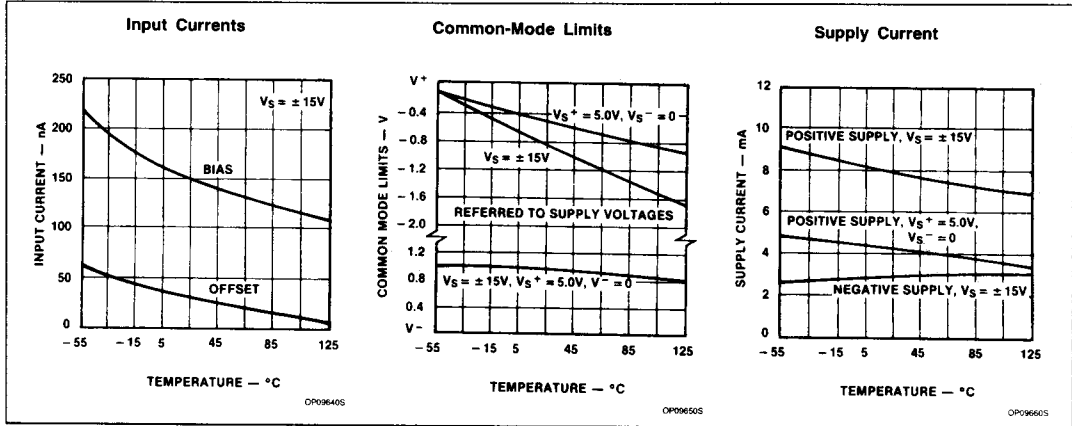
TYPICAL PERFORMANCE CHARACTERISTICS



Dual Voltage Comparator

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TYPICAL PERFORMANCE CHARACTERISTICS (Continued)



TYPICAL APPLICATIONS

