

## Operational Amplifier

For Military, Industrial, and Commercial Applications

### Applications:

- Long-interval integrators
- Timers
- Sample-and-hold circuits
- Summing amplifiers
- Multivibrators

The RCA CA307 is a general-purpose operational amplifier intended for use in military, industrial, and commercial applications. A 30-pF on-chip capacitor provides internal frequency compensation.

The CA307 is available in 8-lead TO-5 style packages with

standard leads (T suffix), with dual-in-line formed leads ("DIL-CAN", S suffix), in the 8-lead dual-in-line plastic package ("MINI-DIP", E suffix), and in chip form (H suffix).

The CA307 is a direct replacement for industry type 307 in packages with similar terminal arrangements.

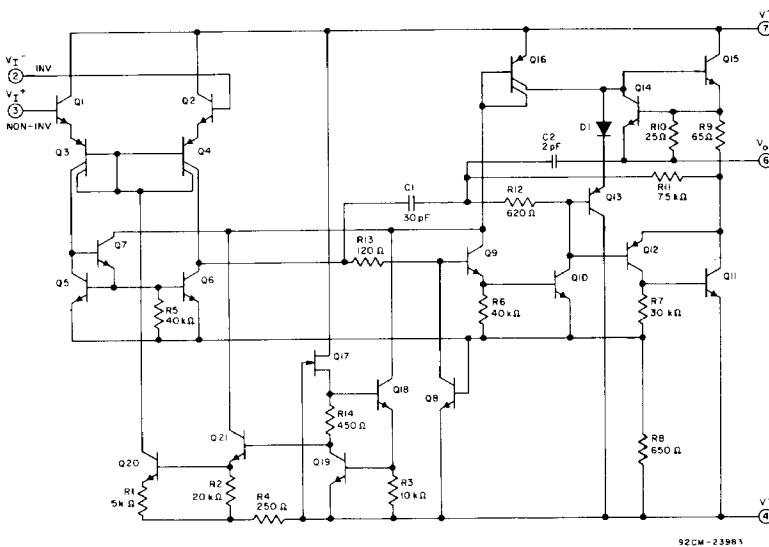


Fig. 1 - Schematic diagram of CA307.

\*Technical Data on LM Branded types is identical to the corresponding CA Branded types.

**CA307, LM307****Maximum Ratings, Absolute Maximum Values at  $T_A = 25^\circ C$ :**DC SUPPLY VOLTAGE (Between  $V_+$  and  $V_-$  Terminals):

CA307 ..... 36 V

DC INPUT VOLTAGE ..... ±15 V

(For supply voltages less than ±15 V, the absolute maximum input voltage is equal to the supply voltage)

DIFFERENTIAL INPUT VOLTAGE ..... ±30 V

OUTPUT SHORT-CIRCUIT DURATION\* ..... Indefinite

DEVICE DISSIPATION UP TO  $T_A = 70^\circ C$  ..... 500 mWAbove  $T_A = 70^\circ C$  Derate linearly at ..... 6.67 mW/ $^\circ C$ 

AMBIENT TEMPERATURE RANGE:

Operating ..... 0° C to +70° C†

Storage ..... -65° C to +150° C

LEAD TEMPERATURE (During Soldering):

At distance  $1/16 \pm 1/32$  inch ( $1.59 \pm 0.79$  mm) from case for 10 seconds max. ..... +265° C

\*For type CA307 continuous short circuit is allowed for Case Temperature to +70° C and ambient temperature to +55° C.

†Types CA307 E, S, and T can be operated over the temperature range of -55 to +125° C, although the published limits for certain electrical specifications apply only over the temperature range of 0 to 70° C.

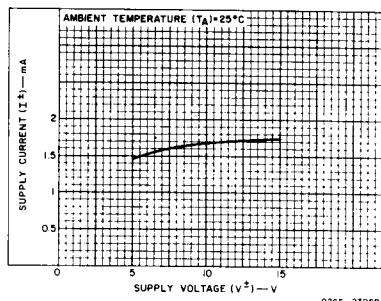


Fig. 2 - Supply current vs. supply voltage.

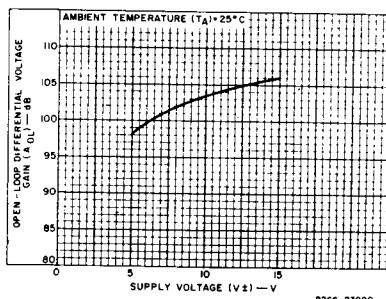


Fig. 3 - Open-loop differential voltage gain vs. supply voltage.

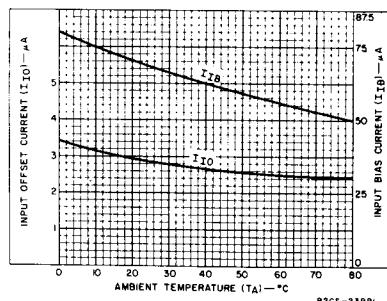


Fig. 4 - Input offset and input bias current vs. ambient temperature.

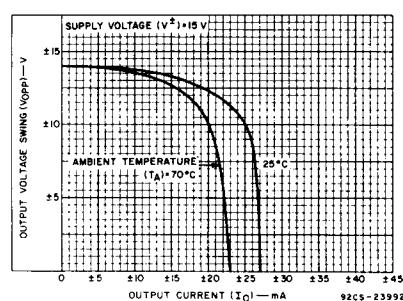
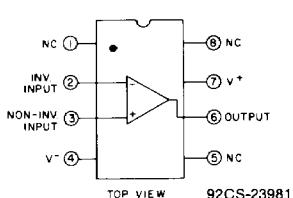


Fig. 5 - Output voltage swing vs. output current.

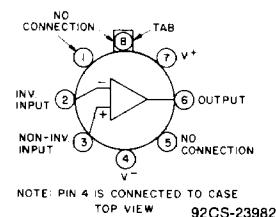
**CA307, LM307****ELECTRICAL CHARACTERISTICS**

CHARACTERISTIC	TEST CONDITIONS <sup>Δ</sup>		LIMITS			UNITS	
	Supply Voltage ( $V_{\pm}$ ) = 5 V to 15 V		CA307				
	Min.	Typ.	Max.				
Input Offset Voltage V <sub>IO</sub>	TA = 25°C, R <sub>S</sub> ≤ 50 kΩ	—	2	7.5		mV	
	R <sub>S</sub> ≤ 50 kΩ	—	—	10			
Average Temperature Coefficient of Input Offset Voltage α <sub>VIO</sub>		—	6	30	μV/°C		
Input Offset Current I <sub>IO</sub>		—	—	70		nA	
	TA = 25°C	—	3	50			
Average Temperature Coefficient of Input Offset Current α <sub>IIO</sub>	+25 to 70°C	—	0.01	0.3		nA/°C	
	0 to +25°C	—	0.02	0.6			
Input Bias Current I <sub>B</sub>		—	—	300		nA	
	TA = 25°C	—	70	250			
Supply Current I <sub>S</sub>	TA = +125°C, V <sub>±</sub> = 20 V	—	—	—		mA	
	TA = 25°C, V <sub>±</sub> = 15 V	—	1.8	3			
Open-Loop Differential Voltage Gain A <sub>OL</sub>	V <sub>±</sub> = 15 V, V <sub>O</sub> = ±10V, R <sub>L</sub> ≥ 2kΩ	15	—	—		V/mV	
	V <sub>±</sub> =15V, V <sub>O</sub> =±10 V, R <sub>L</sub> ≥2kΩ, T <sub>A</sub> =25°C	25	160	—			
Input Resistance R <sub>I</sub>	TA = 25°C	0.5	2	—	MΩ		
Output Voltage Swing V <sub>OVP</sub>	V <sub>±</sub> = 15 V, R <sub>L</sub> = 10 kΩ	±12	±14	—		V	
	V <sub>±</sub> = 15 V, R <sub>L</sub> = 2 kΩ	±10	±13	—			
Input Voltage Range V <sub>ICR</sub>	V <sub>±</sub> = 15 V	±12	—	—	V		
Common-Mode Rejection Ratio CMRR	R <sub>S</sub> ≤ 50 kΩ	70	90	—	dB		
Supply-Voltage Rejection Ratio PSRR	R <sub>S</sub> ≤ 50 kΩ	70	96	—	dB		

ΔCharacteristics applicable over operating temperature range TA = 0 to 70°C unless otherwise specified.



FUNCTIONAL DIAGRAM FOR PLASTIC PACKAGE



FUNCTIONAL DIAGRAM FOR TO-5 STYLE PACKAGES

## CA307, LM307

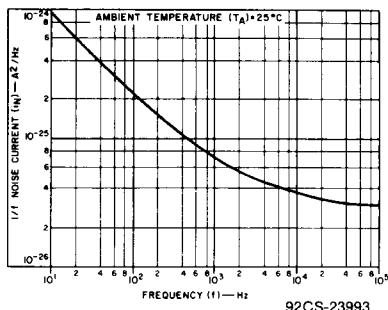


Fig. 6 -  $1/f$  noise current vs. frequency.

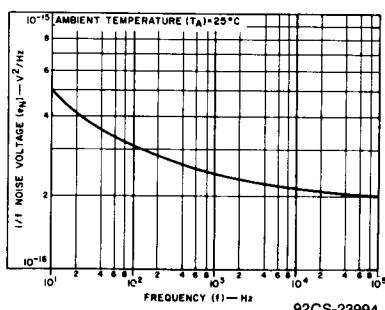


Fig. 7 -  $1/f$  noise voltage vs. frequency.

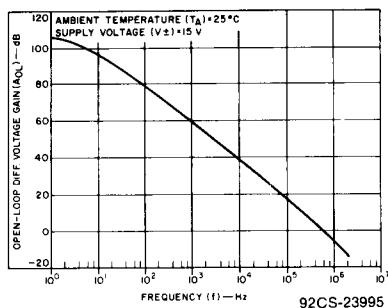


Fig. 8 - Open-loop differential voltage gain vs. frequency.

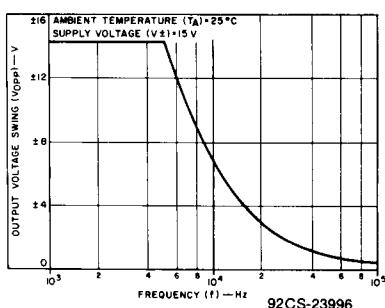


Fig. 9 - Output voltage swing vs. frequency.

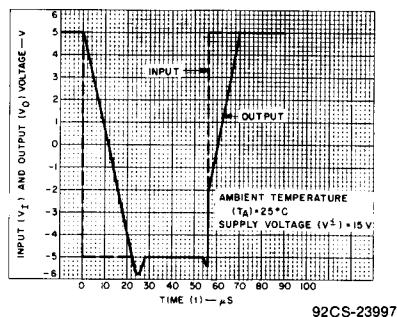
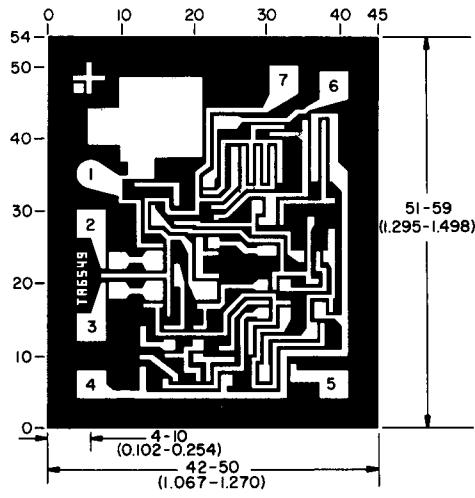


Fig. 10 - Input and output voltage vs. time.



Dimensions and pad layout for CA307H.

Dimensions in parentheses are in millimeters and are derived from the basic inch dimensions as indicated. Grid graduations are in mils ( $10^{-3}$  inch).