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3-to-8-line Decoder/Demultiplexer with Address Latch



ADE-205-443 (Z) 1st. Edition Sep. 2000

Description

The HD74HC137 implements a three-to-eight line decoder with latches on the three address inputs. When \overline{GL} goes from low to high, the address present at the select inputs (A, B and C) is stored in the latches. As long as \overline{GL} remains high no address changes will be recognized. Output enable controls, G_1 and $\overline{G_2}$, control the state of the outputs independently of the select or latch-enable inputs.

All of the outputs are high unless G_1 is high and $\overline{G_2}$ is low. The HD74HC137 is ideally suited for the implementation of glitchfree decoders in stored-address applications in bus oriented systems.

Features

• High Speed Operation: t_{pd} (A, B, C to Y) = 16.5 ns typ ($C_L = 50 \text{ pF}$)

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2 \text{ V to } 6 \text{ V}$

• Low Input Current: 1 µA max

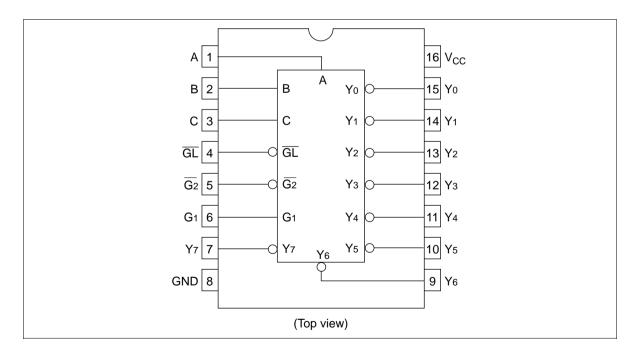
• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

Function Table

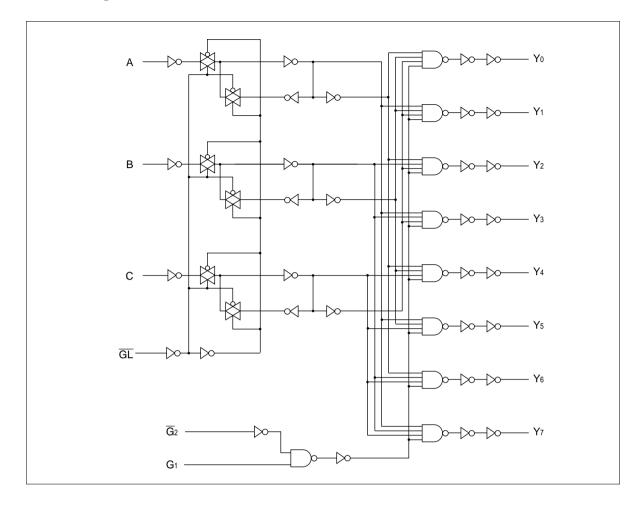
Inputs

Ena	ble		Sele	ct		Outp	Outputs								
GL	G ₁	$\overline{G_{\scriptscriptstyle 2}}$	С	В	Α	Y ₀	Y ₁	Y ₂	Y ₃	Y ₄	Y ₅	Y ₆	Y ₇		
X	Χ	Н	Χ	Χ	Χ	Н	Н	Н	Н	Н	Н	Н	Н		
X	L	Χ	Χ	Χ	Χ	Н	Н	Н	Н	Н	Н	Н	Н		
L	Н	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н		
L	Н	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н		
L	Н	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н		
L	Н	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н		
L	Н	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н		
L	Н	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н		
L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н		
L	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L		
Н	Н	L	Χ	Χ	Х	Output Corresponding to stored address L; all Others. H									

Pin Arrangement



Block Diagram



DC Characteristics

			Ta = 25°C		Ta = -40 to +85°C		_			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Condition	ns
Input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	i —		3.15	_	_		
		6.0	4.2	_	_	4.2	_	=		
	V _{IL}	2.0	_	_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35	_		
		6.0	_	_	1.8	_	1.8	=		
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	_	V	Vin = V _{IH} or V _{IL}	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_	=		
		6.0	5.9	6.0	_	5.9	_	=		
		4.5	4.18	s —	_	4.13	_	=		$I_{OH} = -4 \text{ mA}$
		6.0	5.68	3 —	_	5.63	_	=		$I_{OH} = -5.2 \text{ mA}$
	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	Vin = V _{IH} or V _{IL}	I _{OL} = 20 μA
		4.5	_	0.0	0.1	_	0.1	=		
		6.0	_	0.0	0.1	_	0.1	=		
		4.5	_	_	0.26	_	0.33	_		I _{OL} = 4 mA
		6.0	_	_	0.26	_	0.33	_		I _{OL} = 5.2 mA
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V _{CC} or Gf	ND
Quiescent supply current	I _{cc}	6.0	_	_	4.0	_	40	μΑ	Vin = V _{cc} or Gf	ND, lout = $0 \mu A$

AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

Ta = -40 to $Ta = 25^{\circ}C$ +85°C

Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	_	_	170	_	215	ns	A, B or C to Y
time		4.5	_	16	34	_	43	_	
		6.0	_	_	29	_	37	_	
	t _{PHL}	2.0	_	_	240	_	300	ns	_
		4.5	_	17	48	_	60	_	
		6.0	_	_	41	_	51	_	
	t _{PLH}	2.0	_	_	130	_	165	ns	$\overline{G}_{\scriptscriptstyle{2}}$ to Y
		4.5	_	13	26	_	33	=	
		6.0	_	_	22	_	28	=	
	t _{PHL}	2.0	_	_	195	_	245	ns	_
		4.5	_	14	39	_	49	=	
		6.0	_	_	33	_	42	=	
	t _{PLH}	2.0	_	_	150	_	190	ns	G ₁ to Y
		4.5	_	14	30	_	38	=	
		6.0	_	_	26	_	33	=	
	t _{PHL}	2.0	_	_	195	_	245	ns	_
		4.5	_	14	39	_	49	=	
		6.0	_	_	33	_	42	=	
	t _{PLH}	2.0	_	_	175	_	220	ns	GL to Y
		4.5	_	17	35	_	44		
		6.0	_	_	30	_	37	=	
	t _{PHL}	2.0	_	_	250	_	315	ns	_
		4.5	_	18	50	_	63	=	
		6.0	_	_	43	_	54	=	
Setup time	t _{su}	2.0	100	_	_	125	_	ns	A, B, C inputs
		4.5	20	3	_	25	_	_	
		6.0	17	_	_	21	_	_	
Hold time	t _h	2.0	50	_	_	65	_	ns	A, B, C inputs
		4.5	10	-3	_	13	_	_	
		6.0	9	_	_	11	_	_	

Input capacitance Cin

AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$) (Cont)

6.0

			Ta = 25°C			Ta = −40 to +85°C				
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions	
Pulse width	t _w	2.0	80	_	_	100	_	ns		
		4.5	16	7	_	20	_			
		6.0	14	_	_	17	_	_		
Output rise/fall	t _{TLH}	2.0	_	_	75	_	90	ns		
time	t_{THL}	4.5	_	5	15	_	19	-		

13

10

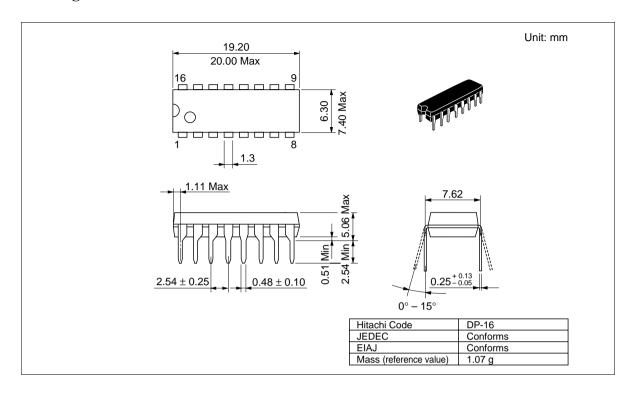
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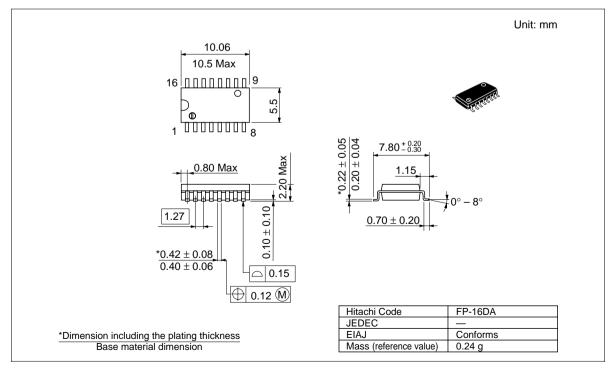
16

10

рF

Package Dimensions





Unit: mm 9.9 10.3 Max 16 _______9 3.95 1 1000000 8 1.27 *0.22 ± 0.03 0.20 ± 0.03 1.75 Max 6.10 + 0.10 1.08 0.635 Max 0° – 8° $0.60^{+0.67}_{-0.20}$ $^{*}0.42 \pm 0.08 \over 0.40 \pm 0.06$ 0.15 0.25 (M) Hitachi Code FP-16DN JEDEC Conforms *Dimension including the plating thickness EIAJ Conforms Base material dimension Mass (reference value) 0.15 g

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