

SN5413, SN54LS13, SN7413, SN74LS13

Dual 4-Input Positive-Nand Schmitt Triggers

Each circuit functions as a 4-input NAND gate, but because of the Schmitt action, it has different input threshold levels for positive (V_{T+}) and for negative going (V_{T-}) signals. These circuits are temperature-compensated and can be triggered from the slowest of input ramps and still give clean, jigger-free output signals.

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
 - Class Q Military
 - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)
 - Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

SN5413, SN54LS13, SN7413, SN74LS13 **DUAL 4-INPUT POSITIVE-NAND SCHMITT TRIGGERS** DECEMBER 1983-REVISED MARCH 1988

- Operation from Very Slow Edges
- ٠ Improved Line-Receiving Characteristics
- **High Noise Immunity**

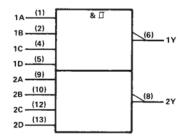
description

Each circuit functions as a 4-input NAND gate, but because of the Schmitt action, it has different input threshold levels for positive (VT +) and for negative going (VT_) signals.

These circuits are temperature-compensated and can be triggered from the slowest of input ramps and still give clean, jitter-free output signals.

The SN5413 and SN54LS13 are characterized for operation over the full military temperature range of -55°C to 125°C. The SN7413 and SN74LS13 are characterized for operation from 0°C to 70°C.

logic symbol[†]



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-13.

Pin numbers shown are for D, J, N, and W packages.

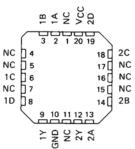
SN5413, SN54LS13 . . . J OR W PACKAGE SN7413 . . . N PACKAGE SN74LS13 . . . D OR N PACKAGE (TOP VIEW) J₁₄] Vcc 13 2D 12 2C 1B []2 NC 3 1C □4 11 NC 10 2B 9 2A 1D 🗗 5

> SN54LS13 . . . FK PACKAGE (TOP VIEW)

8 2Y

1Y 🗗 6

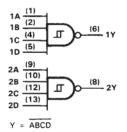
GND 07



TTL Devices **D**

NC-No internal connection

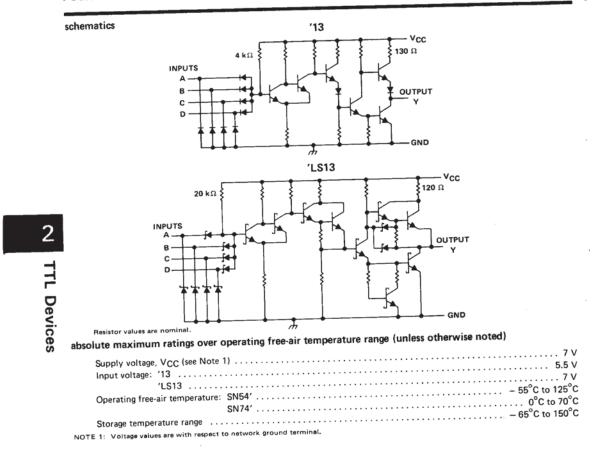
logic diagram (positive logic)



PRODUCTION DATA documents contain information current as of publication data. Products conform to specifications per the terms of Texas instruments standard warrenty. Productien processing does not necessarily include testing of all parameters.



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SN5413, SN7413 **DUAL 4 INPUT POSITIVE-NAND SCHMITT TRIGGERS**

recommended operating conditions

		SN5413		SN7413			
	MIN NOM		MAX	MIN	NOM	MAX	UNIT
V _{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	V
IOH High-level output current			- 0.8			0.8	mA
IOL Low-level output current			16			16	mA
T _A Operating free-air temperature	- 55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS [†]	MIN	TYP [‡] MA	x	UNIT
VT+	V _{CC} = 5 V	1.5	1.7	2	v
V _T -	V _{CC} = 5 V	0.6	0.9	1.1	v
Hysteresis	No EV			-+	
$(v_{T+} - v_{T-})$	V _{CC} = 5 V	0.4	0.8		v
VIK	V _{CC} = MIN, ! ₁ = - 12 mA			1.5	V
VOH	V _{CC} = MIN, V _I = 0.6 V, I _{OH} = - 0.8 mA	2.4	3.4	-+	V
VOL	$V_{CC} = MIN$, $V_I = 2V$, $I_{OL} = 16 mA$		0.2	0.4	v
IT+	$V_{CC} = 5 V, \qquad V_I = V_{T+}$		- 0.65	-+	mA
	$V_{CC} = 5 V$, $V_{I} = V_{T-}$		- 0.85	-+	mΑ
4	V _{CC} = MAX, V ₁ = 5.5 V			1	mA
1 _{IH}	V _{CC} = MAX, V _{IH} = 2.4 V			40	μA
<u>1L</u>	V _{CC} = MAX, V _{IL} = 0.4 V		-1 -	1.6	mA
1 _{OS} §	V _{CC} = MAX,	- 18	_	55	mA
ICCH	V _{CC} = MAX		14	23	mA
CCL	V _{CC} = MAX		20	32	mA

TTL Devices **D**

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[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. [‡] All typical values are at $V_{CC} \approx 5 V$, $T_{A} = 25^{\circ}$ C. § Not more than one output should be shorted at a time.

switching characteristics, V_{CC} = 5 V, T_A = 25° C

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS			түр	MAX	UNIT
tPLH	Any	Y	R _L = 400 Ω, 0	L = 15 pF		18	27	ns
^t PHL						15	22	ns



SN54LS13, SN74LS13 **DUAL 4-INPUT** POSITIVE NAND SCHMITT TRIGGERS

recommended operating conditions

	SN54LS13		3	S	UNIT		
	MIN	NOM	MAX	MIN	NOM	MAX	0
	4,5	5	5,5	4.75	5	5.25	v
V _{CC} Supply voltage			0.4			- 0.4	mA
OH High-level output current			4			8	mA
OL Low-level output current			125	0		70	°C
TA Operating free-air temperature	- 55		125				

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

						SN54LS13			SN74LS13			
PARAMETER	TEST CONDITIONS ^T			MIN	TYP [‡]	MAX	MIN	түр‡		UNIT		
	Versey				1.4	1.6	1.9	1.4	1.6	1.9	V	
VT+	$V_{CC} = 5 V$				0.5	0.8	1	0.5	0.8	1	V	
V _T	V _{CC} = 5 V								0.0		v	
Hysteresis	V _{CC} = 5 V				0.4	0.8		0.4	0.8		Ň	
$(V_{T+} - V_{T-})$					<u>+</u>		- 1.5			- 1.5	V	
VIK	V _{CC} = MIN,	l ₁ = – 18 mA			2.5	3.4		2.7	3.4		V	
Voн	V _{CC} = MIN,	V ₁ = 0.5 V,	IOH = - 0.4 m/		2.5					0.4		
				I _{OL} = 4 mA		0.25	0.4		0.25		v	
VOL	V _{CC} = MIN,	V _I = 1.9 V		IOL = 8 mA					0.35	0.5		
		N N-				- 0.14			- 0.14		mA	
ⁱ T+	V _{CC} = 5 V,	$V_I \approx V_{T+}$				- 0.18			- 0.18		mA	
IT-	V _{CC} = 5 V,						0,1			0.1	mA	
й <u>, і</u>	V _{CC} = MAX,	V ₁ = 7 V			+		20	<u> </u>		20	μA	
Чн	V _{CC} = MAX,	V _{IH} = 2.7 V								- 0.4	mA	
μ	V _{CC} = MAX,	VIL = 0.4 V				_	- 0.4					
IOS§	V _{CC} = MAX				- 20		- 100	- 20		- 100	mA	
				2.9	6		2.9	6	mA			
КСН						4.1	7		4.1	7	mA	
I ICCL	V _{CC} = MAX											

2 TTL Devices

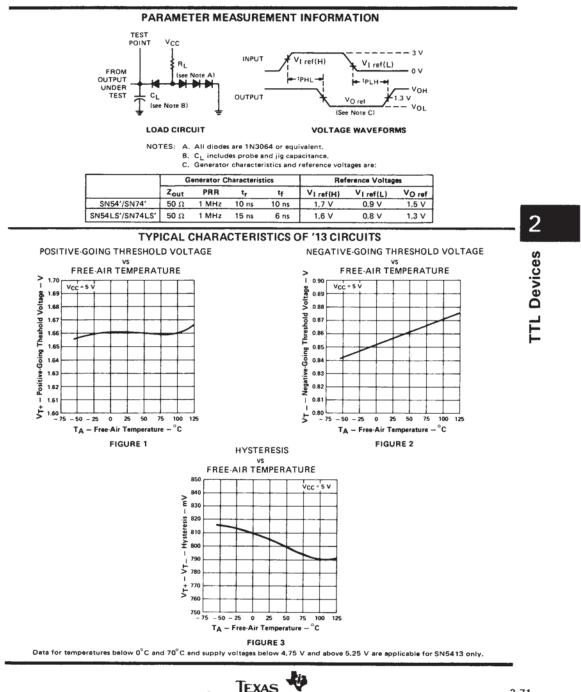
† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

All typical values are at V_{CC} = 5 V, T_A = 25°C.
§ Not more than one output should be shorted at a time, and duration of the short-circuit should not exceed one second.

switching characteristics, VCC = 5 V , TA = 25°C

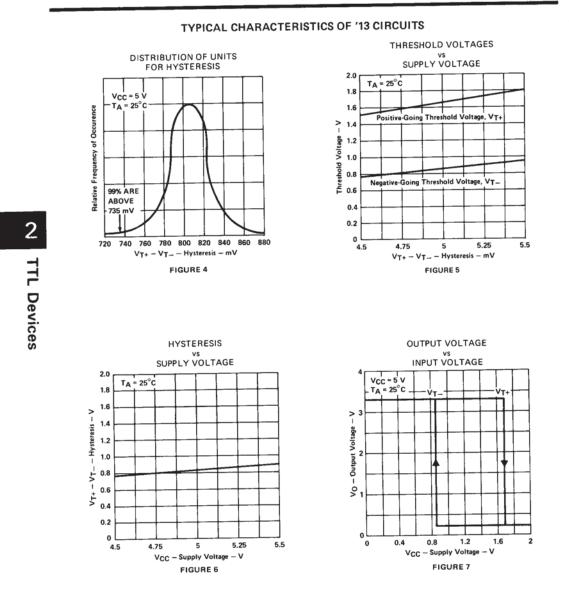
PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS			түр	MAX	UNIT	
^t PLH		~	$R_{L} = 2 k \Omega,$	C _L = 15 pF		15	22	ns	$\frac{1}{2}$
^t PHL	Any					18	27	ns	1

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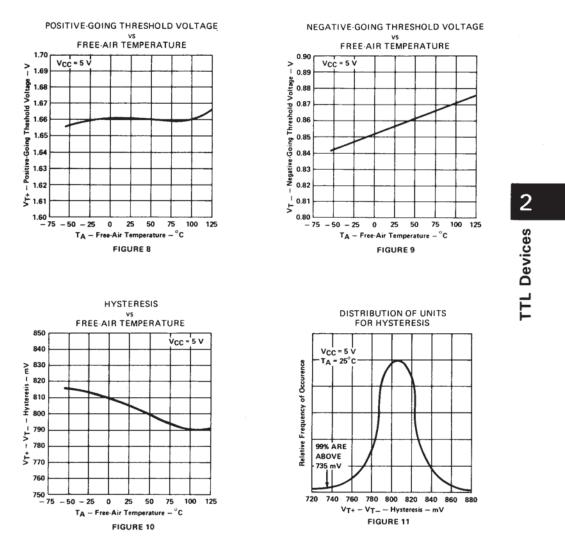


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Data for temperatures below 0°C and 70°C and supply voltages below 4.75 V and above 5.25 V are applicable for SN5413 only.



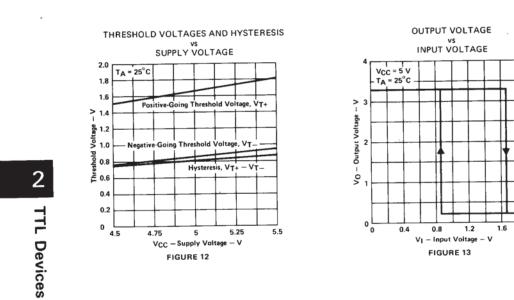
2-72



TYPICAL CHARACTERISTICS OF 'LS13 CIRCUITS

Data for temperatures below 0°C and above 70°C and supply voltages below 4.75 V and above 5.25 V are applicable for SN54LS13 only.

TEXAS EXAMPLE INSTRUMENTS



TYPICAL CHARACTERISTICS OF 'LS13 CIRCUITS

1

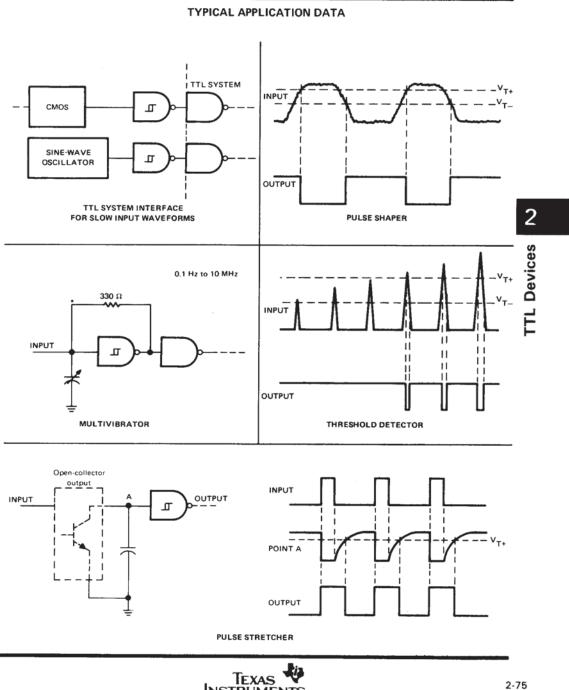
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Data for temperatures below 0°C and above 70°C and supply voltages below 4.75 V and above 5.25 V are applicable for SN54LS13 only.



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