# <u>TOSHIBA</u>

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74HC7240AP,TC74HC7240AF,TC74HC7244AP,TC74HC7244AF

Octal Bus Buffer (with schmitt trigger inputs)

TC74HC7240AP/AF TC74HC7244AP/AF Inverted, 3-State Outputs Non-Inverted, 3-State Outputs

The TC74HC7240A/7244A are high speed CMOS OCTAL BUS BUFFERs with silicon gate  $C^2MOS$  technology.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

The TC74HC7240A/7244A have same pin configuration and function as the TC74HC240A/244A. And they have a hystereis characterictics with each input, so TC74HC7240A/7244A can be used as a line receiver, etc.

They have two active low output enables.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### Features

- High speed:  $t_{pd} = 15$  ns (typ.) at V<sub>CC</sub> = 5 V
- Low power dissipation:  $I_{CC} = 4 \mu A \pmod{at Ta} = 25 \circ C$
- High noise immunity:  $V_H = 1.1 V$  (typ.) at  $V_{CC} = 5 V$
- Output drive capability: 15 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 6 mA (min)
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V<sub>CC</sub> (opr) = 2 to 6 V
- Pin and function compatible with 74LS240/244

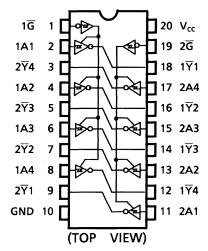
# 

DIP20-P-300-2.54A SOP20-P-300-1.27A

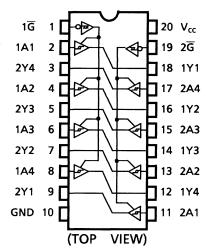
: 1.30 g (typ.) : 0.22 g (typ.)

## **Pin Assignment**

#### TC74HC7240A



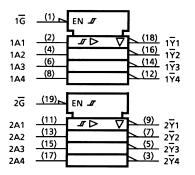
#### TC74HC7244A



# **TOSHIBA**

#### **IEC Logic Symbol**

#### TC74HC7240A



#### **Truth Table**

Inp	uts	Outputs				
IG	A <sub>n</sub>	Yn	$\overline{Y}_n{}^{\scriptscriptstyle \Delta}$			
L	L	L	Н			
L	Н	Н	L			
Н	Х	Z	Z			

 $\Delta:$  For TC74HC7240A only

X: Don't care

Z: High impedance

# Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	–0.5 to 7	V
DC input voltage	V <sub>IN</sub>	–0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	–0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	IIК	±20	mA
Output diode current	IOK	±20	mA
DC output current	IOUT	±35	mA
DC V <sub>CC</sub> /ground current	ICC	±75	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T <sub>stg</sub>	–65 to 150	°C

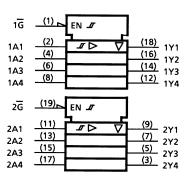
Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

#### TC74HC7244A



# **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	2 to 6	V
Input voltage	VIN	0 to V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40 to 85	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

## **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Symbol	Test Condition			Ta = 25°C		)	Ta = -40 to 85°C		Unit
5,				$V_{CC}(V)$	Min	Тур.	Max	Min	Max	Offic
		_		2.0	1.0	1.25	1.5	1.0	1.5	
Positive threshold voltage	VP			4.5	2.3	2.7	3.15	2.3	3.15	V
				6.0	3.0	3.5	4.2	3.0	4.2	
		_		2.0	0.3	0.65	0.9	0.3	0.9	
Negative threshold voltage	V <sub>N</sub>			4.5	1.13	1.6	2.0	1.13	2.0	V
Ĵ				6.0	1.5	2.3	2.6	1.5	2.6	
		_		2.0	0.3	0.6	1.0	0.3	1.0	
Hysteresis voltage	V <sub>H</sub>			4.5	0.6	1.1	1.4	0.6	1.4	V
				6.0	0.8	1.2	1.7	0.8	1.7	
	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		2.0	1.9	2.0	—	1.9	—	
			$I_{OH} = -20 \ \mu A$	4.5	4.4	4.5	—	4.4	—	
High-level output voltage				6.0	5.9	6.0	_	5.9	5.9 —	V
			I <sub>OH</sub> = -6 mA	4.5	4.18	4.31	—	4.13	—	
			I <sub>OH</sub> = -7.8 mA	6.0	5.68	5.80	_	5.63		
	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		2.0	_	0.0	0.1		0.1	
			$I_{OL} = 20 \ \mu A$	4.5	_	0.0	0.1	—	0.1	
Low-level output voltage				6.0	_	0.0	0.1	—	0.1	V
			$I_{OL} = 6 \text{ mA}$	4.5	_	0.17	0.26	—	0.33	
			$I_{OL} = 7.8 \text{ mA}$	6.0	_	0.18	0.26	—	0.33	
3-state output off-state current	I <sub>OZ</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = V_{CC} \text{ or } GND$		6.0	—	—	±0.5	—	±5.0	μA
Input leakage current	I <sub>IN</sub>	$V_{IN} = V_{CC}$ or GND		6.0			±0.1		±1.0	μΑ
Quiescent supply current	ICC	$V_{IN} = V_{CC}$ or GND		6.0		_	4.0	_	40.0	μΑ

#### AC Characteristics (input: $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
	- ,		CL (pF)	$V_{CC}\left(V\right)$	Min	Тур.	Max	Min	Max	
	t			2.0	_	25	60	_	75	
Output transition time	t <sub>TLH</sub>		50	4.5	_	7	12	_	15	ns
	t <sub>THL</sub>			6.0		6	10		13	
			50	2.0	_	50	125	_	155	
				4.5	_	15	25	_	31	- ns
Propagation delay	t <sub>pLH</sub>	_		6.0		13	21		26	
time	t <sub>pHL</sub>			2.0	_	67	165	_	205	
			150	4.5	_	20	33	_	41	
				6.0	_	17	28	_	35	
	<sup>t</sup> pZL t <sub>p</sub> ZH	R <sub>L</sub> = 1 kΩ	50	2.0	_	68	150	_	190	- ns
				4.5	—	21	30	—	38	
Output enable time				6.0	_	16	26	_	32	
ouput chubic line			150	2.0	_	84	165	_	230	
				4.5	_	26	37	_	46	
				6.0	_	20	31	_	39	
	t		kΩ 50	2.0	_	48	150	_	190	
Output disable time	t <sub>pLZ</sub> t <sub>pHZ</sub>	$R_L = 1 \ k\Omega$		4.5	_	21	30	_	38	ns
	чрн∠			6.0	_	19	26	_	32	
Input capacitance	C <sub>IN</sub>		_		_	5	10	_	10	pF
Output capacitance	C <sub>OUT</sub>		-			10	_			pF
Power dissipation	C <sub>PD</sub>	TC74HC7240A				33			_	рF
capacitance	(Note)	TC74HC7244A			_	34	—		_	Pi

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

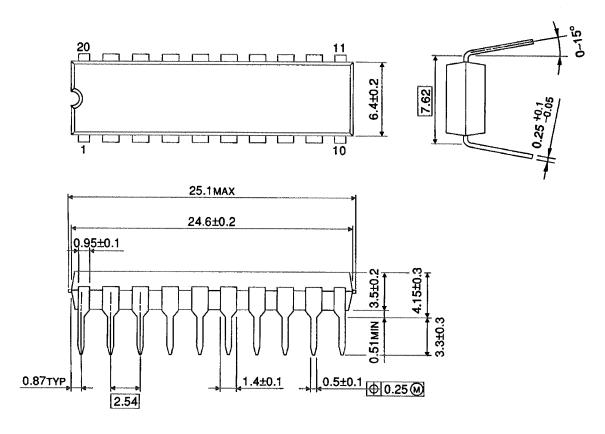
 $I_{CC} \text{ (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC} / 8 \text{ (per bit)}$ 

# **TOSHIBA**

#### **Package Dimensions**

DIP20-P-300-2.54A

Unit : mm



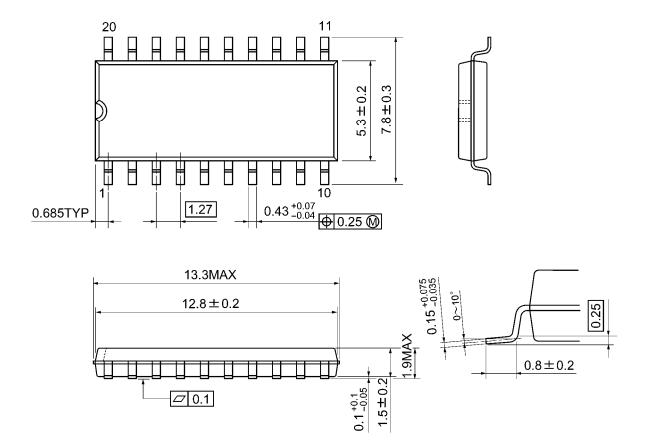
Weight: 1.30 g (typ.)



#### **Package Dimensions**

SOP20-P-300-1.27A

Unit: mm



Weight: 0.22 g (typ.)

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