Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74HCT241

Octal Buffers/Line Drivers/Line Receivers (with inverted 3-state outputs)

REJ03D0663-0200 (Previous ADE-205-551) Rev.2.00 Mar 30, 2006

Description

The HD74HCT241 is a noninverting buffer and has one active low enable and one active high enable. Each enable independently controls 4 buffers.

This device does not have schmitt trigger inputs.

Features

• LSTTL Output Logic Level Compatibility as well as CMOS Output Compatibility

• High Speed Operation: t_{pd} (A to Y) = 10 ns typ ($C_L = 50 \text{ pF}$)

• High Output Current: Fanout of 15 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 4.5$ to 5.5 V

• Low Input Current: 1 µA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HCT241P	DILP-20 pin	PRDP0020AC-B (DP-20NEV)	Р	_
HD74HCT241FPEL	SOP-20 pin (JEITA)	PRSP <mark>002</mark> 0DD-B (FP-20DAV)	FP	EL (2,000 pcs/reel)
HD74HCT241RPEL	SOP-20 pin (JEDEC)	PRSP0020DC-A (FP-20DBV)	RP	EL (1,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

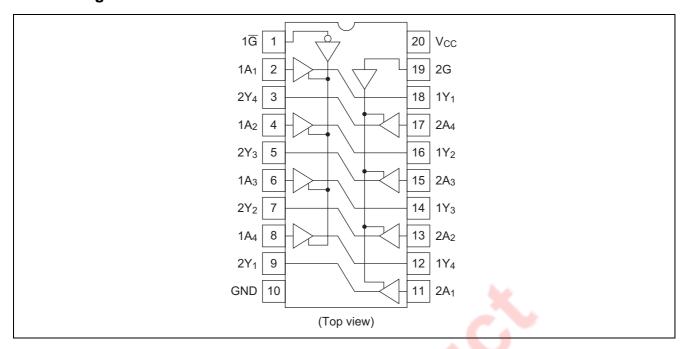
Function Table

	Inputs					
1 <u>G</u>	2G	Α	Y			
Н	L	X	Z			
L	Н	Н	Н			
L	Н	L	L			

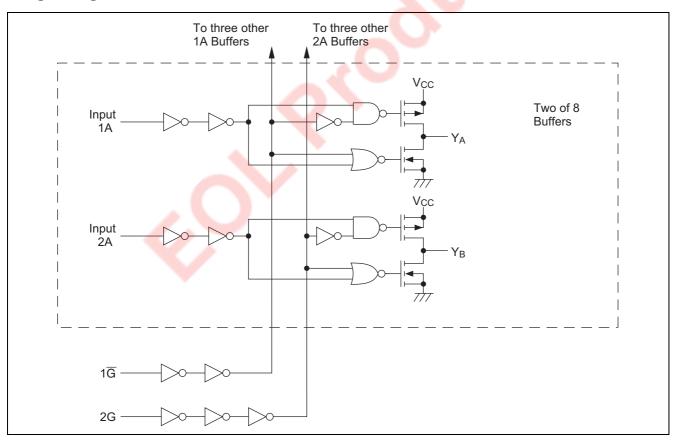
H: high levelL: low levelX: irrelevant

Z : off (high-impedance) state of a 3-state output

Pin Arrangement



Logic Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
Input / Output voltage	V _{IN} , V _{OUT}	–0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IK} , I _{OK}	±20	mA
Output current	I ₀	±35	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±75	mA
Power dissipation	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V_{CC}	4.5 to 5.5	V	
Input / Output voltage	V_{IN}, V_{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
Input rise / fall time*1	t _r , t _f	0 to 500	ns	V _{CC} = 4.5 V

Notes: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.

Electrical Characteristics

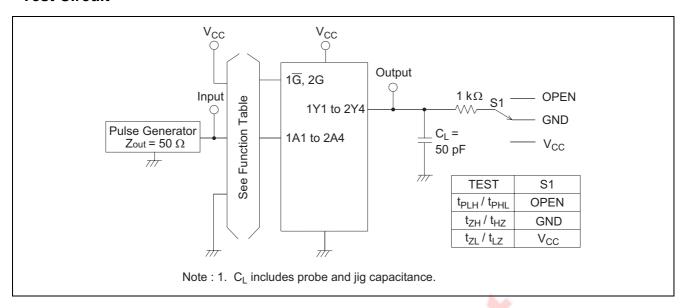
Item	Symbol	V (\(\)	Ta = 25°C			$Ta = -40 \text{ to} + 85^{\circ}\text{C}$		Unit	Test Conditions	
item	Symbol	V _{CC} (V)	Min	n Typ Max Min Max		Oilit				
Input voltage	V_{IH}	4.5 to 5.5	2.0	_	-1	2.0	_	V		
	V_{IL}	4.5 to 5.5	_	-//	0.8	_	0.8	V		
Output voltage	V_{OH}	4.5	4.4		}	4.4	_	V	Vin = V_{IH} or V_{IL} I_{OH} = $-20 \mu A$	
		4.5	4.18	-	_	4.13	_		$I_{OH} = -6 \text{ mA}$	
	V _{OL}	4.5	_	_	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL} _{OL} = 20 \mu A$	
		4.5			0.26	_	0.33		$I_{OL} = 6 \text{ mA}$	
Off-state output	l _{OZ}	5.5	1	<u> </u>	±0.5	_	±5.0	μΑ	$Vin = V_{IH} or V_{IL},$	
current									Vout = V _{CC} or GND	
Input current	lin	5.5	_	_	±0.1	_	±1.0	μΑ	Vin = V _{CC} or GND	
Quiescent current	Icc	5.5		_	4.0	_	40	μΑ	Vin = V_{CC} or GND, lout = $0 \mu A$	

Switching Characteristics

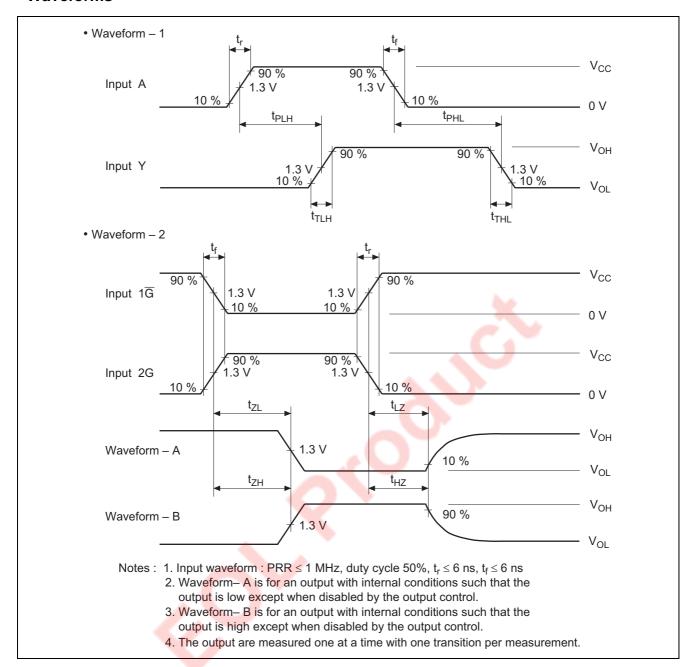
 $(C_L = 50 \text{ pF}, \text{Input } t_r = t_f = 6 \text{ ns})$

Item	Symbol V _{cc} (V)		Ta = 25°C			Ta = -40 to +85°C		Unit	Test Conditions
item	Symbol	ACC (A)	Min	Тур	Max	Min	Max	Oilit	rest Conditions
Propagation delay time	t _{PHL}	4.5	_	11	20	_	25	ns	
	t _{PLH}	4.5	_	9	20	_	25		
Output enable time	t_{ZL}	4.5	_	14	30	_	38	ns	
	t _{zH}	4.5	_	12	30	_	38		
Output disable time	t_{LZ}	4.5	_	13	30	_	38	ns	
	t _{HZ}	4.5	_	17	30	_	38		
Output rise/fall time	t _{TLH}	4.5	_	4	12	_	15	ns	
	t _{THL}								
Input capacitance	Cin	_		5	10	_	10	pF	

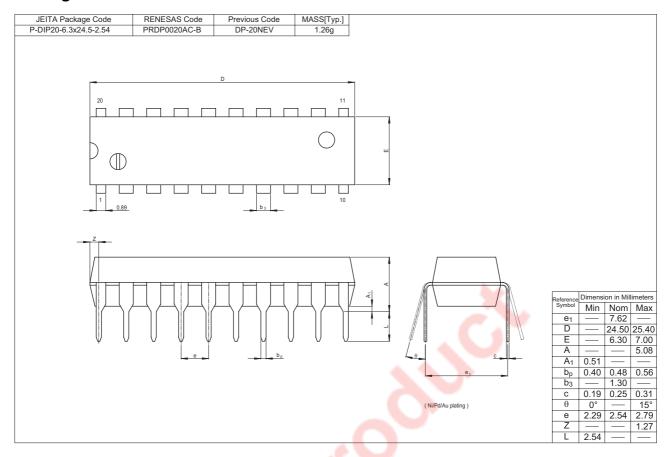
Test Circuit

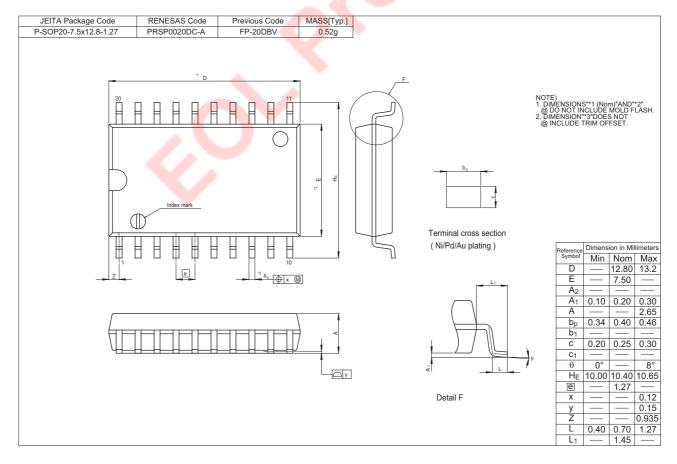


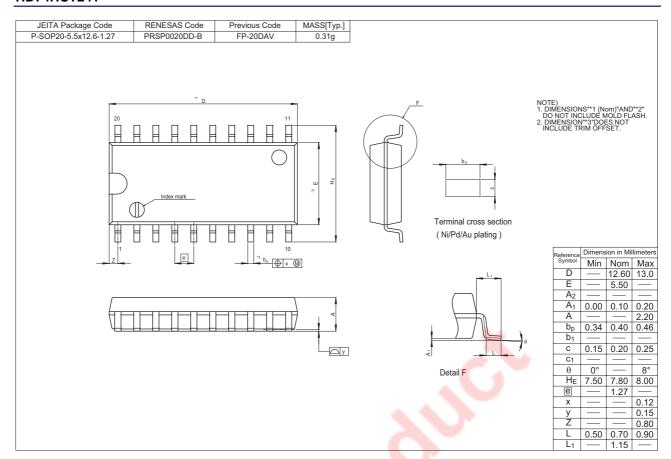
Waveforms



Package Dimensions







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