TOSHIBA Field Effect Transistor Silicon P Channel MOS Type ( $L^2-\pi$ -MOSV)

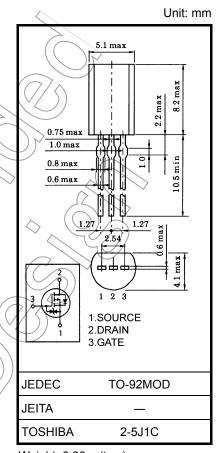
# 2SJ507

# Chopper Regulator, DC–DC Converter and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON resistance  $: R_{DS}(ON) = 0.5 \Omega$  (typ.)
- High forward transfer admittance  $|Y_{fs}| = 1.0 \text{ S (typ.)}$
- Low leakage current  $: I_{DSS} = -100 \ \mu A \ (max) \ (V_{DS} = -60 \ V)$
- Enhancement mode :  $V_{th} = -0.8$  to -2.0 V ( $V_{DS} = -10$  V,  $I_D = -1$  mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	-60	X	
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		V <sub>DGR</sub>	-60	> v	
Gate-source voltage		V <sub>GSS</sub>	±20	V	
Drain current	DC (Note 1)	Ι <sub>D</sub>	-1 V	A	_
	Pulse (Note 1)	I <sub>DP</sub>	-3	/ A	
Drain power dissipation	ı	PD	0.9	VV	
Single pulse avalanche energy (Note 2)		EAS	249.6	mJ	$\rangle \rangle$
Avalanche current			-1	A	
Repetitive avalanche e	nergy (Note 3)	EAR	0.09	Cm	
Channel temperature		T <sub>ch</sub>	150	⊃°C	
Storage temperature ra	inge	T <sub>stg</sub>	-55 to 150	°C	



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

## **Thermal Characteristics**

	$( \land \lor \lor$		
Characteristics	Symbol	Мах	Unit
Thermal resistance, channel to	Rth (ch−a)	138	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = -25 V,  $T_{ch}$  = 25°C (initial), L = 339 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = -1 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

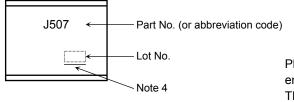
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	—	±10	μA	
Drain cut-off cu	rrent	IDSS	V <sub>DS</sub> = -60 V, V <sub>GS</sub> = 0 V		—	-100	μA	
Drain−source br voltage	reakdown	V (BR) DSS	$I_{D}$ = -10 mA, V <sub>GS</sub> = 0 V	-60	_	_	V	
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -1 mA	(-0.8	4	-2.0	V	
Drain-source ON resistance		R <sub>DS (ON)</sub>	V <sub>GS</sub> = -4 V, I <sub>D</sub> = -0.5 A	X	0.72	1.0	Ω	
			$V_{GS} = -10 V, I_D = -0.5 A$	0.5 0.7			Ω	
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -0.5 A	_0.5	1.0	_	S	
Input capacitance	ce	C <sub>iss</sub>		> _	170	_		
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	25	_	pF	
Output capacitance		Coss	$\leq \langle \rangle$	_	72	$\rightarrow$		
Switching time	Rise time	tr	$V_{GS}_{-10V} \downarrow I_{D} = 0.5A$ $V_{GS}_{-10V} \downarrow I_{D} = 0.5A$ $V_{OUT} \downarrow V_{OUT}$ $R_{L} = 60\Omega$		20	> —	- ns	
	Turn-on time	t <sub>on</sub>			35	) _		
	Fall time	t <sub>f</sub>		$\bigcirc$	30	_		
	Turn-off time	t <sub>off</sub>	$V_{DD} = -30V$ Duty $\leq 1\%$ , $t_w = 10\mu s$	) _	135	_		
Total gate char plus gate-drain)	rge (Gate-source )	Qg	V <sub>DD</sub> <del>~</del> +48 V, V <sub>GS</sub> = -10 V;		5.6	_		
Gate-source charge		Q <sub>gs</sub>	VDD - 1A	—	3.9	—	nC	
Gate-drain ("miller") charge		Qgd		_	1.7			

# Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)			l	l	-1	А
Pulse drain reverse current (Note 1)	V I <sub>DRP</sub>	-			-3	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = -1 Å, V <sub>GS</sub> = 0 V			1.5	V
Reverse recovery time	trr	I <sub>DR</sub> = −1 A, V <sub>GS</sub> = 0 V		58		ns
Reverse recovery charge	Qrr	di <sub>DR</sub> / dt = 50 A / µs		72.5		nC

#### Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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