

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceed the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-35835
 - · Class Q Military
 - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)
- Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.



54LS03/DM54LS03/DM74LS03 Quad 2-Input NAND Gates with Open-Collector Outputs

General Description

This device contains four independent gates each of which performs the logic NAND function. The open-collector outputs require external pull-up resistors for proper logical operation.

Features

 Alternate Military/Aerospace device (54LS03) is available. Contact a National Semiconductor Sales Office/ Distributor for specifications.

Pull-Up Resistor Equations

$$R_{MAX} = \frac{V_{CC} (Min) - V_{OH}}{N_1 (I_{OH}) + N_2 (I_{IH})}$$

$$R_{MIN} = \frac{V_{CC} (Max) - V_{OL}}{I_{OL} - N_3 (I_{IL})}$$

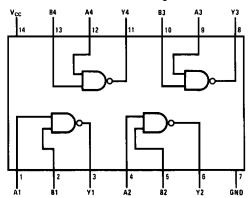
Where: N_1 (I_{OH}) = total maximum output high current for all outputs tied to pull-up resistor

 $N_2 \; (l_{|H}) = total \; maximum \; input high current for all inputs tied to pull-up resistor$

 $N_3 \ (I_{|L}) =$ total maximum input low current for all inputs tied to pull-up resistor

Connection Diagram

Dual-in-Line Package



TL/F/6344-1

Order Number 54LS03DMQB, 54LS03FMQB, 54LS03LMQB, DM54LS03J, DM54LS03W, DM74LS03M or DM74LS03N See NS Package Number E20A, J14A, M14A, N14A or W14B

Function Table

$$Y = \overline{AB}$$

Inputs		Output		
Α	в ү			
L	L	н		
L	н	н		
Н	L	н		
Н	Н	L		

H = High Logic Level

L = Low Logic Level

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V
Input Voltage 7V
Output Voltage 7V

Operating Free Air Temperature Range

Storage Temperature Range -65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54LS03			DM74LS03			Units
		Min	Nom	Max	Min	Nom	Max	Oille
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	٧
V _{IH}	High Level Input Voltage	2			2			٧
V _{IL}	Low Level Input Voltage			0.7			0.8	٧
V _{OH}	High Level Output Voltage			5.5			5.5	٧
lor	Low Level Output Current			4			8	mA
TA	Free Air Operating Temperature	-55		125	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max - 1.5	Units
VI	Input Clamp Voltage V _{CC} = Min, I ₁ = -18 mA						
ICEX	High Level Output Current	$V_{CC} = Min, V_O = 5.5V,$ $V_{IL} = Max$				100	μА
V _{OL} Low Level Output Voltage	Low Level Output	V _{CC} = Min, I _{OL} = Max,	DM54		0.25	0.4	
	V _{IH} = Min	DM74		0.35	0.5	\ \ \	
		I _{OL} = 4 mA, V _{CC} = Min	DM74		0.25	0.4]
lį	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 7V				0.1	mA
liH	High Level Input Current	V _{CC} = Max, V _I = 2.7V				20	μΑ
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.4V				-0.36	mA
Іссн	Supply Current with Outputs High	V _{CC} = Max			0.8	1.6	mA
ICCL	Supply Current with Outputs Low	V _{CC} = Max			2.4	4.4	mA

Switching Characteristics at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$ (See Section 1 for Test Waveforms and Output Load)

Symbol						
	Parameter	C _L =	15 pF	C _L =	Units	
		Min	Max	Min	Max	
^t PLH	Propagation Delay Time Low to High Level Output	6	20	20	45	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	3	15	4	20	ns

Note 1: All typicals are at $V_{CC} = 5V$, $T_A = 25$ °C.