

T. 23.05

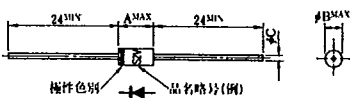
**シリコン整流素子 Silicon Rectifier Diodes**

品名	色別	Io (A)	VRM (V)	IFSM (A)	Vf (max.) (V)	Ir (max.) (μA)	外形図			
DIR20	赤	0.35	200	10	1.65	5	1			
DIR60	青		600							
DIR80	緑		800							
DIR100	銀		1000							
DIR150	金	1500	30	1.05	5	4				
DIR20Z	赤	200								
DIR40Z	黄	400								
DIV20	赤	200								
DIV40	黄	400	30	1.1	10	1				
DIV60	青	600								
S2V10	茶	100								
S2V20	赤	200								
S2V40	黄	400	60	1.05	10	1				
S2V60	青	600								
S3V10	茶	100								
S3V20	赤	200								
S3V40	黄	400	120	1.05	10	1				
S3V60	青	600								
S3V60Z	青	600					120	1.05	10	4
S3V60Z	青	600								

**シリコン整流スタック・ブリッジ Bridge type**

品名	Io (A)	VRM (V)	IFSM (A)	Vf (max.) (V)	Ir (max.) (μA)	外形図				
SIYB10	0.4	100	30	1.05	10	2				
SIYB20		200								
SIYB40		400								
SIYB60		600								
SIWB(A) 10	1	100	30	1.05	10	3				
SIWB(A) 20		200								
SIWB(A) 40		400								
SIWB(A) 60		600								
SIVB10	1	100	30	1.05	10	5				
SIVB20		200								
SIVB40		400								
SIVB60		600								
SIVB20Z	1	200	30	1.05	10	5				
SIVB40Z		400								
SIVB60Z		600								
SIRBA10		100								
SIRBA20	200	30	1.05	10	6					
SIRBA40	400									
SIRBA60	600									
SIRBA80	800									
SIRBA20Z	1	200	30	1.05	10	6				
SIRBA40Z		400								
S2VB10		100					40	1.05	10	7
S2VB20		200								
S2VB40	400									
S2VB60	600									
S3WB10	2.3	100	120	1.05	10	8				
S3WB20		200								
S3WB40		400								
S3WB60		600								
S4VB10	4	100	80	1.05	10	9				
S4VB20		200								
S4VB40		400								
S4VB60		600								
D5SB10	5	100	170	1.05	10	10				
D5SB20		200								
D5SB40		400								
D5SB60		600								
S5VB10	6	100	200	1.05	10	11				
S5VB20		200								
S5VB40		400								
S5VB60		600								

図1



形名	A	B	C
DIR DIV DIK	5.5	2.7	0.6
S1SM	5.5	2.7	0.8
S2V K1V S2K	7.5	4.8	1
S3V S3SM S3K	7.5	4.8	1.4

図2

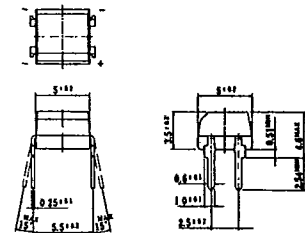


図3

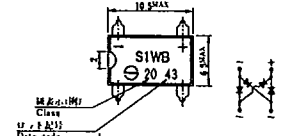
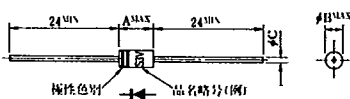


図4



形名	A	B	C
DIRZ	5.5	2.7	0.6
S3V60Z	7.5	4.8	1.4

図5

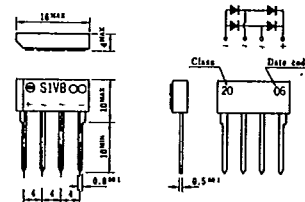


図6

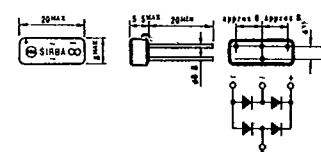


図7

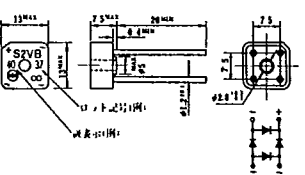


図8

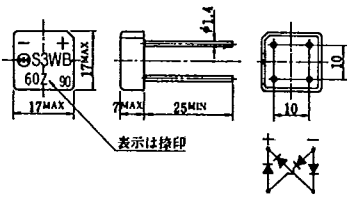


図9

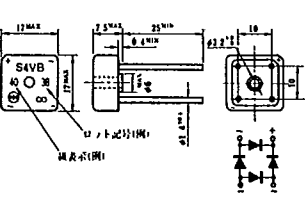


図10

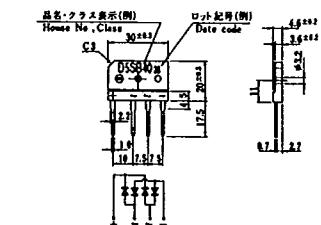


図11

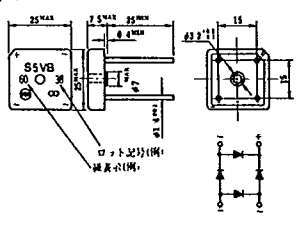


図12

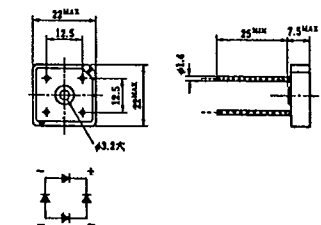


図13

