General Purpose Transistors NPN Silicon

This transistor is designed for general purpose amplifier applications. It is housed in the SOT-416/SC-75 package which is designed for low power surface mount applications.

Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant*



Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	40	Vdc
Collector – Base Voltage	V _{CBO}	60	Vdc
Emitter – Base Voltage	V _{EBO}	6.0	Vdc
Collector Current – Continuous	Ι _C	200	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR–4 Board (Note 1) @T _A = 25°C Derated above 25°C	P _D	200 1.6	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	R_{\thetaJA}	600	°C/W
Total Device Dissipation, FR–4 Board (Note 2) @T _A = 25°C Derated above 25°C	P _D	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	R_{\thetaJA}	400	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-4 @ Minimum Pad

2. FR-4 @ 1.0×1.0 Inch Pad



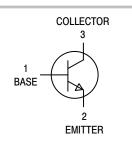
ON Semiconductor®

www.onsemi.com

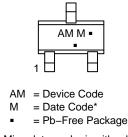
GENERAL PURPOSE AMPLIFIER TRANSISTORS SURFACE MOUNT



SOT-416/SC-75 CASE 463 STYLE 1



MARKING DIAGRAM



(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

	Device	Package	Shipping [†]
Ν	MMBT3904TT1G	SOT-416 (Pb-Free)	3,000 Tape & Reel
S	SMMBT3904TT1G	SOT-416 (Pb-Free)	3,000 Tape & Reel

I details, please ng Techniques †For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

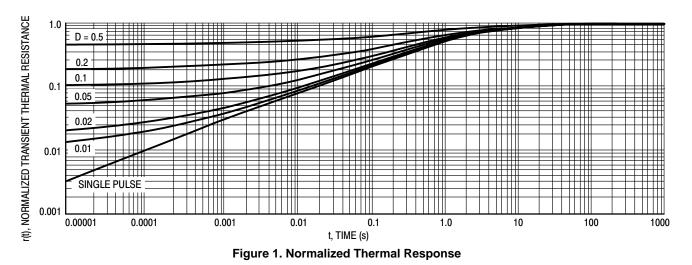
*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

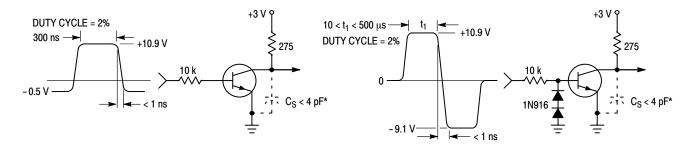
ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage (Note 3) ($I_C = 1.0 \text{ mAdc}, I_B = 0$)	V _{(BR)CEO}	40	_	Vdc
Collector – Base Breakdown Voltage $(I_C = 10 \ \mu Adc, I_E = 0)$	V _{(BR)CBO}	60	-	Vdc
Emitter – Base Breakdown Voltage $(I_E = 10 \ \mu Adc, I_C = 0)$	V _{(BR)EBO}	6.0	_	Vdc
Base Cutoff Current (V _{CE} = 30 Vdc, V _{EB} = 3.0 Vdc)	I _{BL}	_	50	nAdc
Collector Cutoff Current (V _{CE} = 30 Vdc, V _{EB} = 3.0 Vdc)	I _{CEX}	_	50	nAdc
ON CHARACTERISTICS (Note 3)				
DC Current Gain ($I_C = 0.1 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$) ($I_C = 1.0 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$) ($I_C = 10 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$) ($I_C = 50 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$) ($I_C = 100 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc}$)	h _{FE}	40 70 100 60 30	 300 	-
Collector – Emitter Saturation Voltage ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}$) ($I_C = 50 \text{ mAdc}, I_B = 5.0 \text{ mAdc}$)	V _{CE(sat)}	- -	0.2 0.3	Vdc
Base – Emitter Saturation Voltage ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}$) ($I_C = 50 \text{ mAdc}, I_B = 5.0 \text{ mAdc}$)	V _{BE(sat)}	0.65 -	0.85 0.95	Vdc
SMALL-SIGNAL CHARACTERISTICS				•
Current-Gain – Bandwidth Product (I _C = 10 mAdc, V _{CE} = 20 Vdc, f = 100 MHz)	f _T	300	-	MHz
Output Capacitance $(V_{CB} = 5.0 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C _{obo}	_	4.0	pF
Input Capacitance ($V_{EB} = 0.5 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz}$)	C _{ibo}	-	8.0	pF
Input Impedance (V_{CE} = 10 Vdc, I _C = 1.0 mAdc, f = 1.0 kHz)	h _{ie}	1.0	10	kΩ
Voltage Feedback Ratio (V _{CE} = 10 Vdc, I _C = 1.0 mAdc, f = 1.0 kHz)	h _{re}	0.5	8.0	X 10 ⁻⁴
Small-Signal Current Gain (V_{CE} = 10 Vdc, I _C = 1.0 mAdc, f = 1.0 kHz)	h _{fe}	100	400	-
Output Admittance (V_{CE} = 10 Vdc, I _C = 1.0 mAdc, f = 1.0 kHz)	h _{oe}	1.0	40	μmhos
Noise Figure (V _{CE} = 5.0 Vdc, I _C = 100 μ Adc, R _S = 1.0 k Ω , f = 1.0 kHz)	NF	_	5.0	dB
SWITCHING CHARACTERISTICS				
Delay Time (V _{CC} = 3.0 Vdc, V _{BE} = -0.5 Vdc) MMBT3904TT1G, SMMBT3904TT1G	t _d	_	35	

Delay Time	(V _{CC} = 3.0 Vdc, V _{BE} = -0.5 Vdc) MMBT3904TT1G, SMMBT3904TT1G	t _d	-	35		1
Rise Time	(I _C = 10 mAdc, I _{B1} = 1.0 mAdc) MMBT3904TT1G, SMMBT3904TT1G	t _r	_	35	ns	
Storage Time	(V _{CC} = 3.0 Vdc, I _C = 10 mAdc) MMBT3904TT1G, SMMBT3904TT1G	t _s	_	200	115	
Fall Time	(I _{B1} = I _{B2} = 1.0 mAdc) MMBT3904TT1G, SMMBT3904TT1G	t _f	-	50		

3. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

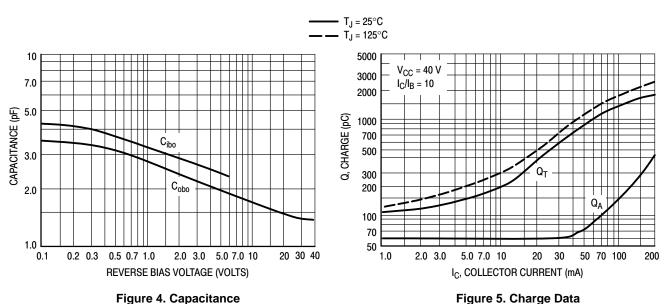




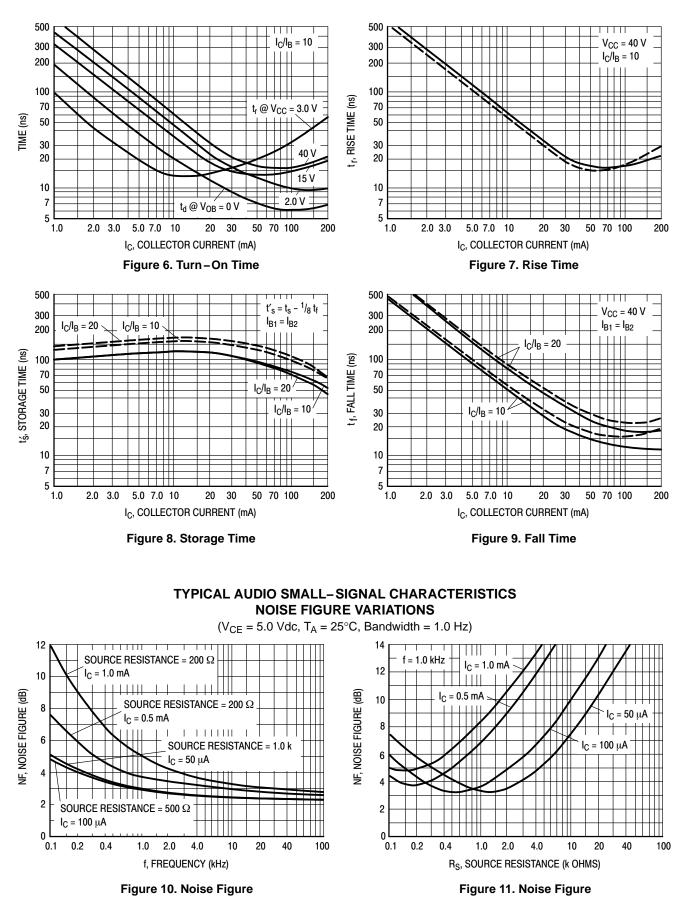
* Total shunt capacitance of test jig and connectors

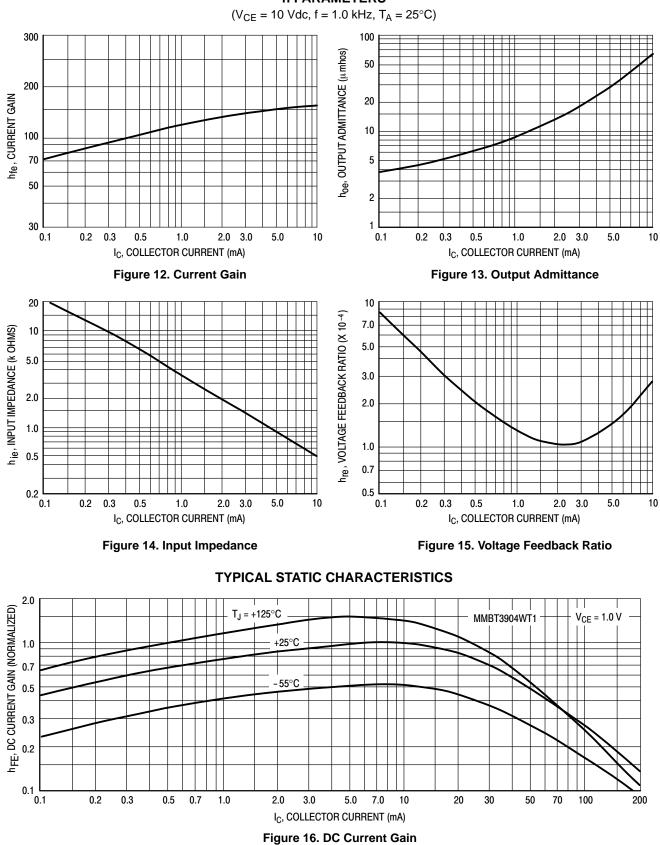
Figure 2. Delay and Rise Time **Equivalent Test Circuit**

Figure 3. Storage and Fall Time **Equivalent Test Circuit**

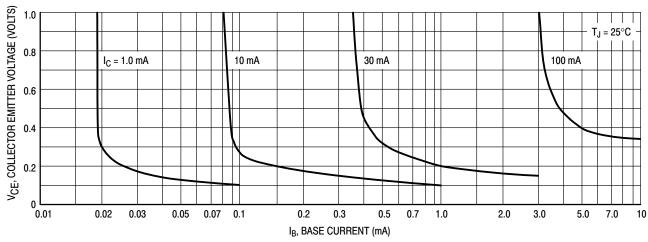


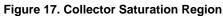
TYPICAL TRANSIENT CHARACTERISTICS

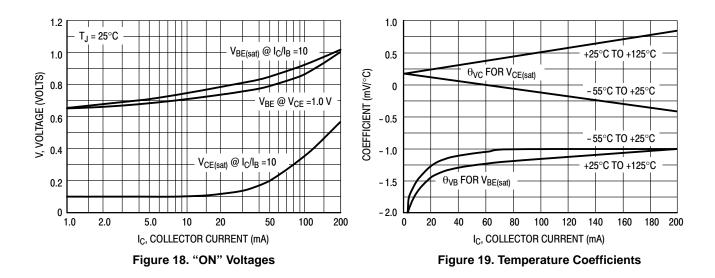




h PARAMETERS







MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

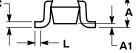
DUSEM

DATE 07 AUG 2015



ISSUE G SCALE 4:1 -E--De ¥ b 3 PL 🕀 0.20 (0.008) 🛞 D H_{E} 0.20 (0.008) E _ С

SC-75/SOT-416 **CASE 463**



STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR



3. CATHODE

STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE

STYLE 5: PIN 1. GATE 2 SOURCE 3. DRAIN

GENERIC **MARKING DIAGRAM***

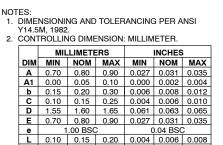


XX = Specific Device Code

Μ = Date Code

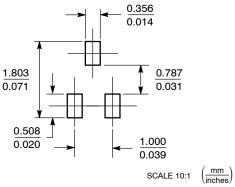
= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



H_E 1.50 1.60 1.70 0.060 0.063 0.067

RECOMMENDED **SOLDERING FOOTPRINT***



*For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	SC-75/SOT-416		PAGE 1 OF 1		

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