

KSD5707

High Voltage Color Display Horizontal Deflection Output

• High Collector - Base Voltage : V_{CBO} = 1500V • High Speed Switching t_F = 0.4 μ s (Max.)



NPN Triple Diffused Planar Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	1500	V
V_{CEO}	Collector-Emitter Voltage	800	V
V _{EBO}	Emitter-Base Voltage	6	V
I _C	Collector Current (DC)	6	Α
I _{CP}	Collector Current (Pulse)	16	Α
P _C	Collector Dissipation (T _C =25°C)	60	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CBO}	Collector Cut-off Current	$V_{CB} = 800V, I_{E} = 0$			10	μΑ
I _{EBO}	Emitter Cut-off Current	V _{EB} = 5V, I _C = 0			1	mA
h _{FE1}	DC Current Gain	V _{CE} = 5V, I _C = 1A	10		30	
h_{FE2}		$V_{CE} = 5V$, $I_{C} = 3A$	5		15	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 4A, I _B = 0.8A		2	5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 4A, I _B = 0.8A			1.5	V
f _T	Current Gain Bandwidth Product	V _{CE} = 10V, I _C = 1A		3		MHz
t _F	Fall Time	V _{CC} =200V, I _C =4A,			0.4	μs
		I _{B1} = 0.8A, I _{B2} = -1.6A				
		$R_L=50\Omega$				

Thermal Characteristics

Symbol	Char	Rating	Unit	
$R_{\theta jc}$	Thermal Resistance	Junction to Case	2.08	°C/W

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Typical Characteristics

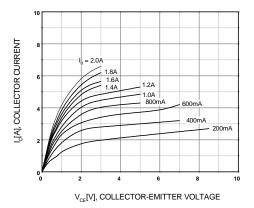


Figure 1. Static Characteristic

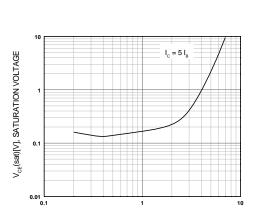


Figure 3. Collector-Emitter Saturation Voltage

 $I_{\rm c}[{\rm A}]$, COLLECTOR CURRENT

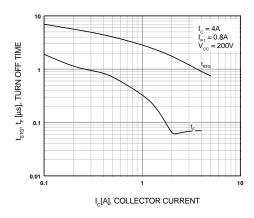


Figure 5. Switching Time

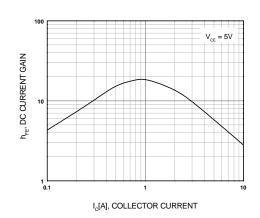


Figure 2. DC current Gain

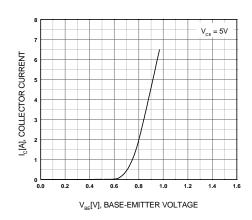


Figure 4. Base-Emitter On Voltage

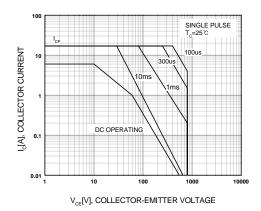


Figure 6. Safe Operating Area

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Typical Characteristics (Continued)

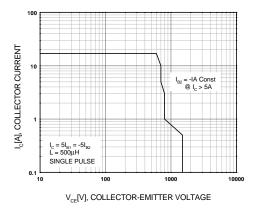


Figure 7. Reverse Bias Safe Operating Area

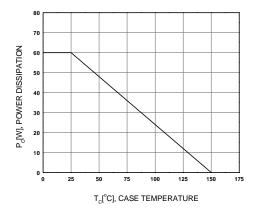
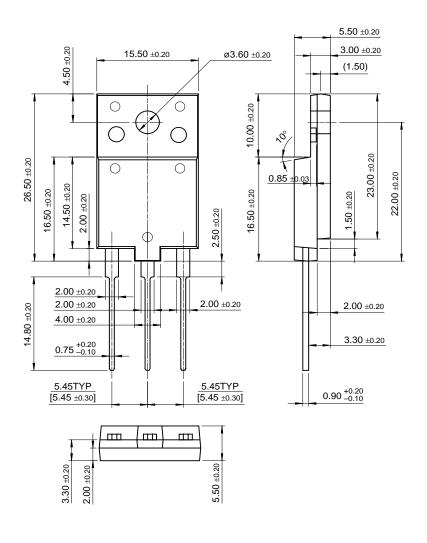


Figure 8. Power Derating

Package Dimensions

TO-3PF



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