

T-46-13-29

Advance Information

# Am27C020

2 MBit (256K x 8) CMOS EPROM



Advanced  
Micro  
Devices

## DISTINCTIVE CHARACTERISTICS

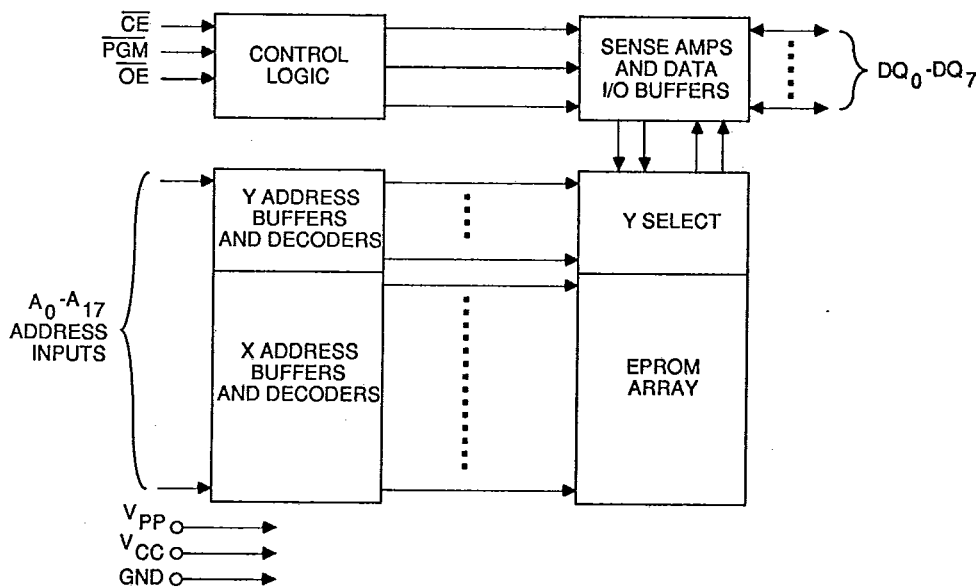
- High-performance CMOS technology
- Fast access time
  - 100 ns
- Low power dissipation
  - 100  $\mu$ A maximum standby current
- Easy upgrade from 28-pin JEDEC EPROMs
- Compact 32-pin DIP package requires no hardware change for upgrades to 8 Mbit
- Flashrite™ programming
- Ceramic DIP and LCC packages
- Latch-up protected to 100 mA from -1 V to  $V_{cc} + 1$  V

## GENERAL DESCRIPTION

The Am27C020 is a 2,097,152-bit ultraviolet-light-erasable, programmable read-only memory (UV EPROM) device organized as 256K bytes of 8 bits each. Its pin compatibility with byte-wide JEDEC EPROMs allows easy upgrades from 512K through 8-Mbit densities. AMD's CMOS process technology provides high-speed and low power consumption.

The standard Am27C020 offers access times as fast as 150 ns, allowing operation with high-speed microprocessors without any wait states. The Am27C020 also offers separate Chip Enable ( $\overline{CE}$ ) and Output Enable ( $\overline{OE}$ ) controls, which eliminates bus contention in a multiple-bus microprocessor system.

## BLOCK DIAGRAM



11507-001A

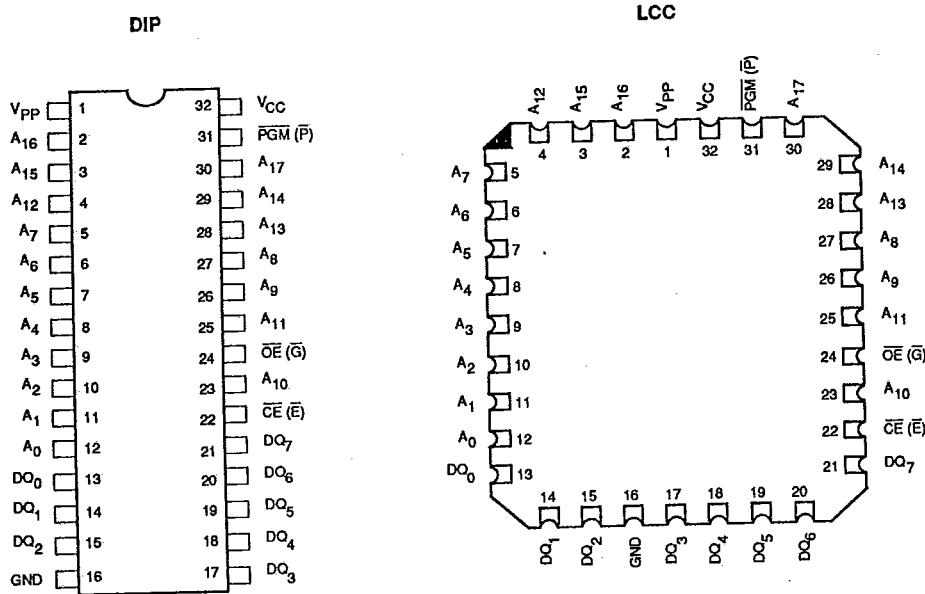
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08140	D	/0
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CONNECTION DIAGRAMS

Top View

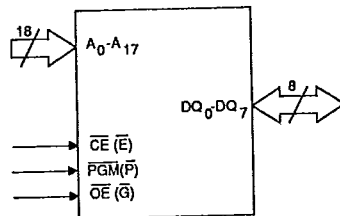


Note: 1. JEDEC nomenclature is in parentheses.  
 2. The 32-Pin DIP to 32-Pin LCC configuration varies from the JEDEC 28-Pin DIP to 32-Pin LCC configuration.

11507-002A



LOGIC SYMBOL



11507-003A

Pin Description

- A<sub>0</sub>-A<sub>17</sub> = Address Inputs
- $\overline{CE}(\overline{E})$  = Chip Enable Input
- DQ<sub>0</sub>-DQ<sub>7</sub> = Data Input/Outputs
- $\overline{OE}(\overline{G})$  = Output Enable Input
- $\overline{PGM}(\overline{P})$  = Program Enable Input
- V<sub>cc</sub> = V<sub>cc</sub> Supply Voltage
- V<sub>pp</sub> = Program Supply Voltage
- GND = Ground