



National Semiconductor™

## Surface Mount Diodes

### Computer Diodes (by Descending $B_V$ )

#### LEADLESS GLASS PACKAGE

Device No.	Package No.	$B_V$ (V) Min	$I_R$ (mA) Max	$V_R$ (V) @	$V_F$ (V) Min Max	$I_F$ (mA) @	C (pF) Max	$t_{rr}$ (ns) Max	Test Conditions	Process Family
FDLL914	LL-34	100	25	20	1 20	10	4	4	(Note 2)	D3
FDLL914A	LL-34	100	25	20	0.62 0.72	5	4	4	(Note 2)	D3
FDLL914B	LL-34	100	25	20	1 1	100	4	4	(Note 2)	D3
FDLL916	LL-34	100	25	20	1 1	10	4	4	(Note 2)	D3
FDLL916A	LL-34	100	25	20	1 1	20	2	4	(Note 2)	D3
FDLL916B	LL-34	100	25	20	1 1	30	2	4	(Note 2)	D3
FDLL4148	LL-34	100	25	20	1 1	10	4	4	(Note 2)	D3
FDLL4448	LL-34	100	25	20	0.63 0.73	5	2	4	(Note 1)	D3
FDLL600	LL-34	75	100	50	0.65 0.79 0.86 0.92 1.0	1 10 50 100 200	2.5	4	(Note 1)	1P
FDLL4150	LL-34	75	100	50	0.87 1	200	2.5	4	(Note 3)	1R
FDLL4151	LL-34	75	50	50	1 1	50	2	4	(Note 1)	D3
FDLL4153	LL-34	75	50	50	0.74 0.88	20	2	2	(Note 2)	D4
FDLL4152	LL-34	40	50	30	0.74 0.88	20	2	2	(Note 2)	D3

#### TEST CONDITIONS

Note 1:  $I_F = I_R = 10$  mA,  $I_{RR} = 1.0$  mA,  $R_L = 100\Omega$ .

Note 2:  $I_F = 10$  mA,  $V_R = 6.0$  V,  $I_{RR} = 1.0$  mA,  $R_L = 100\Omega$ .

Note 3:  $I_F = I_R = 10$  mA to 200 mA,  $I_{RR} = 1.0$  mA,  $R_L = 100\Omega$ .