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Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# SIPOLAR ANALOG INTEGRATED CIRCUIT $\mu$ PC1099

## SWITCHING REGULATOR CONTROL CIRCUIT FOR 500 kHz OPERATION

 $\mu$ PC1099 is a control IC for the high performance switching power supply equipped with high speed/high sensitivity protection circuit. Control ICs for the high performance switching power supply have 3 series of  $\mu$ PC1099, 1905, 1906. The features of  $\mu$ PC1099 are as follows:

Low voltage start up (11 V TYP.) → Suiting for flyback mode converter, DC-DC converter, etc.

### CONTROL IC FAMILY FOR THE HIGH PERFORMANCE SWITCHING POWER SUPPLY

PART NUMBER	SUPPLY VOLTAGE	START-UP THRESHOLD VOLTAGE	THRESHOLD HYSTERESIS	OVER CURRENT LATCH PROTECTION MODE
μPC1099	26 V	11 V	3 V	Pulse by pulse current limiting
μPC1905	31 V	16.5 V	6.5 V	Pulse by pulse current limiting
μPC1906	31 V	16.5 V	6.5 V	Shut down and V <sub>CC</sub> reset

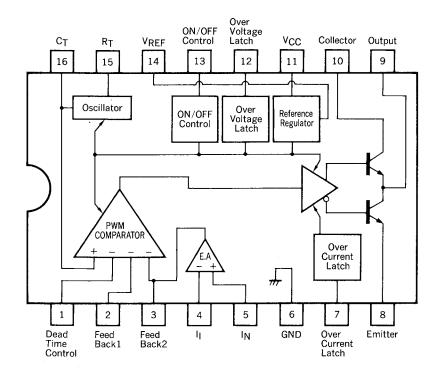
#### **FEATURES**

- Directly drive power MOS FET (totem pole circuit adopted)
- Pulse by pulse over current latch circuit incorporated
- Over voltage latch circuit incorporated
- Under voltage lockout circuit incorporated
- Remote control circuit incorporated
- Error amplifier incorporated

PART NUMBER	PACKAGE	QUALITY GRADE
μPC1099CX	16 pin plastic DIP (300 mil)	Chanadanal
μPC1099GS	16 pin plastic SOP (300 mil)	Standard

Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

### PIN CONNECTION DIAGRAM (Top View)



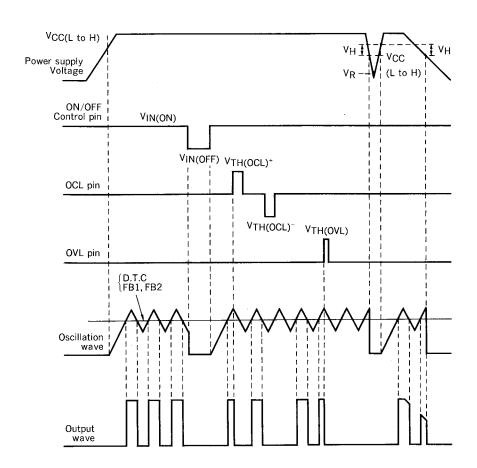
### ABSOLUTE MAXIMUM RATING ( $T_a = 25$ °C)

PARAMETER		SYMBOL	RATING	UNIT
Supply Voltage		Vcc	26	V
Output Voltage		Vc	26	V
Output Current		IC(DC)	100	mA
Peak Output Current		C (peak)	1.2	Α
Total	μPC1099CX	PT (T <sub>a</sub> = 25 °C)	1 000	mW
Power Dissipation		694	mW	
Operation Temperat	Operation Temperature		-20 to +85	°C
Storage Temperature		T <sub>stg</sub>	-55 to +150	°C

### RECOMMENDED OPERATION REQUIREMENTS

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage	Vcc	11.5	15	24	V
Oscillation Frequency	fosc	50	200	500	kHz
Output Load Capacitance	СL	_	2 200	3 000	pF

### **OPERATION WAVES**

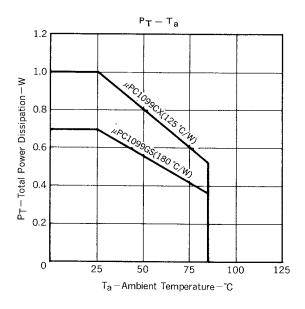


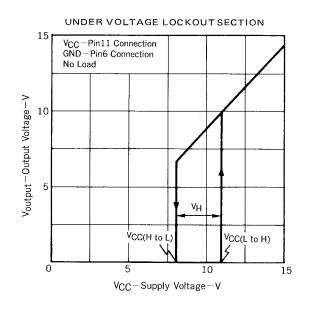
### ELECTRICAL CHARACTERISTICS (T $_{a}$ = 25 $^{\circ}$ C, V $_{CC}$ = 15 V, C $_{T}$ = 470 pF, R $_{T}$ $\leftrightarrows$ 10 k $\Omega$ , f $_{OSC}$ = 200 kHz)

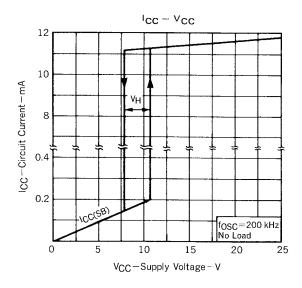
вьоск	PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
	Standby Current	ICC (SB)	0.05	0.1	0.2	mA	$V_{CC} = 8 \text{ V}, -10 ^{\circ}\text{C} \le T_a \le 85 ^{\circ}\text{C}$
tal	Circuit Current at OVL Operation Mode	ICC (OVL)		10		mA	
Total	Circuit Current at Off Mode	¹CC (OFF)		10		mA	
	Circuit Current	Icc		10	15	mA	V <sub>CC</sub> = V <sub>C</sub> = 24 V, V <sub>D</sub> = 2.7 V, no load
Under Voltage Lockout Section	Start-Up Threshold Voltage	VCC (L to H)	10,5	11	11.5	V	
Und Volt Loc Sect	Threshold Hysteresis	V <sub>H</sub>	2.8	3	3.2	٧	
	Output Voltage	V <sub>REF</sub>	4.8	5	5.2	V	I <sub>REF</sub> = 0
Itage	Line Regulation	REGIN		1	10	mV	11.5 V ≤ V <sub>CC</sub> ≤ 20 V, I <sub>REF</sub> = 0
, v	Load Regulation	REGL		6.5	12	mV	0 ≤ I <sub>REF</sub> ≤ 3 mA
Reference Voltage Section	Output Voltage Temperature Coefficient	V <sub>REF/⊿</sub> T		400	700	μV/°C	I <sub>REF</sub> = 0, -10 °C ≤ T <sub>a</sub> ≤ +85 °C
I II S	Short Circuit Current	I <sub>O short</sub>		13		mA	V <sub>REF</sub> = 0
	Input Bias Current	lΒ			10	μΑ	
tion	Low Level Threshold Voltage	V <sub>TH</sub> (L)		1.5		V	
PWM Section	High Level Threshold Voltage	V <sub>TH</sub> (H)		3.5	1.0.3	V	
PWN	Dead Time Temperature Coefficient	⊿DT/⊿T		3		%	$V_D = 0.54 V_{REF},$ -10 °C $\leq$ T <sub>0</sub> $\leq$ +85 °C
JO.	Oscillation Frequency	fosc	180	200	220	kHz	
Oscillator Section	Frequency Line Regulation	Δf/ΔV <sub>CC</sub>		1		%	11.5 V ≤ V <sub>CC</sub> ≤ 24 V
Sec	Frequency Temperature Coefficient	<b>⊿</b> f/ <b>⊿</b> T		2	5	%	-10°C ≤ T <sub>a</sub> ≤ +85°C
no	Low Level Output Voltage	V <sub>OL</sub>			0.5	V	I <sub>SINK</sub> = 3 mA, V <sub>CC</sub> = V <sub>C</sub>
Output Section	High Level Output Voltage	V <sub>OH</sub>		V <sub>CC</sub> 1.6		V	I <sub>SOURCE</sub> = 30 mA, V <sub>CC</sub> = V <sub>C</sub>
tput	Output Voltage Rise Time	t <sub>r</sub>		60		ns	R <sub>L</sub> = 15 Ω, C <sub>L</sub> = 2 200 pF
no	Output Voltage Fall Time	tf		40		ns	VCC = VC
e = c	Input Voltage at Output ON	V <sub>IN</sub> (ON)	2.4	2.6	2.8	V	
Remote Control Section	Input Voltage at Output OFF	VIN(OFF)	2.2	2.4	2.6	V	
ကြီးပဲကို	Hysteresis Width	٧ <sub>H</sub>	0.1	0.2	0.3	V	
e C	Over Voltage Threshold Voltage	V <sub>TH</sub> (OVL)	2.0	2.4	2.8	V	-10 °C ≤ T <sub>a</sub> ≤ +85 °C
Over Voltage Latch Section	Input Bias Current	IB(OVL)			4	μΑ	OVL pin voltage = V <sub>TH</sub> (OVL)
er V	OVL Reset Voltage	V <sub>R</sub> (OVL)		2		V	
Ľ6	Delay to Output	td(OVL)		750		ns	
	Over Current Threshold Voltage	V <sub>TH</sub> (OCL)+	200	220	240	mV	-10 °C ≤ T <sub>a</sub> ≤ +85 °C
ent	Over Current Threshold Voltage	VTH (OCL)	-230	-210	-190	mV	-10 °C ≤ T <sub>a</sub> ≤ +85 °C
Curr Sec	OCL Pin Output Current	IB (OCL)		250		μΑ	
Over Current Latch Section	Delay to Output	td(OCL)+		120		ns	
	Delay to Output	td(OCL)		160		ns	

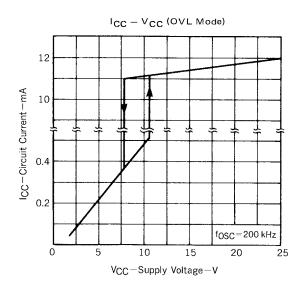
BLOCK	PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
	Input Bias Current	IB (AMP)			1	μΑ	V <sub>IN</sub> = 2.5 V
ection	Open-Loop Voltage Gain	Αυ	60	80		dB	V <sub>FB</sub> = 2.9 V
ဟ	Unit Gain Bandwidth	f <sub>unity</sub>	1	1.6		MHz	
lifier	High Level Output Voltage	V <sub>om</sub> +	3.0			V	
Amp	Low Level Output Voltage	v <sub>om</sub> -			1.0	V	
Error	Common Mode Input Voltage Range	V <sub>ICM</sub> +	3			V	11.5 V ≤ V <sub>CC</sub> ≤ 24 V,
	Common Mode Input Voltage Range	V <sub>ICM</sub> -			-0.3	V	$-10$ °C $\leq$ T <sub>a</sub> $\leq$ +85 °C

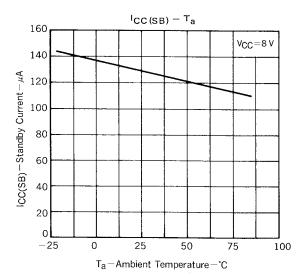
### TYPICAL CHARACTERISTICS ( $T_a = 25$ °C, $V_{CC} = 15$ V)

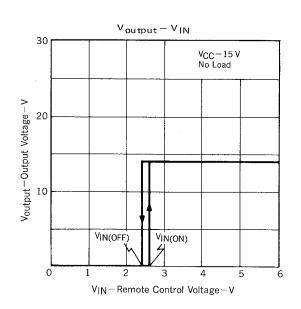


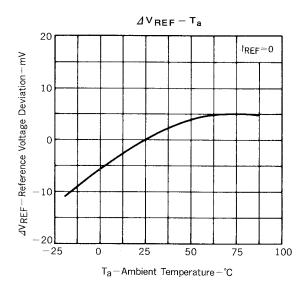


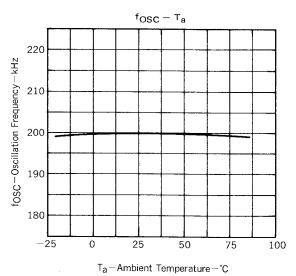


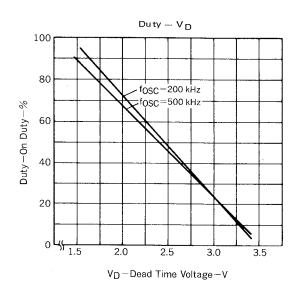


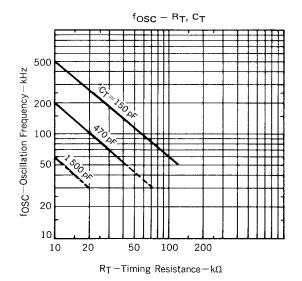


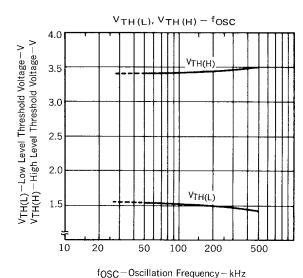


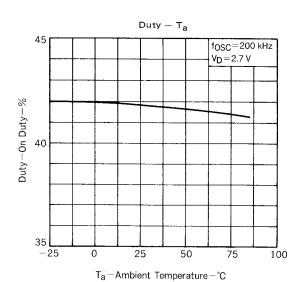


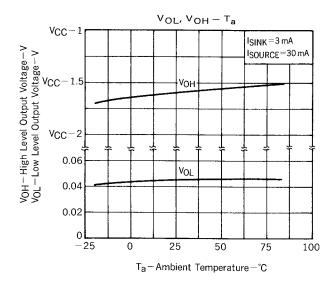


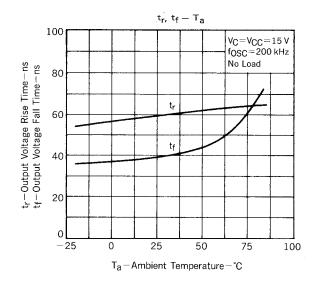


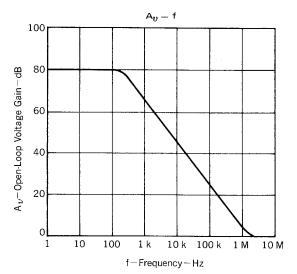




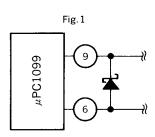






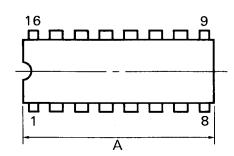


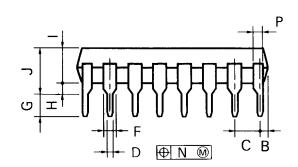
NOTE: When under-shoot voltage at pin 9 occurs, it must be cramped to prevent from wrong operation. See Fig. 1.

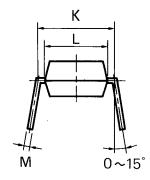


### 16PIN PLASTIC DIP (300 mil)

 $\mu$ PC1099CX







P16C-100-300B

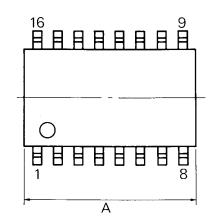
### **NOTES**

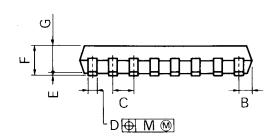
- Each lead centerline is located within 0.25 mm (0.01 inch) of its true position (T.P.) at maximum material condition.
- 2) Item "K" to center of leads when formed parallel.

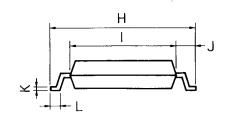
ITEM	MILLIMETERS	INCHES
Α	20.32 MAX.	0.800 MAX.
В	1.27 MAX.	0.050 MAX.
С	2.54 (T.P.)	0.100 (T.P.)
D	0.50 <sup>±0.10</sup>	0.020 + 0.004
F	1.1 MIN.	0.043 MIN.
G	3.5 <sup>±0.3</sup>	0.138 <sup>±0.012</sup>
Н	0.51 MIN.	0.020 MIN.
ı	4.31 MAX.	0.170 MAX.
J	5.08 MAX.	0.200 MAX.
К	7.62 (T.P.)	0.300 (T.P.)
L	6.5	0.256
М	0.25 -0.10	0.010 + 0.004
N	0.25	0.01
Р	1.1 MIN.	0.043 MIN.

### 16PIN PLASTIC SOP (300 mil)

 $\mu$ PC1099GS







### NOTE

Each lead centerline is located within 0.12 mm (0.005 inch) of its true position (T.P.) at maximum material condition.

P16GM-50-300B-1

ITEM	MILLIMETERS	INCHES
Α	10.46 MAX.	0.412 MAX.
В	0.78 MAX.	0.031 MAX.
С	1.27 (T.P.)	0.050 (T.P.)
D	0.40 + 0.10	0.016 -0.003
E	0.1 <sup>±0.1</sup>	0.004 ±0.004
F	1.8 MAX.	0.071 MAX.
G	1.55	0.061
Н	7.7 <sup>±0.3</sup>	0.303 <sup>±0.012</sup>
ı	5.6	0.220
J	1.1	0.043
К	0.20 -0.05	0.008 +0.004
L	0.6 <sup>±0.2</sup>	0.024 + 0.008
М	0.12	0.005

### RECOMMENDED SOLDERING CONDITIONS

The following conditions (see table below) must be set when soldering this product.

Please consult with our sales offices in case other soldering process is used, or in case soldering is done under different conditions.

### TYPES OF SURFACE MOUNT DEVICE

For more details, refer to our document "SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL" (IEI-1207).

 $\mu$ PC1099GS

Soldering process	Soldering conditions	Symbol
Infrared ray reflow	Peak package's surface temperature: 230 °C or below, Reflow time: 30 seconds or below (210 °C or higher), Number of reflow process: 1, Exposure limit*: None	IR30-00-1
VPS	Peak package's surface temperature: 215 °C or below, Reflow time: 40 seconds or below (200 °C or higher), Number of reflow process: 1, Exposure limit*: None	VP15-00-1

<sup>\*:</sup> Exposure limit before soldering after dry-pack package is opened. Storage conditions: 25 °C and relative humidity at 65 % or less.

Note: Do not apply more than a single process at once, except for "Partial heating method".

### TYPES OF THROUGH HOLE MOUNT DEVICE

 $\mu$ PC1099CX

Soldering process	Soldering conditions	Symbol
Wave soldering	Solder temperature: 260 °C or below, Flow time: 10 seconds or below	

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