MOTOROLA

OCTAL BUS TRANSCEIVERS

LOW POWER SCHOTTKY

DESCRIPTION — These octal bus transceivers are designed for asynchronous two-way communication between data buses. Control function implementation minimizes external timing requirements. These circuits allow data transmission from the A bus to B or from the B bus to A bus depending upon the logic level of the direction control (DIR) input. Enable input ($\overline{G}$) can disable the device so that the buses are effectively isolated.

DEVICE	OUTPUT	LOGIC
LS640	3-State	Inverting
LS641	Open-Collector	True
LS642	Open-Collector	Inverting
LS643	3-State	True and Inverting
LS644	Open-Collector	True and Inverting
LS645	3-State	True

FUNCTION TABLE

CONTROL INPUTS		OPERATION		
$\overline{G}$	DIR	LS640 LS642	LS641 LS645	LS643 LS644
L	L	$\overline{B}$ data to A bus	B data to A bus	B data to A bus
L	H	$\overline{A}$ data to B bus	A data to B bus	$\overline{A}$ data to B bus
H	X	Isolation	Isolation	Isolation

H = High level, L = low level, X = irrelevant

CONNECTION DIAGRAMS
(TOP VIEW)
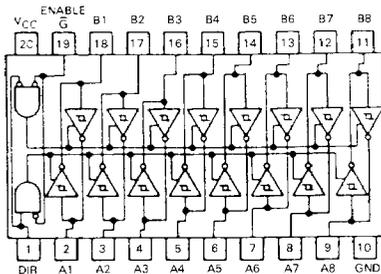
SN54LS/74LS640
SN54LS/74LS642
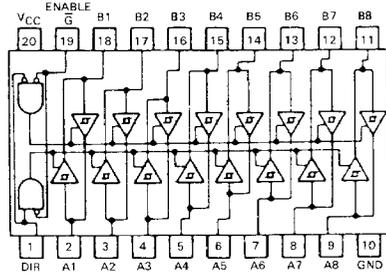
SN54LS/74LS641
SN54LS/74LS645

SN54LS/74LS643
SN54LS/74LS644
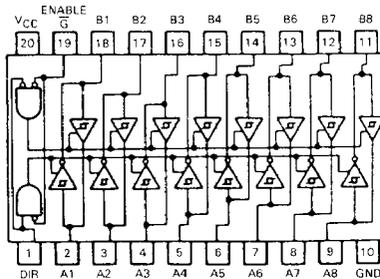
J Suffix — Case 732-03 (Ceramic)
N Suffix — Case 738-01 (Plastic)

GUARANTEED OPERATING RANGES

SYMBOL	PARAMETER		MIN	TYP	MAX	UNIT
V_{CC}	Supply Voltage	54	4.5	5.0	5.5	V
		74	4.75	5.0	5.25	
T_A	Operating Ambient Temperature Range	54	−55	25	125	°C
		74	0	25	70	
I_{OH}	Output Current — High	54,74			−3.0	mA
		54			−12	mA
		74			−15	
I_{OL}	Output Current — Low	54			12	mA
		74			24	

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

SYMBOL	PARAMETER		MIN	TYP	MAX	UNITS	TEST CONDITIONS	
V_{IH}	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage for All Inputs	
V_{IL}	Input LOW Voltage	54			0.5	V	Guaranteed Input LOW Voltage for All Inputs	
		74			0.6			
V_{IK}	Input Clamp Diode Voltage			−0.65	−1.5	V	V_{CC} = MIN, I_{IN} = −18 mA	
V_{OH}	Output HIGH Voltage	54,74	2.4	3.4		V	V_{CC} = MIN, I_{OH} = −3.0 mA	
		54,74	2.0			V	V_{CC} = MIN, I_{OH} = MAX	
V_{OL}	Output LOW Voltage	54,74		0.25	0.4	V	I_{OL} = 12 mA	V_{CC} = V_{CC} MIN, V_{IN} = V_{IL} or V_{IH} per Truth Table
		74		0.35	0.5	V	I_{OL} = 24 mA	
I_{OZH}	Output Off Current HIGH				20	µA	V_{CC} = MAX, V_{OUT} = 2.7 V	
I_{OZL}	Output Off Current LOW				−400	µA	V_{CC} = MAX, V_{OUT} = 0.4 V	
I_{IH}	Input HIGH Current	A or B, DIR or $\overline{G}$			20	µA	V_{CC} = MAX, V_{IN} = 2.7 V	
		DIR or $\overline{G}$			0.1	mA	V_{CC} = MAX, V_{IN} = 7.0 V	
		A or B			0.1	mA	V_{CC} = MAX, V_{IN} = 5.5 V	
I_{IL}	Input LOW Current				−0.4	mA	V_{CC} = MAX, V_{IN} = 0.4 V	
I_{OS}	Output Short Circuit Current		−40		−225	mA	V_{CC} = MAX	
I_{CC}	Power Supply Current Total Output HIGH				70	mA	V_{CC} = MAX	
	Total Output LOW				90			
	Total at HIGH Z				95			

AC CHARACTERISTICS: T_A = 25°C, V_{CC} = 5.0 V

SYMBOL	PARAMETER	LS640			LS643			LS645			UNITS	TEST CONDITIONS
		MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX		
t_{PLH}	Propagation Delay, A to B		6.0	10		6.0	10		8.0	15	ns	
t_{PHL}			8.0	15		9.0	15		11	15		
t_{PLH}	Propagation Delay, B to A		6.0	10		8.0	15		8.0	15	ns	C_L = 45 pF, R_L = 667 Ω
t_{PHL}			8.0	15		11	15		11	15		
t_{PZL}	Output Enable Time $\overline{G}$, DIR to A		31	40		32	45		31	40	ns	
t_{PZH}			23	40		27	40		26	40		
t_{PZL}	Output Enable Time $\overline{G}$, DIR to B		31	40		32	45		31	40	ns	
t_{PZH}			23	40		23	40		26	40		
t_{PLZ}	Output Disable Time $\overline{G}$, DIR to A		15	25		15	25		15	25	ns	
t_{PHZ}			15	25		15	25		15	25		C_L = 5.0 pF
t_{PLZ}	Output Disable Time $\overline{G}$, DIR to B		15	25		15	25		15	25	ns	
t_{PHZ}			15	25		15	25		15	25		

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GUARANTEED OPERATING RANGES

SYMBOL	PARAMETER		MIN	TYP	MAX	UNIT
V_{CC}	Supply Voltage	54	4.5	5.0	5.5	V
		74	4.75	5.0	5.25	
T_A	Operating Ambient Temperature Range	54	−55	25	125	°C
		74	0	25	70	
V_{OH}	Output Voltage — High	54,74			5.5	V
I_{OL}	Output Current — Low	54			12	mA
		74			24	

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

SYMBOL	PARAMETER		LIMITS			UNITS	TEST CONDITIONS
			MIN	TYP	MAX		
V_{IH}	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage for All Inputs
V_{IL}	Input LOW Voltage	54			0.5	V	Guaranteed Input LOW Voltage for All Inputs
		74			0.6		
V_{IK}	Input Clamp Diode Voltage			−0.65	−1.5	V	V_{CC} = MIN, I_{IN} = −18 mA
I_{OH}	Output HIGH Current	54,74			100	μA	V_{CC} = MIN, V_{OH} = MAX
V_{OL}	Output LOW Voltage	54,74		0.25	0.4	V	I_{OL} = 12 mA	V_{CC} = V_{CC} MIN,
		74		0.35	0.5	V	I_{OL} = 24 mA	V_{IN} = V_{IL} or V_{IH} per Truth Table
I_{IH}	Input HIGH Current				20	μA	V_{CC} = MAX, V_{IN} = 2.7 V
					−0.1	mA	V_{CC} = MAX, V_{IN} = 7.0 V
I_{IL}	Input LOW Current				−0.4	mA	V_{CC} = MAX, V_{IN} = 0.4 V
I_{CC}	Power Supply Current Total, Output HIGH				70	mA	V_{CC} = MAX
	Total, Output LOW				90	mA	V_{CC} = MAX
	Total at HIGH Z				95	mA	V_{CC} = MAX

AC CHARACTERISTICS: T_A = 25°C, V_{CC} = 5.0 V

SYMBOL	PARAMETER	LIMITS									UNITS	TEST CONDITIONS
		LS641			LS642			LS644				
		MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX		
t_{PLH}	Propagation Delay, A to B		17	25		19	25		17	25	ns	
t_{PHL}			16	25		14	25		14	25		
t_{PLH}	Propagation Delay, B to A		17	25		19	25		19	25	ns	C_L = 45 pF,
t_{PHL}			16	25		14	25		16	25		R_L = 667 Ω
t_{PLH}	Propagation Delay, $\overline{G}$, DIR to A		23	40		26	40		26	40	ns	
t_{PHL}			34	50		43	60		43	60		
t_{PLH}	Propagation Delay, $\overline{G}$, DIR to B		25	40		28	40		25	40	ns	
t_{PHL}			37	50		39	60		37	50		

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