

Low Voltage Adjustable Precision Shunt Regulator

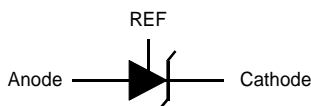
Features

- Precise Reference Voltage to 1.24V
- Guaranteed 0.5%, 1%, or 1.5% Reference Voltage Tolerance
- Sink Current Capability, 80uA to 100mA
- Quick Turn-on
- Adjustable Output Voltage, $V_O = V_{REF}$ to 20V
- Low Operational Cathode Current, 80uA Typical
- 0.1Ω Typical Output Impedance
- SOT-23-5 Package
- Lead Free and Green Devices Available (RoHS Compliant)

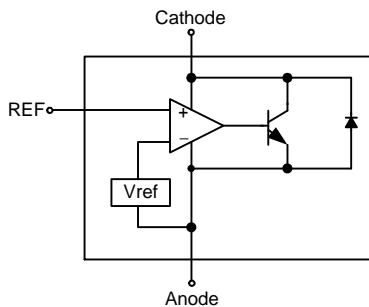
Applications

- Linear Regulators
- Adjustable Power Supply
- Switching Power Supply

Symbol



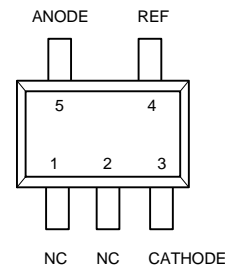
Functional Diagram



General Description

The APL1431L is a 3-terminal low voltage adjustable precision reference with specified thermal stability over applicable commercial temperature ranges. Output voltage may be set to any value between V_{REF} (1.24 V) and 20 V with two external resistors (see Figure 2). When used with a photocoupler, the APL1431L is an ideal voltage reference in isolated feedback circuits for 3V to 12V switching-mode power supplies. This device has a typical output impedance of 0.1Ω. Active output circuitry provides a very sharp turn-on characteristic, making the APL1431L excellent replacements for zener diodes in many applications, including on-board regulation and adjustable power supplies.

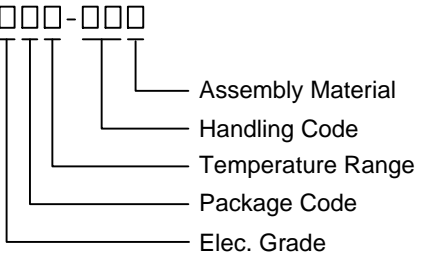
Pin Configuration



SOT-23-5 (Top View)

ANPEC reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Ordering and Marking Information

<p>APL1431L □□□-□□□</p>  <ul style="list-style-type: none"> — Assembly Material — Handling Code — Temperature Range — Package Code — Elec. Grade 	<p>Elec. Grade A : 0.5% Reference Voltage Tolerance B : 1% Reference Voltage Tolerance</p> <p>Package Code B : SOT-23-5</p> <p>Temperature Range C : 0 to 70 °C I : -40 to 85 °C</p> <p>Handling Code TR : Tape & Reel</p> <p>Assembly Material L : Lead Free Device G : Halogen and Lead Free Device</p>
APL1431L B : 1431L	

Note: ANPEC lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. ANPEC lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020C for MSL classification at lead-free peak reflow temperature. ANPEC defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
V_{KA}	Cathode Voltage	21	V
I_K	Continuous Cathode Current Range	120	mA
I_{REF}	Reference Current Range	3	mA
θ_{JA}	Thermal Resistance from Junction to Ambient in Free Air SOT-23-5	357	°C/W
T_J	Operating Junction Temperature Range	-40 to 150	°C
T_{STG}	Storage Temperature Range	-65 to 150	°C
T_{SDR}	Maximum Lead Soldering Temperature, 10 Seconds	260	°C

Electrical Characteristics $T_A = 25^\circ\text{C}$ (unless otherwise noted)

Symbol	Parameter	Test Conditions	APL1431L			Unit	
			Min.	Typ.	Max.		
V_{REF}	Reference Voltage	$V_{KA}=V_{REF}, I_K=10\text{mA}$ $T_A = 25^\circ\text{C}$, (Fig. 1)	APL1431LA	1.234	1.240	1.246	V
			APL1431LB	1.228	1.240	1.252	
		$T_A = \text{full range}$ (Note1) , (Fig.1)	APL1431LA	1.222	1.240	1.258	
			APL1431LB	1.215	1.240	1.265	
V_{DEF}	V_{DEF} Temp Deviation	$T_A = \text{full range}$ (Note1) $V_{KA}=V_{REF}, I_K=10\text{mA}$ (Fig. 1)	-	5	15	mV	
$\frac{\Delta V_{REF}}{\Delta V_{KA}}$	Ratio of Change in V_{REF} to Change in Cathods Votage	$I_K=10\text{mA}, V_{KA}=16\text{V to } V_{REF}$ (Fig. 2)	-	-0.2	-1.0	mV/V	
I_{REF}	Reference Input Current	$I_K=10\text{mA}, R_1=10\text{k}\Omega, R_2=\infty$ (Fig. 2)	-	0.15	0.5	μA	
$I_{REF(DEV)}$	I_{REF} Temp Deviation	$T_K = \text{full range}$ (Note 1), $R_1=10\text{k}\Omega, R_2=\infty, I_K=10\text{mA}$, (Fig. 2)	-	0.05	0.3	μA	
$I_{K(off)}$	Off-State Cathode Current	$V_{REF}=0\text{V}$, (Fig. 3)	$V_{KA}=6\text{V}$	-	0.01	0.1	μA
			$V_{KA}=16\text{V}$	-	0.01	0.5	
Z_{KA}	Dynamic Output Impedance	$V_{KA}=V_{REF}, I_K=1\text{mA to } 100\text{mA}$, $f \leq 1\text{kHz}$ (Fig. 1)	-	0.1	0.4	Ω	
$I_{K(MIN)}$	Minimum Operating Current	$V_{KA}=V_{REF}$ (Fig. 1)	-	80	100	μA	

Note 1 : Full temperature range is 0°C to 70°C for APL1431LXXC, and -40°C to 85°C for APL1431LXXI.

Test Figures

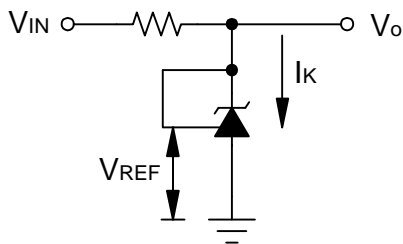


Figure 1. Test Circuit for $V_{KA}=V_{REF}, V_O=V_{KA}=V_{REF}$

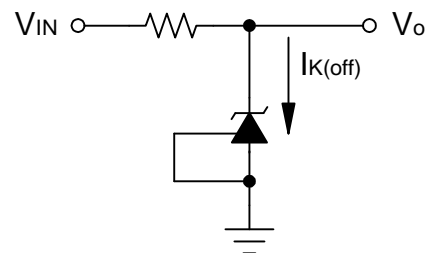


Figure 2. Test Circuit for $I_{K(off)}$

Test Figures (Cont.)

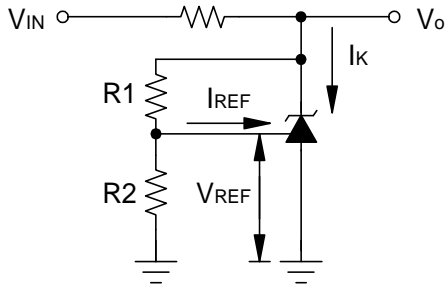
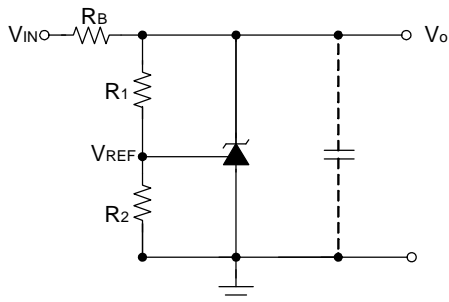
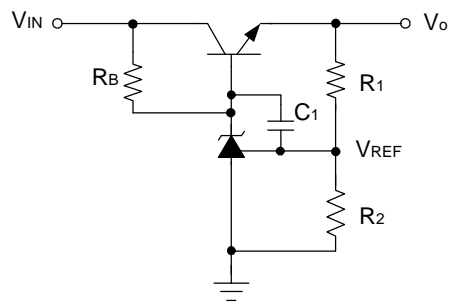


Figure 3. Test Circuit for $V_{KA} > V_{REF}$
 $V_O = V_{KA} = V_{REF} \times (1 + R_1/R_2) + I_{REF} \times R_1$

Application Circuits



Precision Voltage Reference



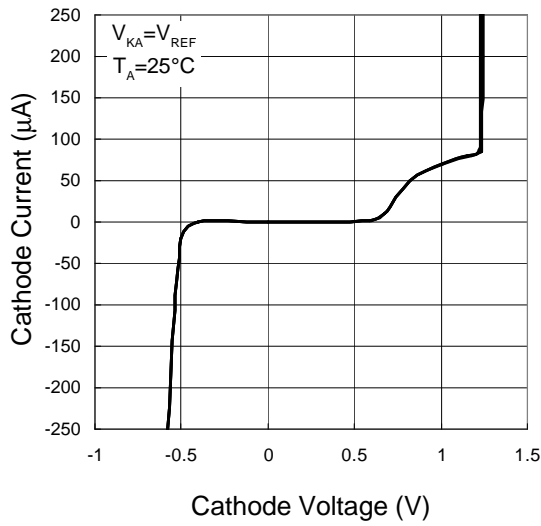
Precision High-Current Series Regulator

Notes for Application Circuits:

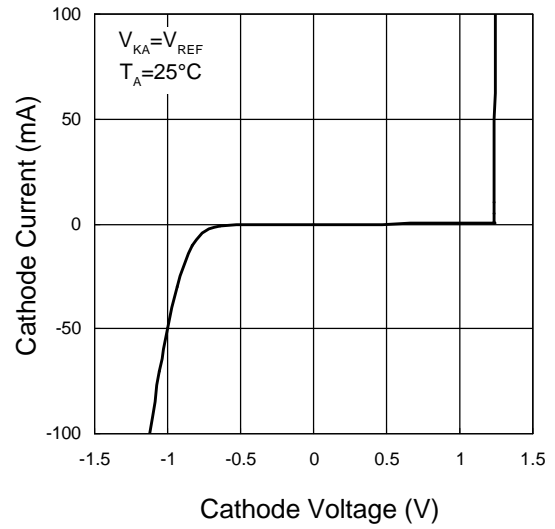
- 1) For the series regulator applications, add a compensation capacitor C1 between CATHODE and REF is strongly recommended to improve the stability of output voltage .
- 2) Set V_O according to the following equation: $V_O = V_{REF}(1+R1/R2)+I_{REF} \times R1$
- 3) Choose the value for R_B as follows:
 - A) The maximum limit for R_B should be such that the cathode current (I_k) is greater than the minimum operating current (80µA) at $V_{IN(MIN)}$.
 - B) The minimum limit for R_B should be such that the cathode current (I_k) does not exceed 100mA under all load conditions, and the instantaneous turn-on value for I_k does not exceed 150mA.

Typical Characteristics

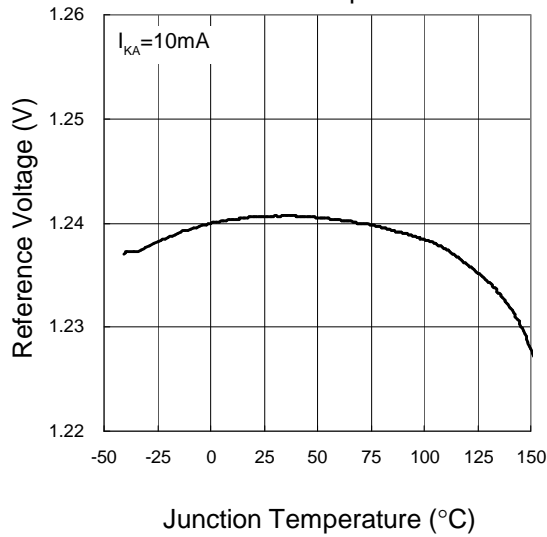
Cathode Current vs. Cathode Voltage



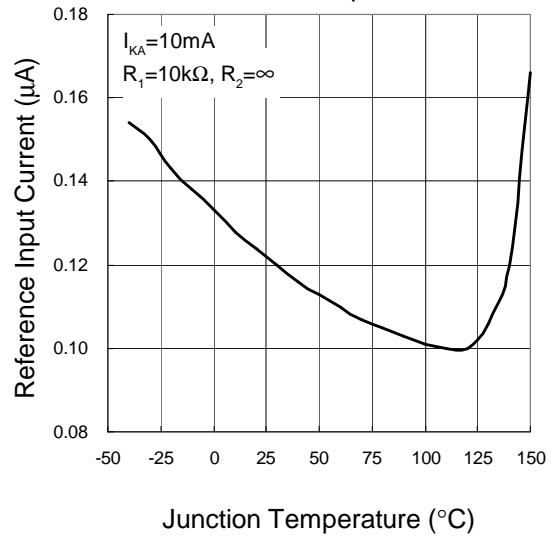
Cathode Current vs. Cathode Voltage



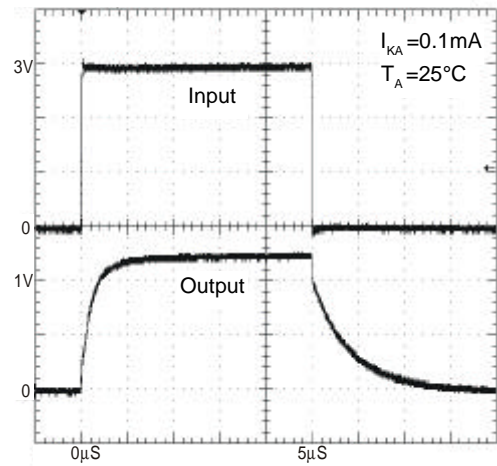
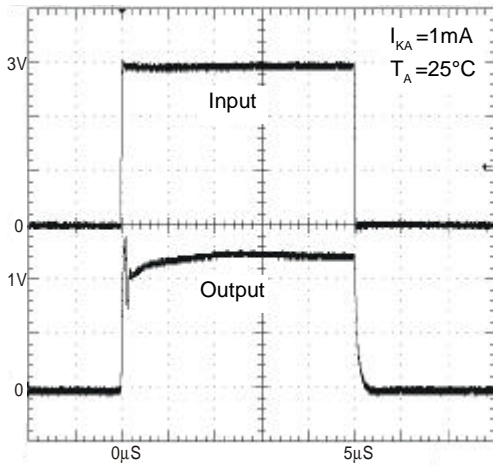
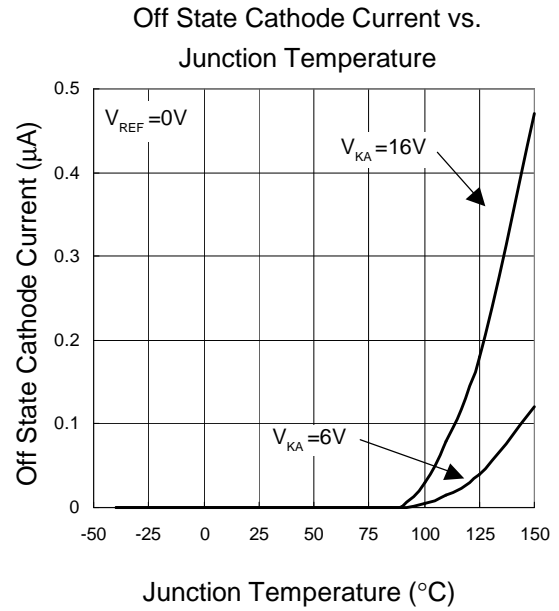
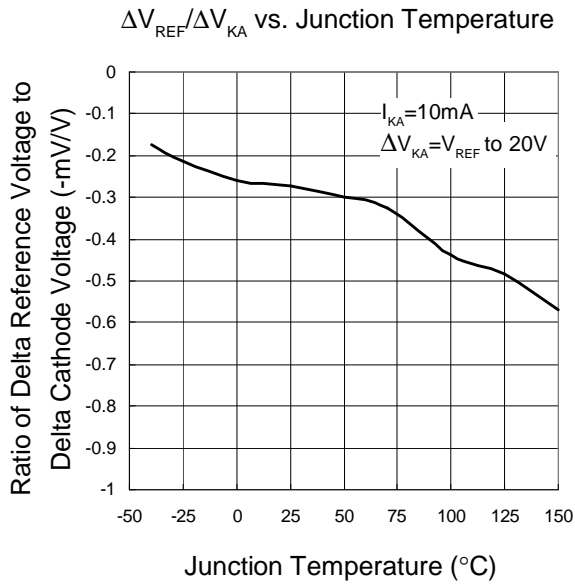
Reference Voltage vs. Junction Temperature



Reference Input Current vs. Junction Temperature

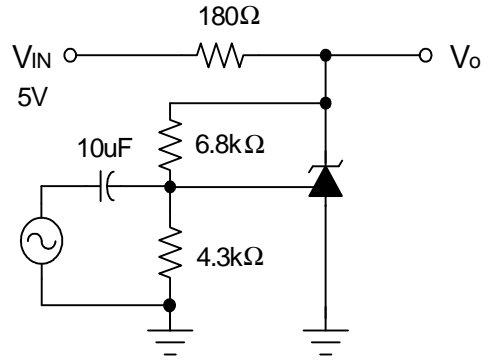
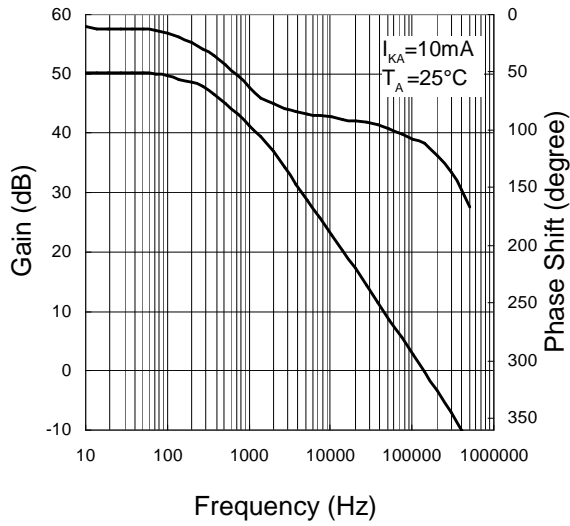


Typical Characteristics (Cont.)



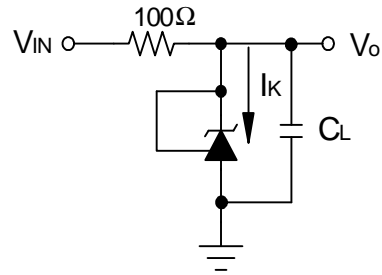
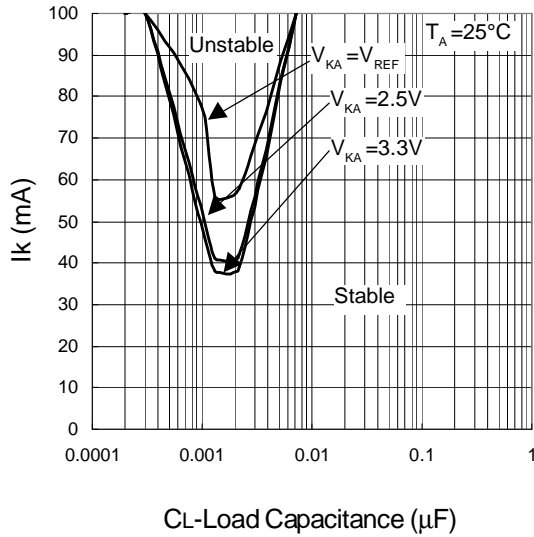
Typical Characteristics (Cont.)

Gain vs. Phase Shift vs. Frequency

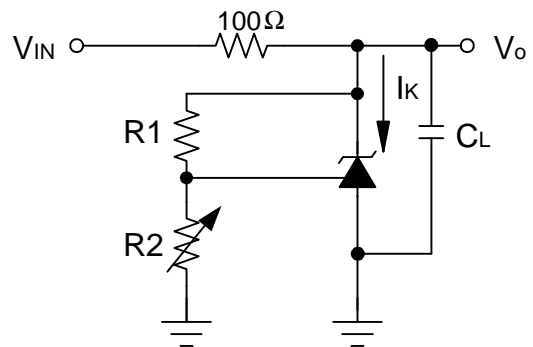


Gain & Phase Test Circuit

Stability Boundary Conditions

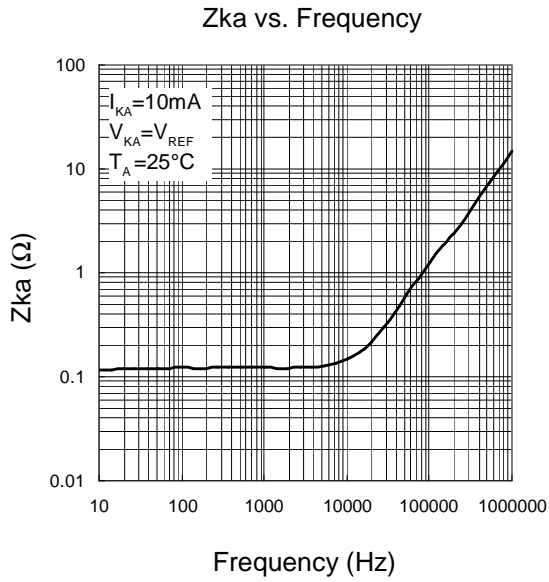


Stability Test Circuit for $V_{KA} = V_{REF}$



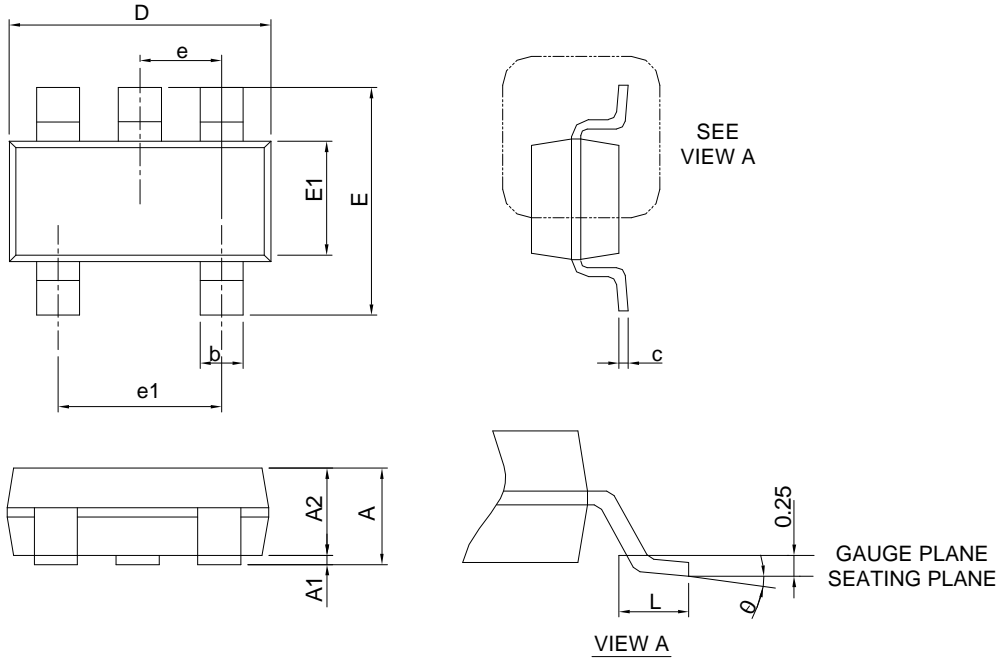
Stability Test Circuit for $V_{KA} > V_{REF}$
 $V_O = V_{KA} = V_{REF} \times (1 + R_1/R_2) + I_{REF} \times R_1$
 Use the MLCC for C_L

Typical Characteristics (Cont.)



Package Information

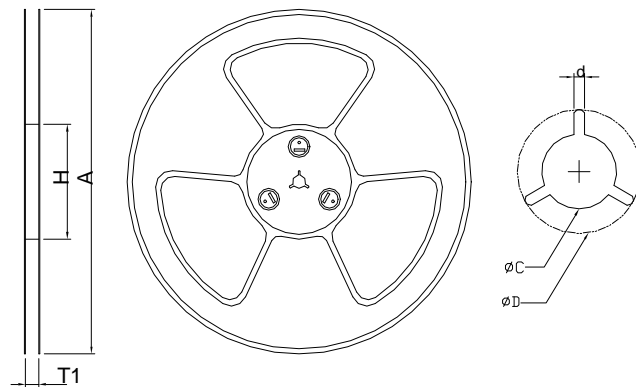
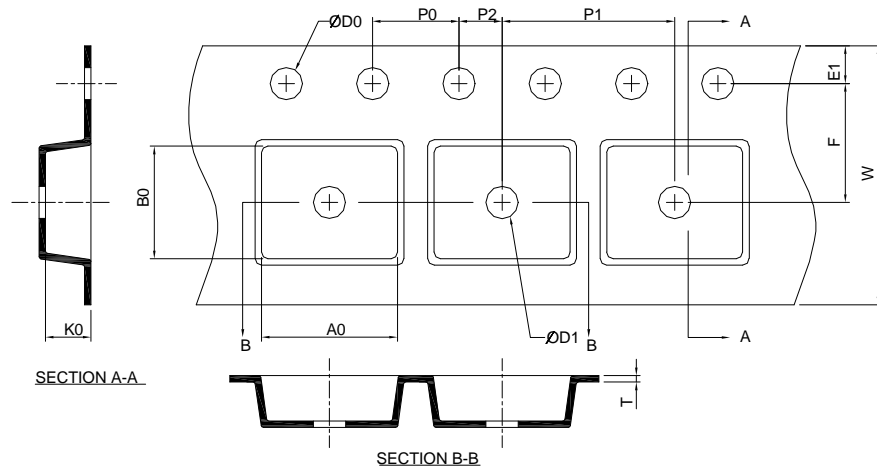
SOT-23-5



SYMBOL	SOT-23-5			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A		1.45		0.057
A1	0.00	0.15	0.000	0.006
A2	0.90	1.30	0.035	0.051
b	0.30	0.50	0.012	0.020
c	0.08	0.22	0.003	0.009
D	2.70	3.10	0.106	0.122
E	2.60	3.00	0.102	0.118
E1	1.40	1.80	0.055	0.071
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.30	0.60	0.012	0.024
θ	0°	8°	0°	8°

Note : 1. Follow JEDEC TO-178 AA.
 2. Dimension D and E1 do not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.

Carrier Tape & Reel Dimensions



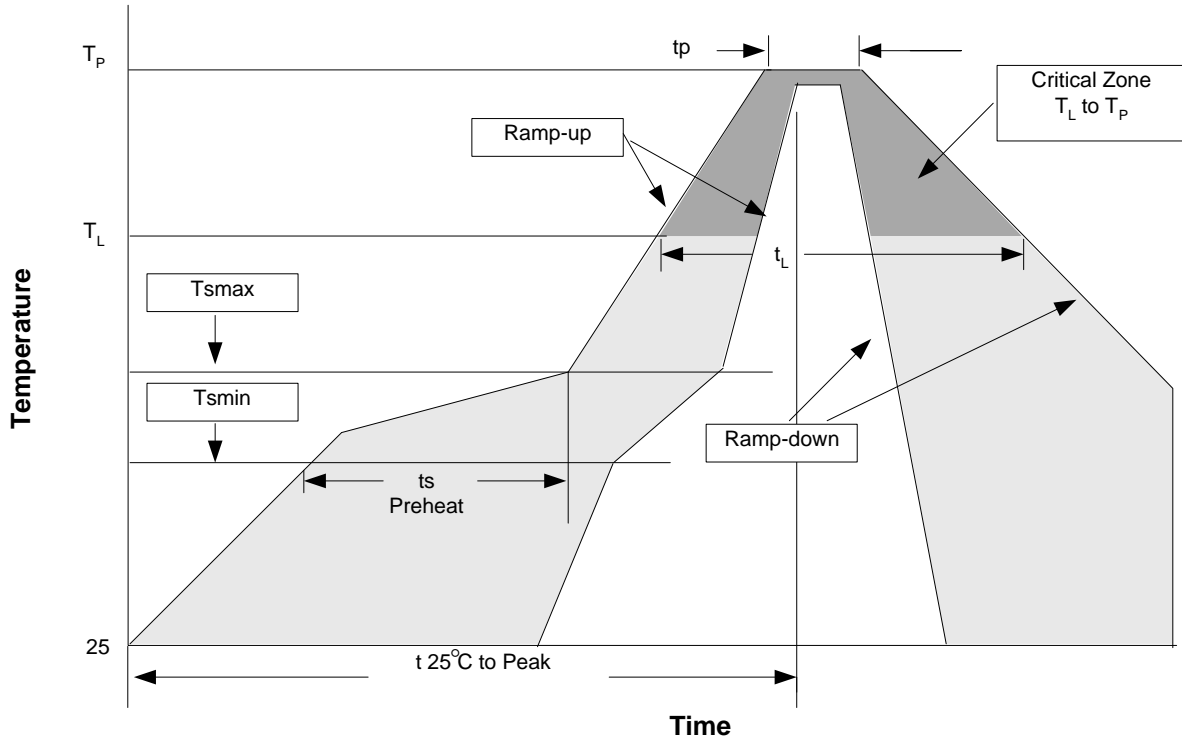
Application	A	H	T1	C	d	D	W	E1	F
SOT-23-5	178.0 ±2.00	50 MIN.	8.4+2.00 -0.00	13.0+0.50 -0.20	1.5 MIN.	20.2 MIN.	8.0 ±0.30	1.75 ±0.10	3.5 ±0.05
	P0	P1	P2	D0	D1	T	A0	B0	K0
	4.0 ±0.10	4.0 ±0.10	2.0 ±0.10	1.5+0.10 -0.00	1.5 MIN.	0.6+0.00 -0.40	3.20 ±0.20	3.10 ±0.20	1.50 ±0.20

(mm)

Devices Per Unit

Package Type	Unit	Quantity
SOT-23-5	Tape & Reel	3000

Reflow Condition (IR/Convection or VPR Reflow)



Reliability Test Program

Test item	Method	Description
SOLDERABILITY	MIL-STD-883D-2003	245°C, 5 sec
HOLT	MIL-STD-883D-1005.7	1000 Hrs Bias @125°C
PCT	JESD-22-B,A102	168 Hrs, 100%RH, 121°C
TST	MIL-STD-883D-1011.9	-65°C~150°C, 200 Cycles
ESD	MIL-STD-883D-3015.7	VHBM > 2KV, VMM > 200V
Latch-Up	JESD 78	10ms, $I_{tr} > 100mA$

Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	3°C/second max.	3°C/second max.
Preheat		
- Temperature Min (T_{smin})	100°C	150°C
- Temperature Max (T_{smax})	150°C	200°C
- Time (min to max) (t_s)	60-120 seconds	60-180 seconds
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60-150 seconds	60-150 seconds
Peak/Classification Temperature (T_p)	See table 1	See table 2
Time within 5°C of actual Peak Temperature (t_p)	10-30 seconds	20-40 seconds
Ramp-down Rate	6°C/second max.	6°C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

Note: All temperatures refer to topside of the package. Measured on the body surface.

Classification Reflow Profiles (Cont.)

Table 1. SnPb Eutectic Process – Package Peak Reflow Temperatures

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	240 +0/-5°C	225 +0/-5°C
≥2.5 mm	225 +0/-5°C	225 +0/-5°C

Table 2. Pb-free Process – Package Classification Reflow Temperatures

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 +0°C*	260 +0°C*	260 +0°C*
1.6 mm – 2.5 mm	260 +0°C*	250 +0°C*	245 +0°C*
≥2.5 mm	250 +0°C*	245 +0°C*	245 +0°C*

* Tolerance: The device manufacturer/supplier **shall** assure process compatibility up to and including the stated classification temperature (this means Peak reflow temperature +0°C. For example 260°C+0°C) at the rated MSL level.

Customer Service

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