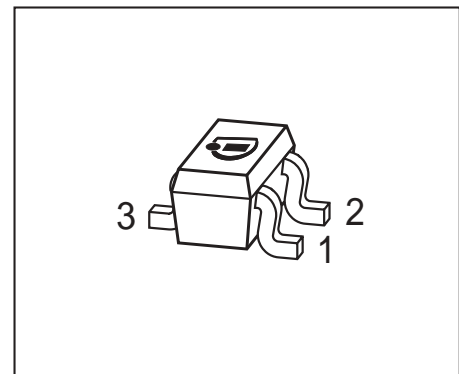


NPN Silicon RF Transistor*

- For low noise, high-gain broadband amplifiers at collector currents from 2 mA to 30 mA
- $f_T = 8$ GHz, $F = 0.9$ dB at 900 MHz
- * Short term description



ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Type	Marking	Pin Configuration			Package
BFR183T	RH	1=B	2=E	3=C	SC75

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CEO}	12	V
Collector-emitter voltage	V_{CES}	20	
Collector-base voltage	V_{CBO}	20	
Emitter-base voltage	V_{EBO}	2	
Collector current	I_C	65	mA
Base current	I_B	5	
Total power dissipation ¹⁾ $T_S \leq 83$ °C	P_{tot}	250	mW
Junction temperature	T_j	150	°C
Ambient temperature	T_A	-65 ... 150	
Storage temperature	T_{stg}	-65 ... 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ²⁾	R_{thJS}	≤ 270	K/W

¹⁾ T_S is measured on the collector lead at the soldering point to the pcb

²⁾ For calculation of R_{thJA} please refer to Application Note Thermal Resistance

Electrical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage $I_C = 1 \text{ mA}, I_B = 0$	$V_{(BR)CEO}$	12	-	-	V
Collector-emitter cutoff current $V_{CE} = 20 \text{ V}, V_{BE} = 0$	I_{CES}	-	-	100	μA
Collector-base cutoff current $V_{CB} = 10 \text{ V}, I_E = 0$	I_{CBO}	-	-	100	nA
Emitter-base cutoff current $V_{EB} = 1 \text{ V}, I_C = 0$	I_{EBO}	-	-	1	μA
DC current gain- $I_C = 15 \text{ mA}, V_{CE} = 8 \text{ V}, \text{ pulse measured}$	h_{FE}	70	100	140	-

Electrical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random sampling)					
Transition frequency $I_C = 25\text{ mA}$, $V_{CE} = 8\text{ V}$, $f = 500\text{ MHz}$	f_T	6	8	-	GHz
Collector-base capacitance $V_{CB} = 10\text{ V}$, $f = 1\text{ MHz}$, $V_{BE} = 0$, emitter grounded	C_{cb}	-	0.37	0.57	pF
Collector emitter capacitance $V_{CE} = 10\text{ V}$, $f = 1\text{ MHz}$, $V_{BE} = 0$, base grounded	C_{ce}	-	0.2	-	
Emitter-base capacitance $V_{EB} = 0.5\text{ V}$, $f = 1\text{ MHz}$, $V_{CB} = 0$, collector grounded	C_{eb}	-	1.1	-	
Noise figure $I_C = 5\text{ mA}$, $V_{CE} = 8\text{ V}$, $Z_S = Z_{Sopt}$, $f = 900\text{ MHz}$ $I_C = 5\text{ mA}$, $V_{CE} = 8\text{ V}$, $Z_S = Z_{Sopt}$, $f = 1.8\text{ GHz}$	F	-	0.9	-	dB
Power gain, maximum stable ¹⁾ $I_C = 15\text{ mA}$, $V_{CE} = 8\text{ V}$, $Z_S = Z_{Sopt}$, $Z_L = Z_{Lopt}$, $f = 900\text{ MHz}$	G_{ms}	-	20	-	dB
Power gain, maximum available ²⁾ $I_C = 15\text{ mA}$, $V_{CE} = 8\text{ V}$, $Z_S = Z_{Sopt}$, $Z_L = Z_{Lopt}$, $f = 1.8\text{ GHz}$	G_{ma}	-	13	-	dB
Transducer gain $I_C = 15\text{ mA}$, $V_{CE} = 8\text{ V}$, $Z_S = Z_L = 50\ \Omega$, $f = 900\text{ MHz}$ $I_C = 15\text{ mA}$, $V_{CE} = 8\text{ V}$, $Z_S = Z_L = 50\ \Omega$, $f = 1.8\text{ MHz}$	$ S_{21e} ^2$	-	16	-	dB
		-	10	-	

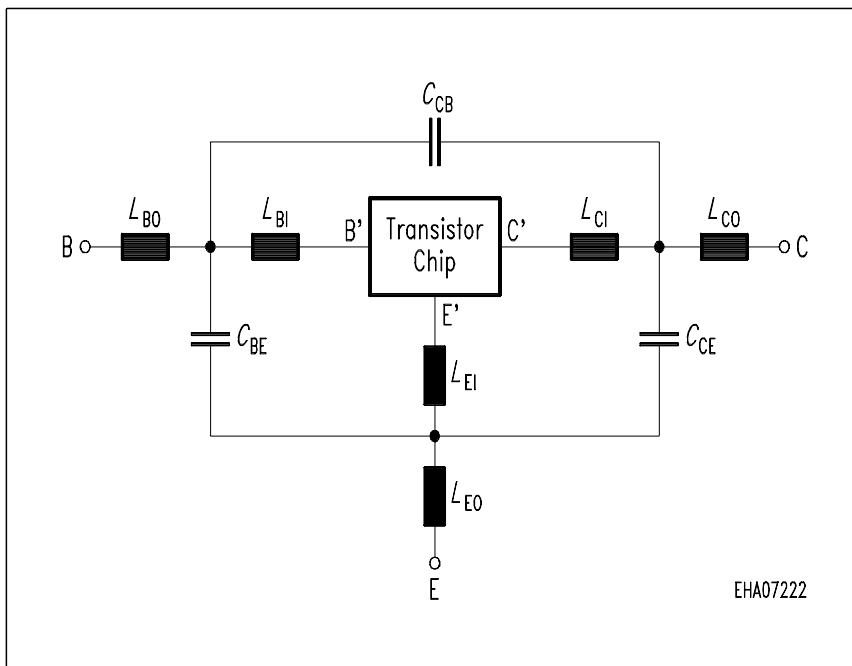
$$^1G_{ms} = |S_{21} / S_{12}|$$

$$^2G_{ma} = |S_{21e} / S_{12e}| (k \cdot (k^2 - 1)^{1/2}),$$

SPICE Parameter (Gummel-Poon Model, Berkley-SPICE 2G.6 Syntax):
Transistor Chip Data:

IS =	1.0345	fA	BF =	115.98	-	NF =	0.80799	-
VAF =	14.772	V	IKF =	0.14562	A	ISE =	16.818	fA
NE =	1.2149	-	BR =	10.016	-	NR =	0.99543	-
VAR =	3.4276	V	IKR =	0.013483	A	ISC =	1.3559	fA
NC =	0.85331	-	RB =	2.5426	Ω	IRB =	0.43801	mA
RBM =	1.0112	Ω	RE =	1.3435	-	RC =	0.20486	Ω
CJE =	23.077	fF	VJE =	1.0792	V	MJE =	0.45354	-
TF =	22.746	ps	XTF =	0.36823	-	VTF =	0.50905	V
ITF =	1.8773	mA	PTF =	0	deg	CJC =	460.11	fF
VJC =	1.1967	V	MJC =	0.3	-	XCJC =	0.053823	-
TR =	1.0553	ns	CJS =	0	fF	VJS =	0.75	V
MJS =	0	-	XTB =	0	-	EG =	1.11	eV
XTI =	3	-	FC =	0.54852	-	TNOM	300	K

All parameters are ready to use, no scaling is necessary. Extracted on behalf of Infineon Technologies AG by: Institut für Mobil- und Satellitentechnik (IMST)

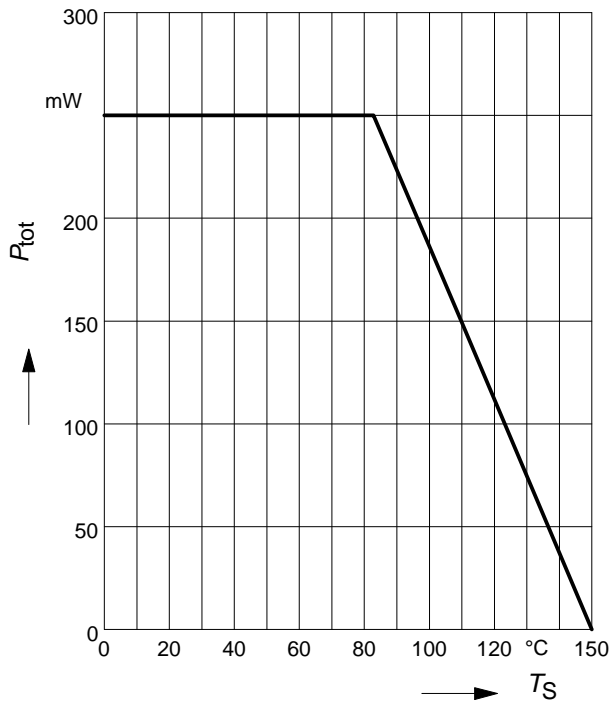
Package Equivalent Circuit:


$L_1 =$	0.762	nH
$L_2 =$	0.706	nH
$L_3 =$	0.382	nH
$C_1 =$	62	fF
$C_2 =$	84	fF
$C_3 =$	180	fF
$C_4 =$	7	fF
$C_5 =$	40	fF
$C_6 =$	48	fF

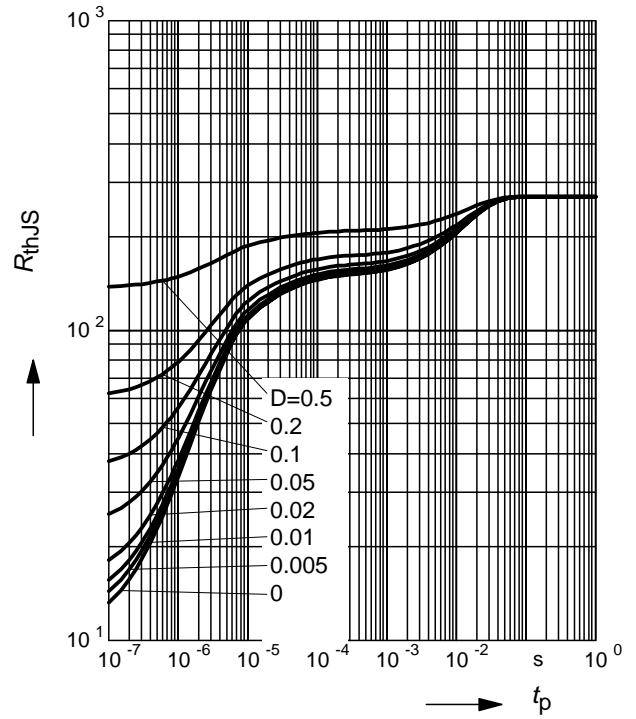
Valid up to 6GHz

For examples and ready to use parameters please contact your local Infineon Technologies distributor or sales office to obtain a Infineon Technologies CD-ROM or see Internet: <http://www.infineon.com>

Total power dissipation $P_{tot} = f(T_S)$

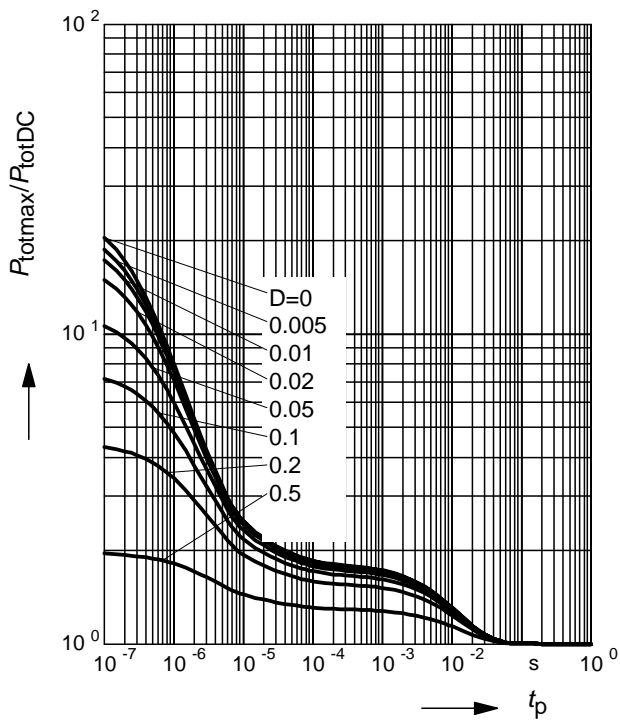


Permissible Pulse Load $R_{thJS} = f(t_p)$

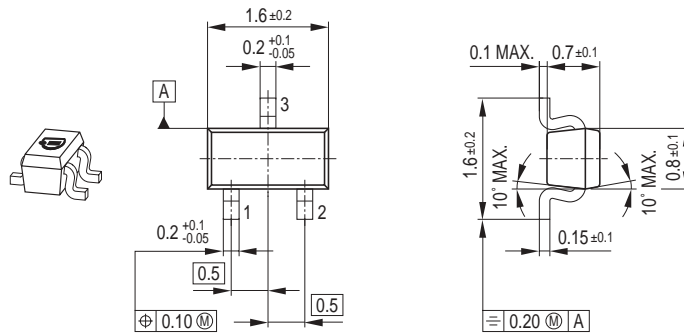


Permissible Pulse Load

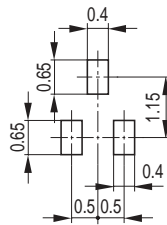
$P_{totmax}/P_{totDC} = f(t_p)$



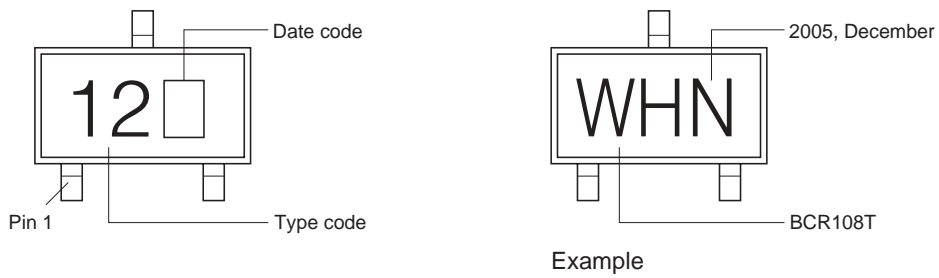
Package Outline



Foot Print

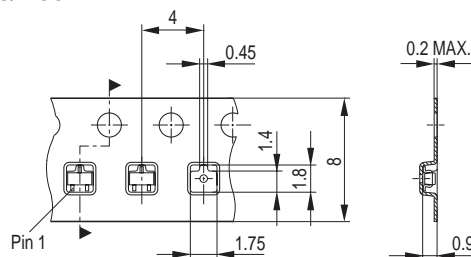


Marking Layout



Standard Packing

Reel \varnothing 180 mm = 3.000 Pieces/Reel
 Reel \varnothing 330 mm = 10.000 Pieces/Reel



Date Code marking for discrete packages with one digit (SCD80, SC79, SC75¹⁾) CES-Code

Month	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014
01	a	p	A	P	a	p	A	P	a	p	A	P
02	b	q	B	Q	b	q	B	Q	b	q	B	Q
03	c	r	C	R	c	r	C	R	c	r	C	R
04	d	s	D	S	d	s	D	S	d	s	D	S
05	e	t	E	T	e	t	E	T	e	t	E	T
06	f	u	F	U	f	u	F	U	f	u	F	U
07	g	v	G	V	g	v	G	V	g	v	G	V
08	h	x	H	X	h	x	H	X	h	x	H	X
09	j	y	J	Y	j	y	J	Y	j	y	J	Y
10	k	z	K	Z	k	z	K	Z	k	z	K	Z
11	l	2	L	4	l	2	L	4	l	2	L	4
12	n	3	N	5	n	3	N	5	n	3	N	5

1) New Marking Layout for SC75, implemented at October 2005.

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81726 München
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