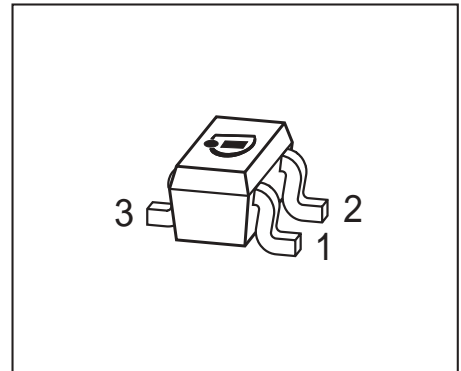**NPN Silicon RF Transistor***

- Low voltage/ low current operation

- Transition frequency of 14 GHz

- High insertion gain

- Ideal for low current amplifiers and oscillators

* Short term description

ESD (**E**lectro**s**tatic **d**ischarge) sensitive device, observe handling precaution!

Type	Marking	Pin Configuration			Package
BFR340T	FA	1 = B	2 = E	3 = C	SC75

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CEO}	6	V
Collector-emitter voltage	V_{CES}	15	
Collector-base voltage	V_{CBO}	15	
Emitter-base voltage	V_{EBO}	2	
Collector current	I_C	10	mA
Base current	I_B	2	
Total power dissipation[1] $T_S \leq 113°C$	P_{tot}	60	mW
Junction temperature	T_j	150	°C
Ambient temperature	T_A	-65 ... 150	
Storage temperature	T_{stg}	-65 ... 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point[2]	R_{thJS}	≤ 605	K/W

[1] T_S is measured on the collector lead at the soldering point to the pcb

[2] For calculation of R_{thJA} please refer to Application Note Thermal Resistance

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Electrical Characteristics at T_A = 25°C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage I_C = 1 mA, I_B = 0	$V_{(BR)CEO}$	6	9	-	V
Collector-emitter cutoff current V_{CE} = 15 V, V_{BE} = 0	I_{CES}	-	-	10	µA
Collector-base cutoff current V_{CB} = 5 V, I_E = 0	I_{CBO}	-	-	100	nA
Emitter-base cutoff current V_{EB} = 1 V, I_C = 0	I_{EBO}	-	-	1	µA
DC current gain- I_C = 5 mA, V_{CE} = 3 V, pulse measured	h_{FE}	90	120	160	-

Electrical Characteristics at T_A = 25°C, unless otherwise specified

| Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random sampling)					
Transition frequency I_C = 6 mA, V_{CE} = 3 V, f = 1 GHz	f_T	10	14	-	GHz
Collector-base capacitance V_{CB} = 5 V, f = 1 MHz, V_{BE} = 0 , emitter grounded	C_{cb}	-	0.24	0.4	pF
Collector emitter capacitance V_{CE} = 5 V, f = 1 MHz, V_{BE} = 0 , base grounded	C_{ce}	-	0.2	-	
Emitter-base capacitance V_{EB} = 0.5 V, f = 1 MHz, V_{CB} = 0 , collector grounded	C_{eb}	-	0.1	-	
Noise figure I_C = 1 mA, V_{CE} = 3 V, $Z_S = Z_{Sopt}$, f = 1.8 GHz	F_{min}	-	1.15	-	dB
Power gain, maximum stable[1] I_C = 5 mA, V_{CE} = 3 V, $Z_S = Z_{Sopt}$, $Z_L = Z_{Lopt}$, f = 1.8 GHz	G_{ms}	-	15.5	-	-
Power gain, maximum available[1] I_C = 5 mA, V_{CE} = 3 V, $Z_S = Z_{Sopt}$, $Z_L = Z_{Lopt}$, f = 3 GHz	G_{ma}	-	10.5	-	dB
Transducer gain I_C = 5 mA, V_{CE} = 3 V, $Z_S = Z_L = 50\Omega$, f = 1.8 GHz f = 3 GHz	$\lvert S_{21e}\rvert^2$	- -	12.5 8.5	- -	dB
Third order intercept point at output[2] V_{CE} = 3 V, I_C = 5 mA, f = 1.8 GHz, $Z_S = Z_L = 50\Omega$	IP_3	-	13.5	-	dBm
1dB Compression point at output I_C = 5 mA, V_{CE} = 3 V, $Z_S = Z_L = 50\Omega$, f = 1.8 GHz	P_{-1dB}	-	0	-	

[1] $G_{ma} = \lvert S_{21e} / S_{12e}\rvert (k-(k^2-1)^{1/2})$, $G_{ms} = \lvert S_{21e} / S_{12e}\rvert$

[2] IP3 value depends on termination of all intermodulation frequency components. Termination used for this measurement is 50Ω from 0.1 MHz to 6 GHz

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SPICE Parameter (Gummel-Poon Model, Berkley-SPICE 2G.6 Syntax):

Transitor Chip Data:

IS =	6.12	fA	BF =	98.48	-	NF =	0.4213	-
VAF =	42.228	V	IKF =	103	mA	ISE =	11.768	nA
NE =	2.4753	-	BR =	19.61	-	NR =	0.3253	-
VAR =	16.777	V	IKR =	0.834	A	ISC =	3.632	nA
NC =	0.8956	-	RB =	59.99	Ω	IRB =	0.01	mA
RBM =	0.2403	Ω	RE =	3.677	-	RC =	5.2493	Ω
CJE =	182	fF	VJE =	0.626	V	MJE =	0.4172	-
TF =	10.3	ps	XTF =	0	-	VTF =	0.262	V
ITF =	0.0017	mA	PTF =	0	deg	CJC =	222.63	fF
VJC =	0.5487	V	MJC =	0.319	-	XCJC =	0.3904	-
TR =	2.71	ns	CJS =	0	fF	VJS =	0.75	V
MJS =	0	-	NK =	0.5	-	EG =	1.11	eV
XTI =	0	-	FC =	0.735		TNOM	300	K

All parameters are ready to use, no scalling is necessary. Extracted on behalf of Infineon Technologies AG by: Institut für Mobil- und Satellitentechnik (IMST)
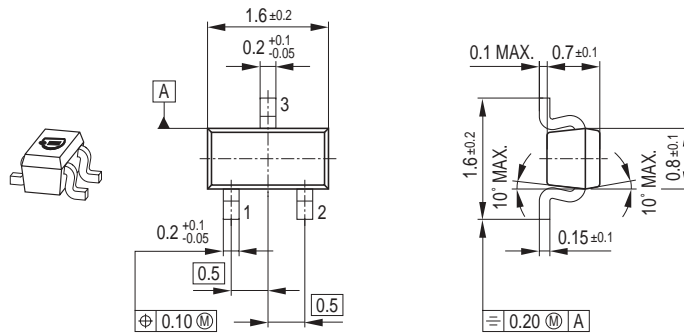
Package Equivalent Circuit:
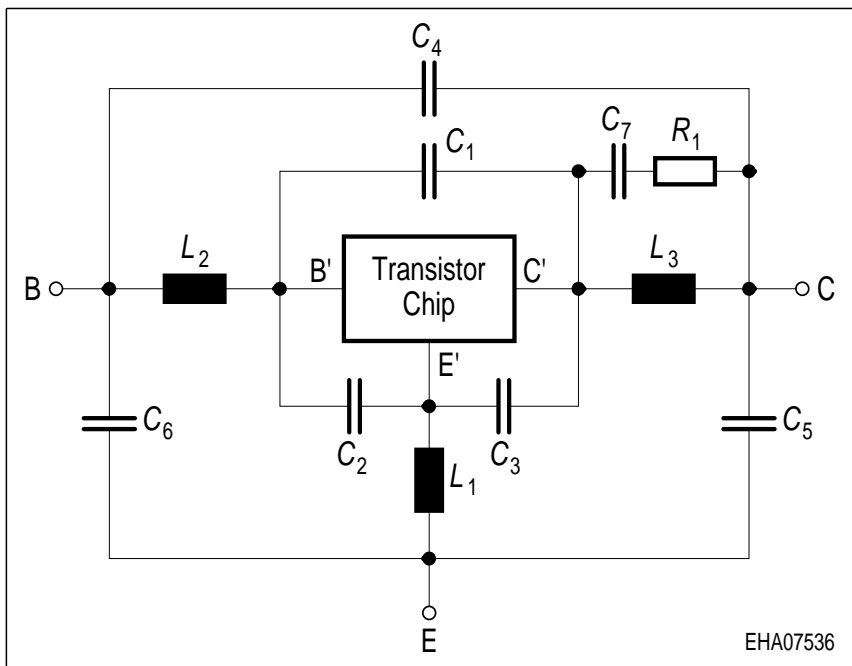
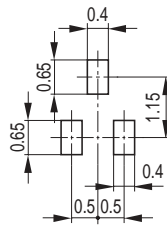|		
L_1 =	0.762	nH
L_2 =	0.706	nH
L_3 =	0.382	nH
C_1 =	62	fF
C_2 =	84	fF
C_3 =	180	fF
C_4 =	7	fF
C_5 =	40	fF
C_6 =	48	fF

Valid up to 6GHz

EHA07536

For examples and ready to use parameters please contact your local Infineon Technologies distributor or sales office to obtain a Infineon Technologies CD-ROM or see Internet: http://www.infineon.com
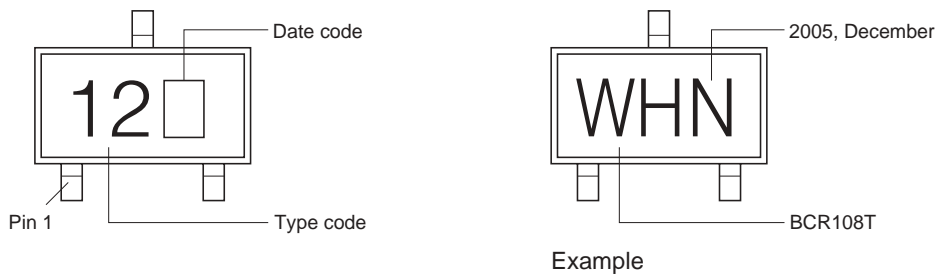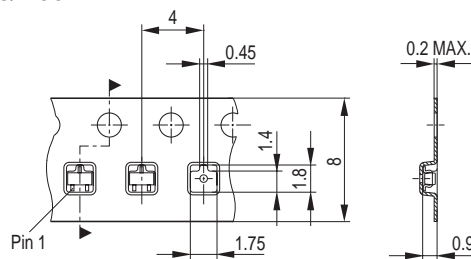
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Package Outline

1.6 ±0.2

$0.2 {}^{+0.1}_{-0.05}$

A

3

1

2

$0.2 {}^{+0.1}_{-0.05}$

0.5

0.5

⊕ 0.10 Ⓜ

0.1 MAX.   0.7 ±0.1

1.6 ±0.2

10° MAX.

10° MAX.

0.8 ±0.1

0.15 ±0.1

⊟ 0.20 Ⓜ A

Foot Print

0.4

0.65

0.65

1.15

0.4

0.5 0.5

Marking Layout

Date code

12

Pin 1   Type code

2005, December

WHN

BCR108T

Example

Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel
Reel ø330 mm = 10.000 Pieces/Reel

4

0.45

1.4

1.8

8

Pin 1   1.75

0.2 MAX.

0.9

Date Code marking for discrete packages with one digit (SCD80, SC79, SC75[1)]) CES-Code

Month	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014
01	a	p	A	P	a	p	A	P	a	p	A	P
02	b	q	B	Q	b	q	B	Q	b	q	B	Q
03	c	r	C	R	c	r	C	R	c	r	C	R
04	d	s	D	S	d	s	D	S	d	s	D	S
05	e	t	E	T	e	t	E	T	e	t	E	T
06	f	u	F	U	f	u	F	U	f	u	F	U
07	g	v	G	V	g	v	G	V	g	v	G	V
08	h	x	H	X	h	x	H	X	h	x	H	X
09	j	y	J	Y	j	y	J	Y	j	y	J	Y
10	k	z	K	Z	k	z	K	Z	k	z	K	Z
11	l	2	L	4	l	2	L	4	l	2	L	4
12	n	3	N	5	n	3	N	5	n	3	N	5

1) New Marking Layout for SC75, implemented at October 2005.

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2006-01-17

Attention please!

Information

For further information on technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies Office (www.Infineon.com).

Warnings