

# AN90B00/AN90B00S Series

## Transistor Arrays

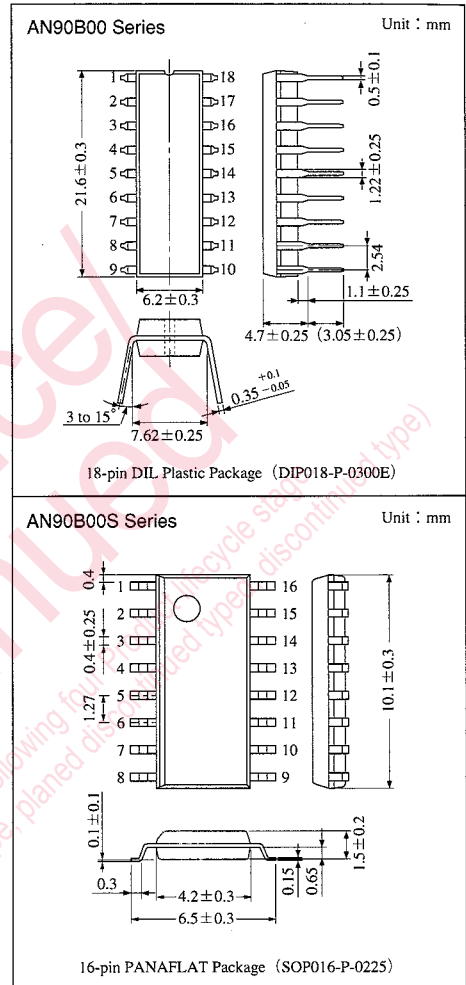
### Overview

The transistor array, the AN90B00 and the AN90B00S series, includes the circuits with eight transistors connected in emitter-common style (seven transistors in AN90B00S series) and also the ones with five independent transistors integrated on a single chip.

The 18-DIL and PANAFLAT (SO-16D) packages are used in the AN90B00 series and the AN90B00S series respectively.

### Features

- Output current :  $I_O = 25\text{mA}$
- Breakdown voltage :  $V_{CEO} = 24\text{V}$
- Base current limiting resistor built-in.
- Output breakdown voltage protection diode built-in



### Block Diagram

Basic circuit				
Type No.				
AN90B01S	●			
AN90B10	●			
AN90B20, AN90B20S		●		
AN90B21, AN90B21S			●	
AN90B22, AN90B22S			●	
AN90B60, AN90B60S	●			
AN90B70, AN90B70S		●		
AN90B81, AN90B81S				●
AN90B82S				●

Note) A type No. ending with S stands for PANAFLAT package. (SO package)

Others

### ■ Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	50	V
Collector-substrate voltage	V <sub>CIO</sub>	50	V
Collector-emitter voltage	V <sub>CEO</sub>	24	V
	V <sub>CER</sub> *1	50	
Emitter-base voltage	V <sub>EBO</sub> *2	5	V
Collector current	I <sub>C</sub>	25	mA
Collector power dissipation	P <sub>C</sub> *3	200	mW
Power dissipation	P <sub>D</sub>	1000 *4	mW
		380 *5	
Operating ambient temperature	T <sub>opr</sub>	-30 to +75	°C
Storage temperature	T <sub>stg</sub>	-55 to +150 *4	°C
		-55 to +125 *5	

\*1 AN90B21/21S \*2 AN90B10/20/20S/21/21S/22/22S are excluded. \*3 Allowable value per transistor

\*4 Allowable value per 18-DIL package \*5 Allowable value per SO-16D package

### ■ Electrical Characteristics (Ta=25°C)

#### ● Common specification

Parameter	Symbol	Condition	min	typ	max	Unit
Collector-emitter voltage	V <sub>CEO</sub>	I <sub>C</sub> = 1mA, I <sub>B</sub> = 0	24	—	—	V
Collector-base voltage	V <sub>CBO</sub>	I <sub>C</sub> = 10 μA, I <sub>E</sub> = 0	50	—	—	V
Emitter-base voltage	V <sub>EBO</sub>	I <sub>E</sub> = 10 μA, I <sub>C</sub> = 0	0.5	—	—	V
Collector cutoff current	I <sub>CEO</sub>	V <sub>CE</sub> = 10V, R <sub>BE</sub> = ∞	—	—	1	μA

#### ● AN90B01S/10/60/60S (No base current limit resistor)

Parameter	Symbol	Condition	min	typ	max	Unit
Collector-emitter saturation voltage	V <sub>CE(sat)1</sub>	I <sub>C</sub> = 1mA, I <sub>B</sub> = 0.1mA	—	0.1	0.2	V
	V <sub>CE(sat)2</sub>	I <sub>C</sub> = 10mA, I <sub>B</sub> = 1mA	—	0.25	0.4	V
Base-emitter saturation voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> = 10mA, I <sub>B</sub> = 1mA	—	0.88	1	V
DC current amplification factor	h <sub>FE1</sub>	V <sub>CE</sub> = 3V, I <sub>C</sub> = 1mA	50	120	—	—
	h <sub>FE2</sub>	V <sub>CE</sub> = 3V, I <sub>C</sub> = 10mA	50	100	—	—
Output voltage	V <sub>O</sub>	V <sub>I</sub> = V <sub>C</sub> = 5V, I <sub>O</sub> = 1mA	4	4.3	—	V

#### ● AN90B20/20S/70/70S (With base current limit resistor)

Parameter	Symbol	Condition	min	typ	max	Unit
Collector-emitter saturation voltage	V <sub>CE(sat)1</sub>	I <sub>C</sub> = 1mA, I <sub>B</sub> = 0.1mA	—	0.1	0.2	V
	V <sub>CE(sat)2</sub>	I <sub>C</sub> = 10mA, I <sub>B</sub> = 1mA	—	0.25	0.4	V
Input voltage	V <sub>I1</sub>	V <sub>CE</sub> = 0.2V, I <sub>C</sub> = 1mA	—	0.85	1.2	V
	V <sub>I2</sub>	V <sub>CE</sub> = 0.4V, I <sub>C</sub> = 10mA	—	2.3	3.5	V
DC current amplification factor	h <sub>FE1</sub>	V <sub>CE</sub> = 3V, I <sub>C</sub> = 1mA	50	120	—	—
	h <sub>FE2</sub>	V <sub>CE</sub> = 3V, I <sub>C</sub> = 10mA	50	100	—	—
Output voltage	V <sub>O</sub>	V <sub>I</sub> = V <sub>C</sub> = 5V, I <sub>O</sub> = 1mA	3.8	4.3	—	V

### ■ Electrical Characteristics (cont.) ( $T_a=25^\circ\text{C}$ )

#### ● AN90B21/21S

Parameter	Symbol	Condition	min	typ	max	Unit
Collector-emitter saturation voltage	$V_{CE(sat)1}$	$I_C=1\text{mA}, I_I=0.4\text{mA}$	—	0.1	0.2	V
	$V_{CE(sat)2}$	$I_C=10\text{mA}, I_I=1.4\text{mA}$	—	0.25	0.4	V
Input voltage	$V_{I1}$	$V_{CE}=0.2\text{V}, I_C=1\text{mA}$	—	3.1	4.1	V
	$V_{I2}$	$V_{CE}=0.4\text{V}, I_C=10\text{mA}$	—	4.2	5.8	V
Input current	$I_{I1}$	$I_C=10\text{mA}, V_I=5\text{V}$	—	0.55	1.1	mA
	$I_{I2}$	$I_C=10\text{mA}, V_I=10\text{V}$	—	1.2	2.0	mA

#### ● AN90B22/22S

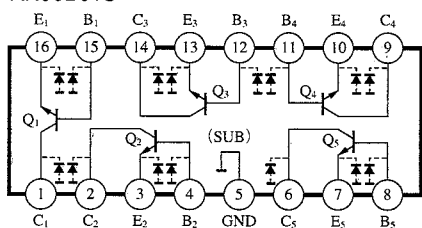
Parameter	Symbol	Condition	min	typ	max	Unit
Collector-emitter saturation voltage	$V_{CE(sat)1}$	$I_C=1\text{mA}, I_I=0.3\text{mA}$	—	0.1	0.2	V
	$V_{CE(sat)2}$	$I_C=10\text{mA}, I_I=1.2\text{mA}$	—	0.3	0.4	V
Input voltage	$V_{I1}$	$V_{CE}=0.2\text{V}, I_C=1\text{mA}$	—	1.4	2.0	V
	$V_{I2}$	$V_{CE}=0.4\text{V}, I_C=10\text{mA}$	—	1.9	3.0	V
Input current	$I_{I1}$	$I_C=1\text{mA}, V_I=5\text{V}$	—	0.9	1.1	mA
	$I_{I2}$	$I_C=10\text{mA}, V_I=10\text{V}$	—	1.9	2.3	mA

#### ● AN90B81/81S/82S (With output breakdown protect diode)

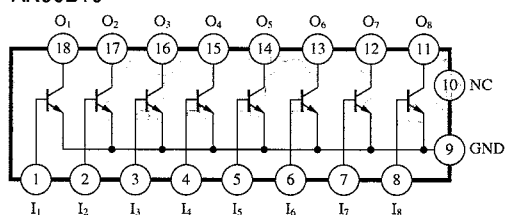
Parameter	Symbol	Condition	min	typ	max	Unit
Collector-emitter saturation voltage	$V_{CE(sat)1}$	$I_C=1\text{mA}, I_B=0.1\text{mA}$	—	0.85	1.1	V
	$V_{CE(sat)2}$	$I_C=10\text{mA}, I_B=1\text{mA}$	—	1.15	1.4	V
Input voltage	AN90B81/81S $V_{I1}$	$V_{CE}=1.1\text{V}, I_C=1\text{mA}$	—	1.6	2.2	V
	AN90B82S $V_{I2}$	$V_{CE}=1.4\text{V}, I_C=10\text{mA}$	—	3.0	4.5	V
DC current amplification factor	$h_{FE1}$	$V_{CE}=3\text{V}, I_O=1\text{mA}$	25	60	—	—
	$h_{FE2}$	$V_{CE}=3\text{V}, I_O=10\text{mA}$	20	50	—	—
Output voltage	AN90B81/81S AN90B82S $V_O$	$V_I=V_C=5\text{V}, I_O=1\text{mA}$	2.9	3.3	—	V
Diode reverse voltage	$V_R$	$I_R=10\mu\text{A}, I_C=0$	50	—	—	V
Diode leakage current	$I_R$	$V_{EB}=10\text{V}, I_C=0$	—	—	1	$\mu\text{A}$

■ Schematic Diagram

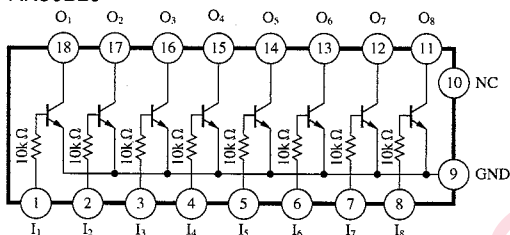
AN90B01S



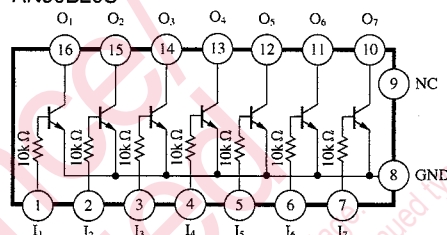
AN90B10



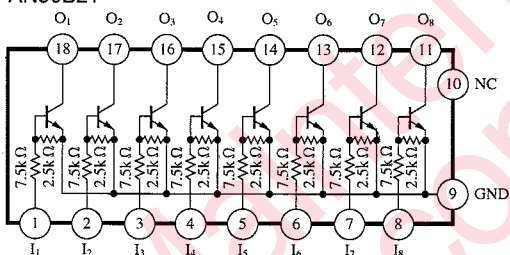
AN90B20



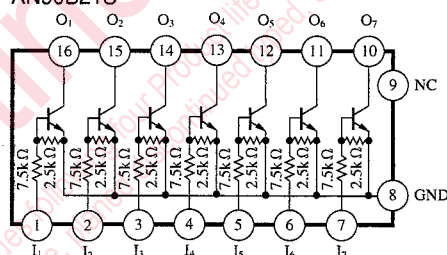
AN90B20S



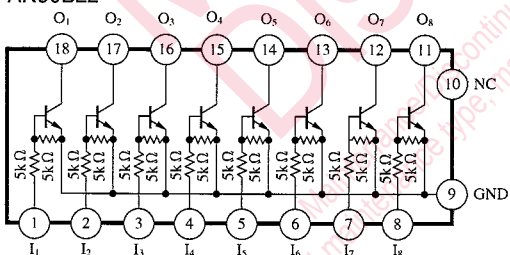
AN90B21



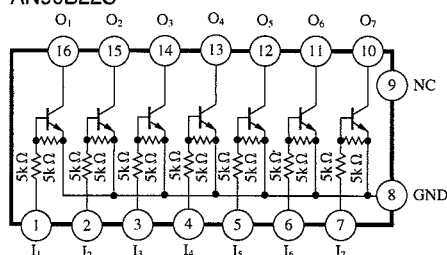
AN90B21S



AN90B22



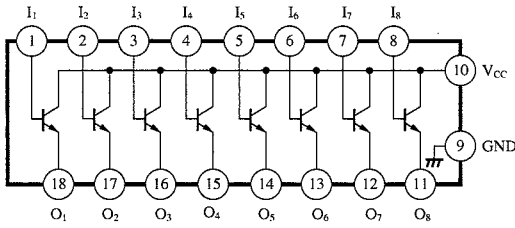
AN90B22S



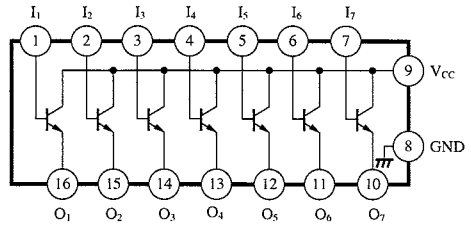
Note) I...Input O...Output

■ Schematic Diagram (cont.)

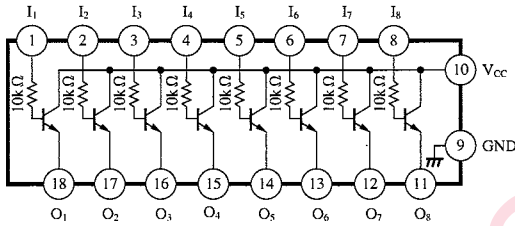
AN90B60



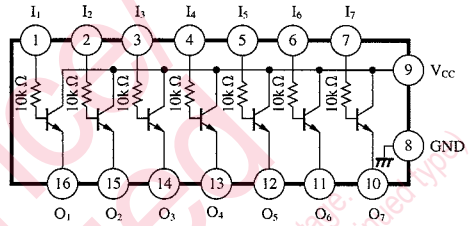
AN90B60S



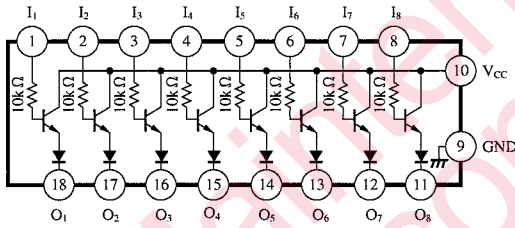
AN90B70



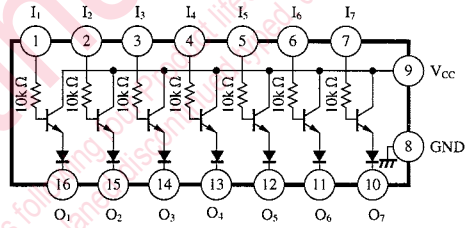
AN90B70S



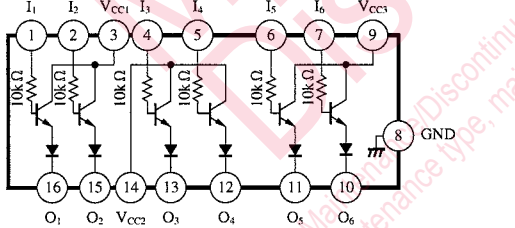
AN90B81



AN90B81S



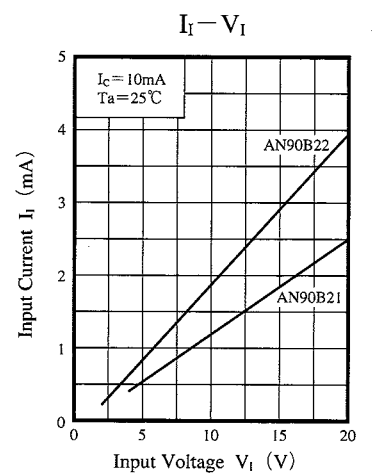
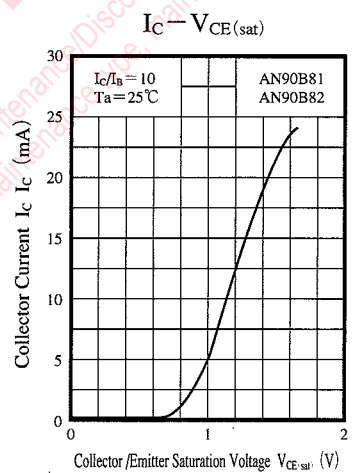
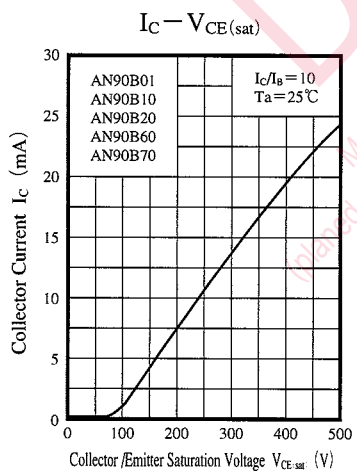
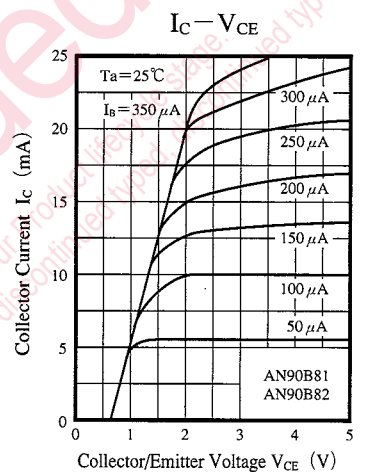
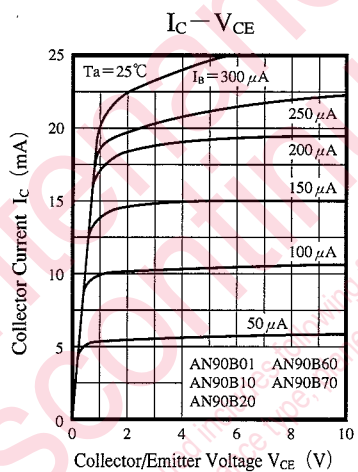
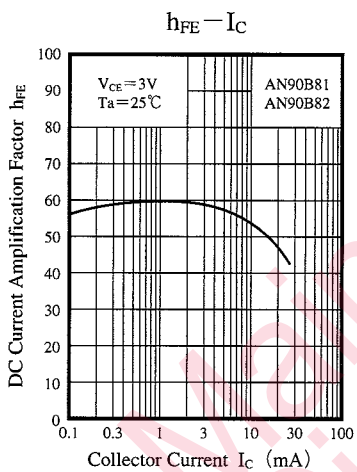
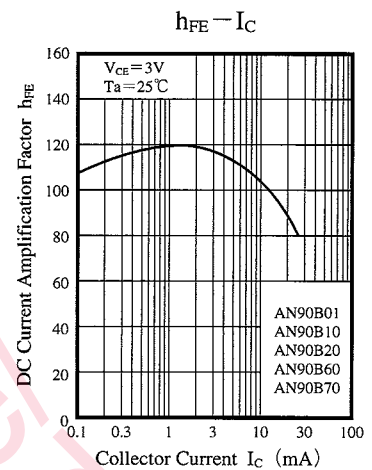
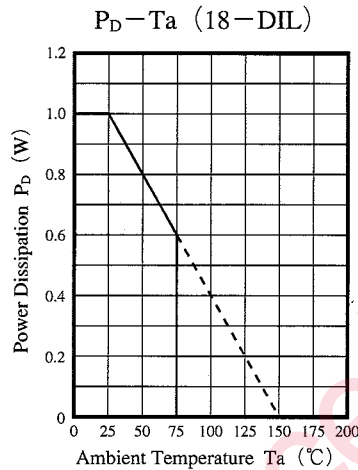
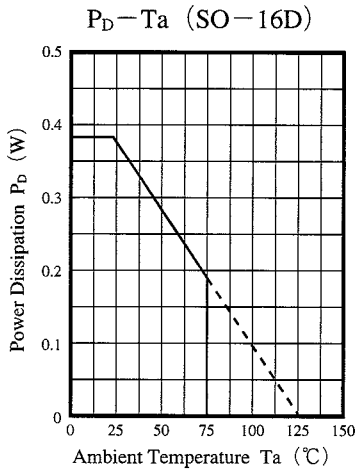
AN90B82S



Note) I ... Input O ... Output



Characteristics Curve



## Request for your special attention and precautions in using the technical information and semiconductors described in this book

- (1) If any of the products or technical information described in this book is to be exported or provided to non-residents, the laws and regulations of the exporting country, especially, those with regard to security export control, must be observed.
- (2) The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products. No license is granted in and to any intellectual property right or other right owned by Panasonic Corporation or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information described in this book.
- (3) The products described in this book are intended to be used for standard applications or general electronic equipment (such as office equipment, communications equipment, measuring instruments and household appliances).  
Consult our sales staff in advance for information on the following applications:
  - Special applications (such as for airplanes, aerospace, automobiles, traffic control equipment, combustion equipment, life support systems and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
  - Any applications other than the standard applications intended.
- (4) The products and product specifications described in this book are subject to change without notice for modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.
- (5) When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Otherwise, we will not be liable for any defect which may arise later in your equipment.
  - Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
- (6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.
- (7) This book may be not reprinted or reproduced whether wholly or partially, without the prior written permission of our company.