

32 K × 8 3.3 Volts High Speed CMOS SRAM

Short description. Please refer to the full datasheet available on TEMIC web for detailed technical information.

Description

The L 65756 is a high speed CMOS static RAM organised as 32,768 × 8 bits. It is manufactured using TEMIC's high performance CMOS technology.

The L 65756 provides fast access time of 25 ns for a 3 volts power supply.

The L 65756 features fully static operation requiring no external clocks or timing strobes. The automatic power-down feature reduces the power consumption by 80 % when the circuit is deselected.

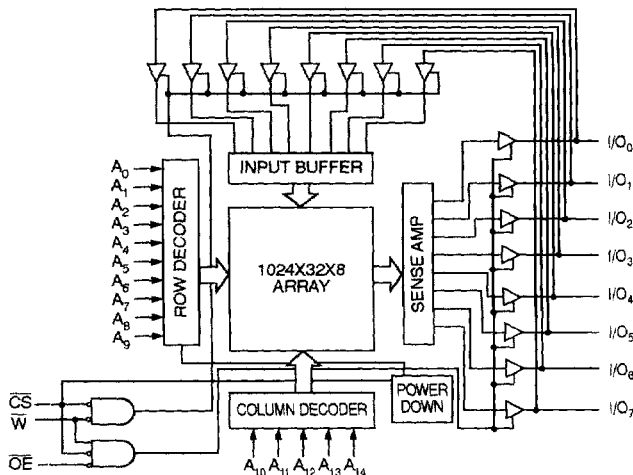
Easy memory expansion is provided by an active low chip select (\overline{CS}) an active low output enable (\overline{OE}), and three state drivers.

Features

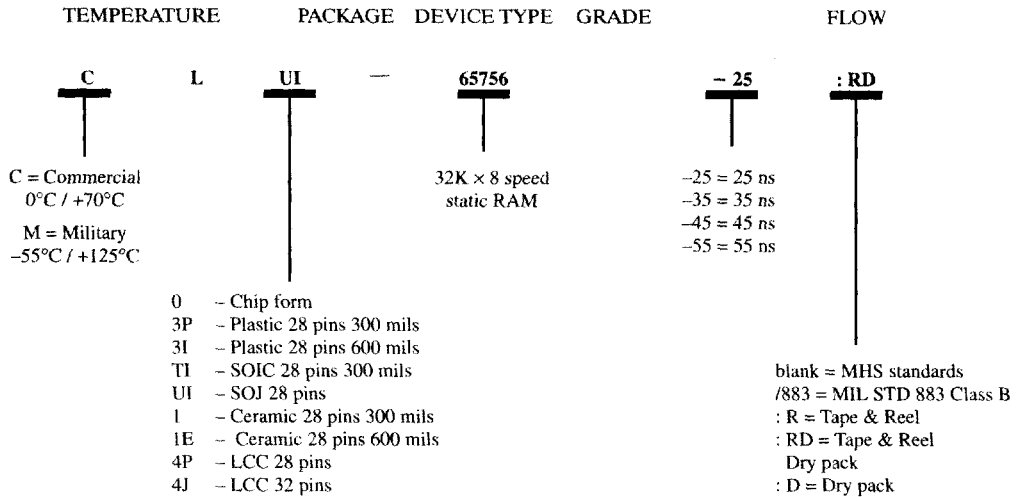
- Single supply 3.3 ± 0.3 Volts
- Fast access time
- Commercial : 25/35/45/55 ns (max)
- Military : 35/45/55 ns (max)
- Low power consumption
Active : 290 mW
Standby : 72 mW
- Asynchronous

Interface

Block Diagram



Ordering Information



Military Version

The following table gives package/access time/process flow available combinations

Temp. range	Package	Access Time (ns)			Std process 65756
		35 (K)	45 (M)	55 (N)	Mil flows
M	J	X	X	X	X
	1E	X	X	X	X
	4J	X	X	X	X
	4P	X	X	X	X
	0	X	X	X	X

● = product in production

X = call sales office for availability

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