Silicon NPN Epitaxial

HITACHI

ADE-208-1482 (Z)

Rev.0 Feb. 2002

Features

• Low frequency amplifier

Outline

SMPAK	
	1. Emitter 2. Base 3. Collector



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

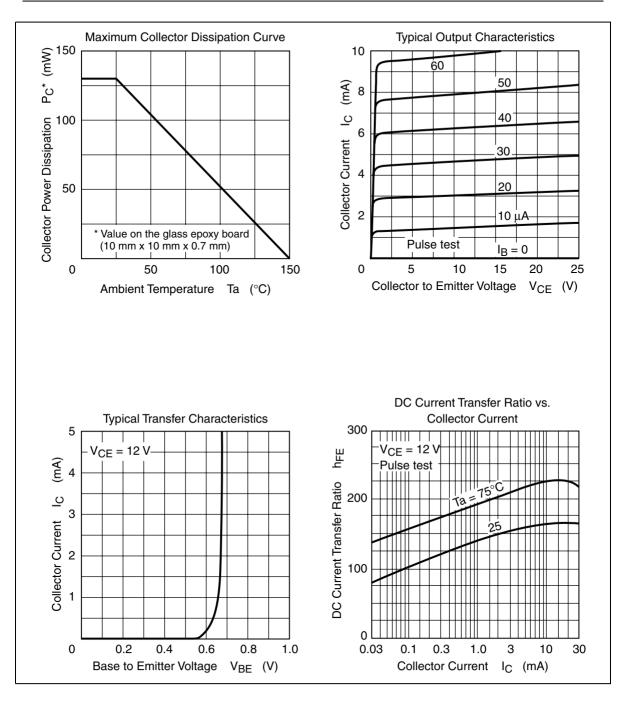
Item	Symbol	Ratings	Unit
Collector to base voltage	V _{CBO}	50	V
Collector to emitter voltage	V _{CEO}	40	V
Emitter to base voltage	V _{EBO}	5	V
Collector current	I _c	100	mA
Emitter current	I _E	-100	mA
Collector power dissipation	P _c *	130	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

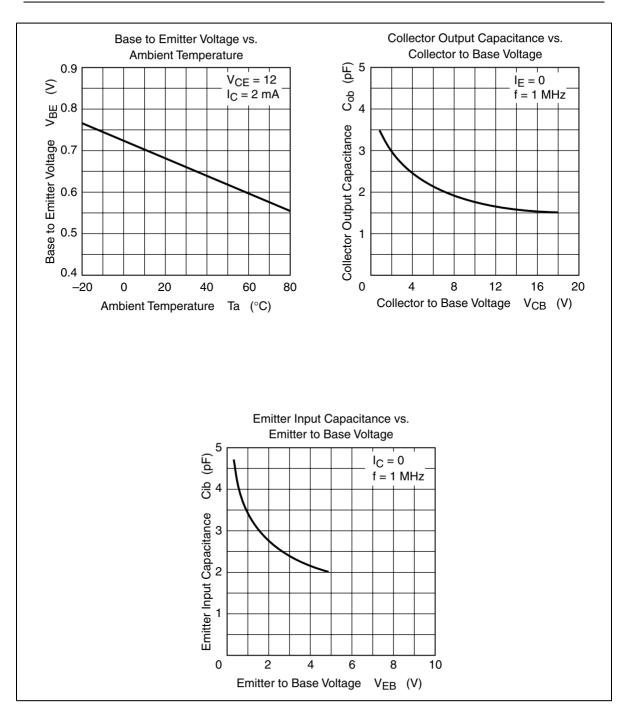
*Value on the glass epoxy board (10 mm x 10 mm x 0.7 mm)

Electrical Characteristics

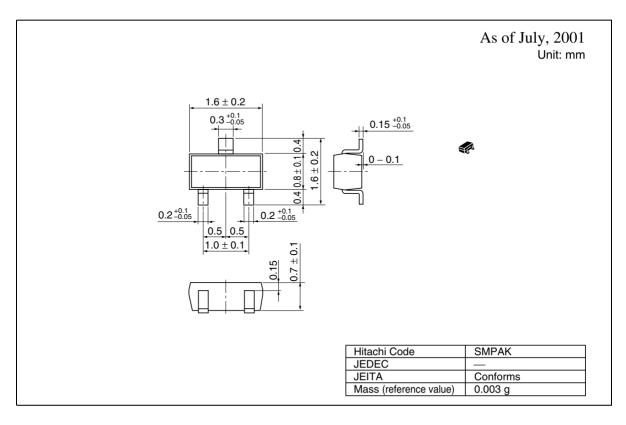
 $(Ta = 25^{\circ}C)$

Item			Symbol	Min	Тур	Мах	Unit	Test conditions
Collector to voltage	o base brea	kdown	$V_{_{(BR)CBO}}$	50	_	—	V	$I_{c} = 10 \ \mu A, \ I_{E} = 0$
Collector to emitter breakdown voltage		$V_{\scriptscriptstyle (BR)CEO}$	40	_	—	V	$I_c = 1 \text{ mA}, \text{ R}_{BE} = \infty$	
Emitter to base breakdown voltage		$V_{_{(BR)EBO}}$	5		—	V	$I_{\rm E} = 10 \ \mu {\rm A}, \ I_{\rm C} = 0$	
Collector c	utoff curren	ıt	I _{cbo}			0.5	μA	$V_{_{CB}} = 30 \text{ V}, \text{ I}_{_{E}} = 0$
Emitter cutoff current		I _{EBO}			0.5	μA	$V_{_{EB}} = 2 V, I_{_{C}} = 0$	
DC current transfer ratio		h_{FE}^{*1}	100	_	500	_	$V_{ce} = 12 \text{ V}, \text{ I}_{c} = 2 \text{ mA}$	
Collector to voltage	o emitter sa	turation	$V_{\text{CE(sat)}}$	—	—	0.2	V	$I_{c} = 10 \text{ mA}, I_{B} = 1 \text{ mA}$
Base to emitter voltage		$V_{_{BE}}$			0.75	V	$V_{ce} = 12 \text{ V}, \text{ I}_{c} = 2 \text{ mA}$	
Notes: 1. The 2SC5862 is grouped by h _{FE} as follows.								
	Grade	В	C)		D		
	Mark	LB	L	C		LD		
	$h_{_{FE}}$	100 to 2	00 1	60 to 3	20	250 to 500	0	





Package Dimensions



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