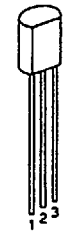
P-CHANNEL JFETs



TO-92/TO-226AA

ELECTRICAL CHARACTERISTICS at T_A = 25°C

Device Type	$V_{(BR)GSS}$		I_{GSS}		$V_{GS(off)}$ Limits		$V_{GS(off)}$ Conditions		I_{DSS}			g_{fs}			C_{iss}[1]		C_{rss}[1]		r_{DS}	Pinning 1, 2, 3
	Min. (V)	@I_G (μA)	Max. (nA)	@ V_{DS} (V)	Min. (V)	Max. (V)	V_{DS} (V)	I_D (nA)	Min. (mA)	Max. (mA)	@ V_{DS} (V)	Min. (mS)	Max. (mS)	@ V_{DS} (V)	Max. (pF)	@ V_{DS} (V)	Max. (pF)	@ V_{DS} (V)	Max. (Ω)	
2N3820	20	10	20	10	—	8.0	-10	-10[2]	-0.3	-15	-10	0.8	5.0	-10	32	-10	16	-10	—	DGS†
TP3993	25	1.0	1.0	15	4.0	9.5	-10	-1[2]	-10	—	-10	6.0	12	-10	16	-10	5.5	10[3]	150	DSG‡
TP3994	25	1.0	1.0	15	1.0	5.5	-10	-1[2]	-2.0	—	-10	4.0	10	-10	16	-10	5.5	10[3]	300	DSG‡
TP4381	25	1.0	1.0	15	1.0	5.0	-15	-1.0[2]	-3.0	-12	-15	2.0	6.0	-15	20	-15	5.0	-15	—	DSG‡
2N5460	40	10	5.0	20	0.75	6.0	-15	-1.0	-1.0	-5.0	-15	1.0	5.0	-15	7.0	-15	3.0	-15	—	DSG‡
2N5461	40	10	5.0	20	1.0	7.5	-15	-1.0	-2.0	-9.0	-15	1.5	5.5	-15	7.0	-15	3.0	-15	—	DSG‡
2N5462	40	10	5.0	20	1.8	9.0	-15	-1.0	-4.0	-16	-15	2.0	6.0	-15	7.0	-15	3.0	-15	—	DSG‡
J174	30	1.0	1.0	20	5.0	10	-15	-10	-20	-135	-15	—	—	—	—	—	—	—	85	DSG†
J175	30	1.0	1.0	20	3.0	6.0	-15	-10	-7.0	-70	-15	—	—	—	—	—	—	—	125	DSG†
J176	30	1.0	1.0	20	1.0	4.0	-15	-10	-2.0	-35	-15	—	—	—	—	—	—	—	250	DSG†
J177	30	1.0	1.0	20	0.8	2.25	-15	-10	-1.5	-20	-15	—	—	—	—	—	—	—	300	DSG†
TPU304	30	1.0	1.0	20	5.0	10	-15	-1[2]	-30	-90	-15	—	—	—	27	-15	7.0	12[3]	85	DSG‡
TPU305	30	1.0	1.0	20	3.0	6.0	-15	-1[2]	-15	-60	-15	—	—	—	27	-15	7.0	7.0[3]	110	DSG‡
TPU306	30	1.0	1.0	20	1.0	4.0	-15	-1[2]	-5.0	-25	-15	—	—	—	27	-15	7	5.0[3]	175	DSG‡

NOTES: † Reversed pinning (S-G-D) available on special order—add suffix letter 'R' to part number.

‡ Reversed pinning (S-D-G) available on special order—add suffix letter 'R' to part number.

1) V_{GS} = 0 V.

2) I_D in μA.

3) V_{DS} = 0 V, V_{GS} in volts.

4) V_{GS} = 1.0 V.