TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH34F, TC7SH34FU

Non-Inverter Buffer

Features

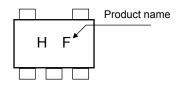
• High speed operation : t_{pd} = 3.8ns (typ.) at V_{CC} = 5 V, 15 pF

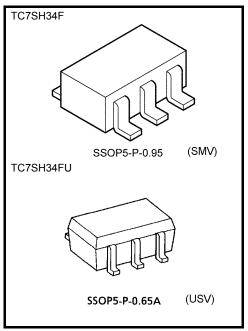
Low power dissipation : I_{CC} = 2μA (max) at Ta = 25°C
 High noise immunity : V_{NIH} = V_{NIL} = 28% V_{CC} (min)

5.5-V tolerant input.

Wide operating voltage range: V_{CC}= 2 to 5.5 V

Marking





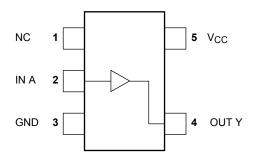
Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	l _{IK}	-20	mA
Output diode current	lok	±20 (Note1)	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	I _{CC}	±50	mA
Power dissipation	PD	200	mW
Storage temperature	T _{stg}	-65 to 150	°C
Lead temperature (10 s)	TL	260	°C

Pin Assignment (top view)



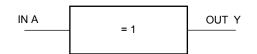
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: V_{OUT} < GND, V_{OUT} > V_{CC}

IEC Logic Symbol

Truth Table



Α	Υ
L	L
Н	Н

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	٧
Operating temperature	T _{opr}	–40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V_{CC} = 3.3 V \pm 0.3 V)	ns/V
	ui/uv	0 to 20 (V _{CC} = 5.0 V \pm 0.5 V)	113/ V

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Toot	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics	Symbol	rest condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
High-level input voltage V _{IH} —			2.0	1.50	_	_	1.50			
		_	3.0 to 5.5	V _{CC} × 0.7			V _{CC} × 0.7		V	
				2.0	_		0.5	_	0.5	v
Low-level input voltage V _{IL} —	_	3.0 to 5.5	_	_	V _{CC} × 0.3	_	V _{CC} × 0.3			
		V _{IN} = V _{IH}	I _{OH} = -50 μA	2.0	1.9	2.0	_	1.9	_	V
	Voн			3.0	2.9	3.0	_	2.9	_	
High-level output voltage				4.5	4.4	4.5	_	4.4	_	
			$I_{OH} = -4 \text{ mA}$	3.0	2.58	_	_	2.48	_	
			$I_{OH} = -8 \text{ mA}$	4.5	3.94	1		3.80	1	
		$V_{IN} = V_{IL}$	I _{OL} = 50 μA	2.0	_	0.0	0.1	_	0.1	
				3.0	_	0.0	0.1	_	0.1	
Low-level output voltage	V_{OL}			4.5	_	0.0	0.1	_	0.1	
			I _{OL} = 4 mA	3.0	_		0.36	_	0.44	
			I _{OL} = 8 mA	4.5	_		0.36	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	± 0.1	_	± 1.0	μΑ
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μΑ

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	_	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
			V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	tPLH tPHL	3.3 ± 0.3	15		5.0	7.1	1.0	8.5		
			3.5 ± 0.5	50	_	7.5	10.6	1.0	12.0	- ns
			5.0 ± 0.5	15	_	3.8	5.5	1.0	6.5	
		5.0 ± 0.5	50	_	5.3	7.5	1.0	8.5		
Input capacitance	C _{IN}		_		1	4	10	_	10	pF
Power dissipation capacitance	C _{PD}			(Note 2)		13		_	_	pF

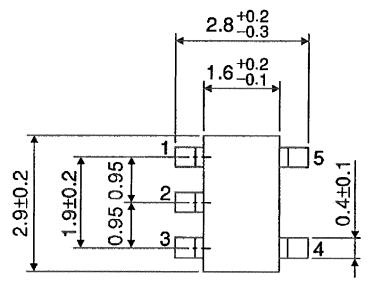
Note 2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

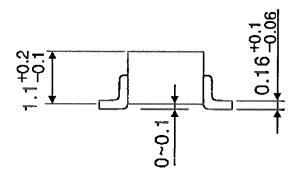
Average operating current can be obtained by the equation.

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions

SSOP5-P-0.95 Unit: mm





Weight: 0.016 g (typ.)

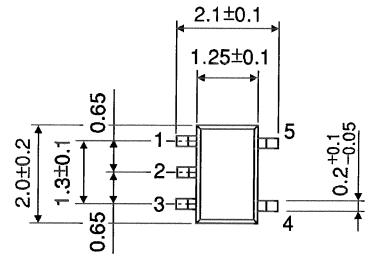
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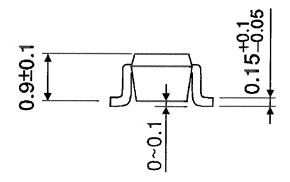


Package Dimensions

SSOP5-P-0.65A Unit: mm

TC7SH34F/FU





Weight: 0.006 g (typ.)

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