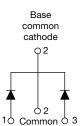


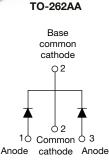
Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 10 A



D²PAK (TO-263AB)





VS-20CTQ...S-M3

cathode Anode

Anode

VS-20CTQ ... -1-M3

PRIMARY CHARACTERISTICS								
I _{F(AV)}	2 x 10 A							
V _R	35 V, 40 V, 45 V							
V _F at I _F	0.57 V							
I _{RM}	15 mA at 125 °C							
T _J max.	175 °C							
E _{AS}	13 mJ							
Package	D ² PAK (TO-263AB), TO-262AA							
Circuit configuration	Common cathode							

FEATURES

- 175 °C T_J operation
- · Center tap configuration
- · Low forward voltage drop
- High frequency operation
- purity. • Hiah high temperature epoxv encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-20CTQ... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

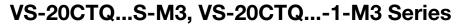
MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	20	А						
V _{RRM}	Range	35 to 45	V						
I _{FSM}	t _p = 5 μs sine	1060	А						
V _F	10 A _{pk} , T _J = 125 °C (per leg)	0.57	V						
TJ	Range	-55 to +175	°C						

VOLTAGE RATINGS

PARAMETER	SYMBOL	VS-20CTQ035S-M3 VS-20CTQ035-1-M3	VS-20CTQ040S-M3 VS-20CTQ040-1-M3	VS-20CTQ045S-M3 VS-20CTQ045-1-M3	UNITS
Maximum DC reverse voltage	V _R	35	40	45	V
Maximum working peak reverse voltage	V _{RWM}		40	45	v

Revision: 21-Dec-2021

1



www.vishay.com

SHAY

Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS					
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at $T_C = 145$ °C	20							
Maximum peak one cycle non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load	1060	A					
surge current per leg See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	265						
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 2.0 \ A, \ L = 6.5$	13	mJ						
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim	2.0	А						

ELECTRICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS							
		10 A	T ₁ = 25 °C	0.64	v					
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	20 A	1j=25 C	0.76						
See fig. 1	VFM ()	10 A	T ₁ = 125 °C	0.57						
		20 A	1j=125 C	0.68						
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C		2	mA					
See fig. 2		T _J = 125 °C	V _R = Rated V _R	15						
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		pF					
Typical series inductance per leg	L _S	Measured lead to lead 5 mm	8.0	nH						
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs						

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 175	°C				
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation See fig. 4	3.25					
Maximum thermal resistance, junction to case per package		nthJC	DC operation	1.63	°C/W				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50					
Approximate weight				2	g				
Approximate weight				0.07	oz.				
Mounting torque	minimum			6 (5)	kgf ⋅ cm				
Mounting torque	maximum			12 (10)	(lbf · in)				
			Case style D ² PAK (TO-263AB)	20CTC 20CTC 20CTC	040S				
			Case style TO-262AA	20CTC 20CTC 20CTC	040-1				





VS-20CTQ...S-M3, VS-20CTQ...-1-M3 Series

Vishay Semiconductors

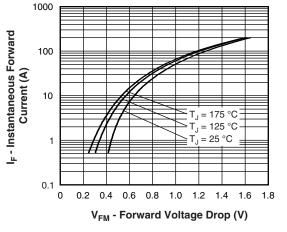


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

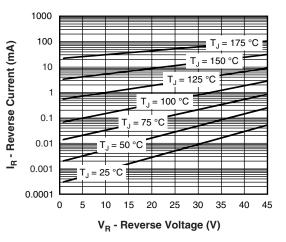


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

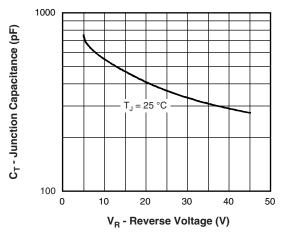
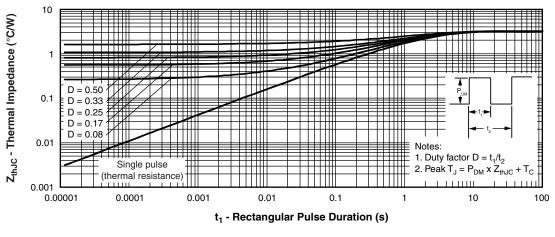


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)



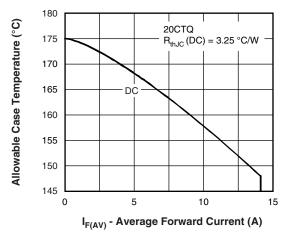


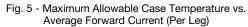
Revision: 21-Dec-2021	3	Document Number: 94932
For technical questions within your region:	DiodesAmericas@vishay.com, DiodesAsia@visha	<u>iy.com, DiodesEurope@vishay.com</u>
	E WITHOUT NOTICE. THE PRODUCTS DESCRIE CIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay</u>	

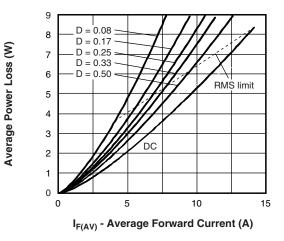


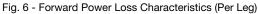
VS-20CTQ...S-M3, VS-20CTQ...-1-M3 Series

Vishay Semiconductors









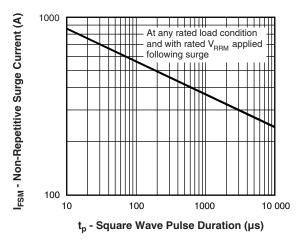


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

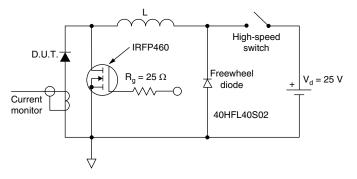


Fig. 8 - Unclamped Inductive Test Circuit

(1) Formula weeks

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 Revision: 21-Dec-2021
 4
 Document Number: 94932

 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com
 DiodesEurope@vishay.com

 THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



VS-20CTQ...S-M3, VS-20CTQ...-1-M3 Series

Vishay Semiconductors

 $\begin{array}{l} \mbox{Pd} = \mbox{forward power loss} = \mbox{I}_{F(AV)} \ x \ V_{FM} \ at \ (\mbox{I}_{F(AV)}/D) \ (see \ fig. \ 6); \\ \mbox{Pd}_{REV} = \ inverse \ power \ loss = \ V_{R1} \ x \ I_R \ (1 \ - \ D); \ I_R \ at \ V_{R1} = \ 80 \ \% \ rated \ V_R \ applied \end{array}$

ORDERING INFORMATION TABLE

Device code	VS-	20	С	т	Q	045	S	TRL	-M3
		(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)
	<u> </u>	Visł	\bigcirc	nicondu	0	oduct	\bigcirc	\bigcirc	\bigcirc
	2 -		-	ng (20 A	•				
	3 -	Circ	cuit con	figuratio	n: C = c	commor	n catho	de	
	4 -	- T =	TO-220)					
	5 -	Sch	ottky "O	Q" series	s [035 = 3	5 V		
	6 -	Vol	tage rati	ings —		040 = 4			
	7 -	• S	= D ² PA	K (TO-2	63AB)	045 = 4	5 V		
	_	• -1	= TO-2	262AA					
	8 -	• N	one = tı	lbe					
		• TI	RL = tap	be and r	eel (left	orienteo	d - for D) ² PAK (1	FO-263/
	_	• TI	RR = tap	be and r	eel (righ	nt oriente	ed - for	D ² PAK	(TO-26
	9 -	-M3	3 = halo	gen-free	e, RoHS	S-compl	iant, an	d termiı	nation le

ORDERING INFORMATION		
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION
VS-20CTQ035S-M3	50	Antistatic plastic tubes
VS-20CTQ035STRL-M3	800	13" diameter plastic tape and reel
VS-20CTQ035STRR-M3	800	13" diameter plastic tape and reel
VS-20CTQ040S-M3	50	Antistatic plastic tubes
VS-20CTQ040STRL-M3	50	13" diameter plastic tape and reel
VS-20CTQ040STRR-M3	800	13" diameter plastic tape and reel
VS-20CTQ045S-M3	800	Antistatic plastic tubes
VS-20CTQ045STRL-M3	50	13" diameter plastic tape and reel
VS-20CTQ045STRR-M3	50	13" diameter plastic tape and reel
VS-20CTQ150S-M3	800	Antistatic plastic tubes
VS-20CTQ150STRL-M3	800	13" diameter plastic tape and reel
VS-20CTQ150STRR-M3	50	13" diameter plastic tape and reel
VS-20CTQ045-1-M3	50	Antistatic plastic tubes

LINKS TO RELATED DOCUMENTS								
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164						
	TO-262AA	www.vishay.com/doc?96165						
Part marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444						
	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?96424						

Revision: 21-Dec-2021

Document Number: 94932

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

www.vishay.com

SHA



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

1

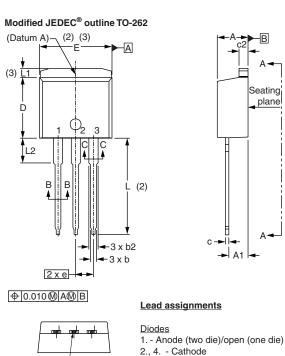
Outline Dimensions



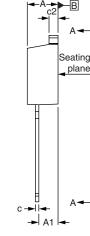
Vishay Semiconductors

TO-262

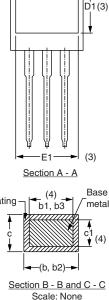
DIMENSIONS in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 _ 1.65 0.065 3 _ 3.36 0.132 0.146 L2 3.71

3. - Anode

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019

Document Number: 95419

1 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.