Tautation	DRC4	4152Z
Tentative	Total pages	page

DRC4152Z

Silicon NPN epitaxial planar type

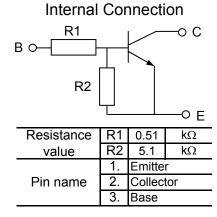
For digital circuits

Marking Symbol : N0

Package Code : NS-B1-B-B

Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	VCBO	50	V
Collector-emitter voltage (Base open)	VCEO	50	V
Collector current	IC	100	mA
Total power dissipation	PT	300	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	С°



Electrical Characteristics Ta = 25 °C±3 °C

	0 0 10	•				
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	VCBO	IC = 10 μA, IE = 0	50			V
Collector-emitter voltage (Base open)	VCEO	IC = 2 mA, IB = 0	50			V
Collector-base cutoff current (Emitter open)	ICBO	VCB = 50 V, IE = 0			0.1	μA
Collector-emitter cutoff current (Base open)	ICEO	VCE = 50 V, IB = 0			0.5	μA
Emitter-base cutoff current (Collector open)	IEBO	VEB = 6 V, IC = 0			2.0	mA
Forward current transfer ratio	hFE	VCE = 10 V, IC = 5 mA	20			-
Collector-emitter saturation voltage	VCE(sat)	IC = 10 mA, IB = 0.5 mA			0.25	V
Input voltage	Vi(on)	VCE = 0.2 V, IC = 5 mA	1.0			V
Input voltage	Vi(off)	VCE = 5 V, IC = 100 µA			0.4	V
Input resistance	R1		-30%	0.51	+30%	kΩ
Resistance ratio	R1/R2		0.08	0.10	0.12	-

Note: Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

Packing Radial type : 5000 pcs / carton

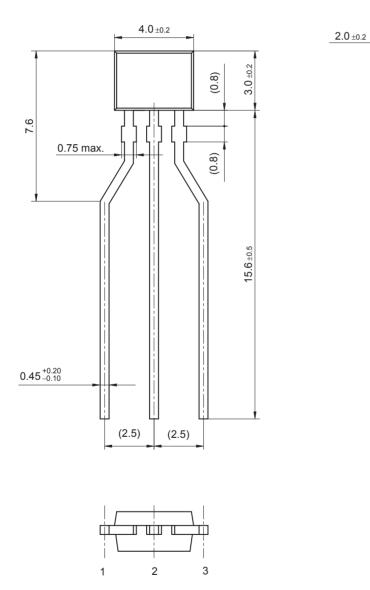
2010.2.23	2010.8.17
Prepared	Revised

NS-B1-B-B

Unit: mm

 $0.45 \, {}^{+0.20}_{-0.10}$

0.7 ±0.1



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