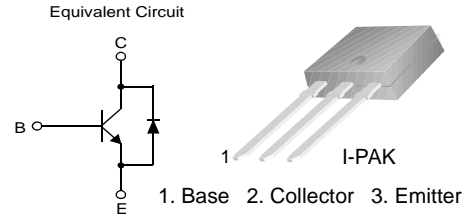


## KSC5302DI

KSC5302DI

### High Voltage & High Speed Power Switch Application

- Built-in Free-wheeling Diode makes efficient anti saturation operation  
Suitable for half-bridge light ballast Applications
- No need to interest an hFE value because of low variable storage-time spread even though corner spirit
- Low base drive requirement



### NPN Silicon Transistor

#### Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

| Symbol    | Parameter                                   | Value      | Units            |
|-----------|---|------------|------------------|
| $V_{CBO}$ | Collector-Base Voltage                      | 800        | V                |
| $V_{CEO}$ | Collector-Emitter Voltage                   | 400        | V                |
| $V_{EBO}$ | Emitter-Base Voltage                        | 12         | V                |
| $I_C$     | Collector Current (DC)                      | 2          | A                |
| $I_{CP}$  | *Collector Current (Pulse)                  | 5          | A                |
| $I_B$     | Base Current (DC)                           | 1          | A                |
| $I_{BP}$  | *Base Current (Pulse)                       | 2          | A                |
| $P_C$     | Power Dissipation( $T_C=25^\circ\text{C}$ ) | 25         | W                |
| $T_J$     | Junction Temperature                        | 150        | $^\circ\text{C}$ |
| $T_{STG}$ | Storage Temperature                         | - 55 ~ 150 | $^\circ\text{C}$ |

#### Thermal Characteristics $T_C=25^\circ\text{C}$ unless otherwise noted

| Symbol          | Characteristics    |                     | Rating | Unit               |
|-----------------|--------------------|---------------------|--------|--------------------|
| $R_{\theta jc}$ | Thermal Resistance | Junction to Case    | 5.0    | $^\circ\text{C/W}$ |
| $R_{\theta ja}$ |                    | Junction to Ambient | 83.3   |                    |

**Electrical Characteristics**  $T_C=25^\circ\text{C}$  unless otherwise noted

| Symbol                 | Parameter   | Test Condition   | Min.        | Typ.              | Max.        | Units                                |
|------------------------|---|--|-------------|-------------------|-------------|--------------------------------------|
| $BV_{CBO}$             | Collector-Base Breakdown Voltage                              | $I_C=1\text{mA}, I_E=0$  | 800         | -                 | -           | V                                    |
| $BV_{CEO}$             | Collector-Emitter Breakdown Voltage                           | $I_C=5\text{mA}, I_B=0$  | 400         | -                 | -           | V                                    |
| $BV_{EBO}$             | Emitter Cut-off Current                                       | $I_E=1\text{mA}, I_C=0$  | 12          | -                 | -           | V                                    |
| $I_{CBO}$              | Collector Cut-off Current                                     | $V_{CB}=500\text{V}, I_E=0$  | -           | -                 | 10          | $\mu\text{A}$                        |
| $I_{EBO}$              | Emitter Cut-off Current                                       | $V_{EB}=9\text{V}, I_C=0$  | -           | -                 | 10          | $\mu\text{A}$                        |
| $h_{FE1}$<br>$h_{FE2}$ | DC Current Gain   | $V_{CE}=1\text{V}, I_C=0.4\text{A}$<br>$V_{CE}=1\text{V}, I_C=1\text{A}$   | 20<br>10    | -<br>-            | -<br>-      |                                      |
| $V_{CE(sat)}$          | Collector-Emitter Saturation Voltage                          | $I_C=0.4\text{A}, I_B=0.04\text{A}$<br>$I_C=1\text{A}, I_B=0.2\text{A}$  | -<br>-      | -<br>-            | 0.4<br>0.5  | V<br>V                               |
| $V_{BE(sat)}$          | Base-Emitter Saturation Voltage                               | $I_C=0.4\text{A}, I_B=0.04\text{A}$<br>$I_C=1\text{A}, I_B=0.2\text{A}$  | -<br>-      | -<br>-            | 0.9<br>1.0  | V<br>V                               |
| $C_{ob}$               | Output Capacitance  | $V_{CB}=10\text{V}, f=1\text{MHz}$   | -           | -                 | 75          | pF                                   |
| $t_{ON}$               | Turn ON Time  | $V_{CC}=300\text{V}, I_C=1\text{A}$  | -           | -                 | 150         | ns                                   |
| $t_{STG}$              | Storage Time  | $I_{B1}=0.2\text{A}, I_{B2}=-0.5\text{A}$<br>$R_L=300\Omega$   | -           | -                 | 2           | $\mu\text{s}$                        |
| $t_F$                  | Fall Time   |  | -           | -                 | 0.2         | $\mu\text{s}$                        |
| $t_{STG}$              | Storage Time  | $V_{CC}=15\text{V}, V_Z=300\text{V}$<br>$I_C=0.8\text{A}, I_{B1}=0.16\text{A}$<br>$I_{B2}=-0.16\text{A}$<br>$L_C=200\mu\text{H}$ | -           | -                 | 2.35        | $\mu\text{s}$                        |
| $t_F$                  | Fall Time   |  | -           | -                 | 150         | ns                                   |
| $V_F$                  | Diode Forward Voltage   | $I_F=0.4\text{A}$<br>$I_F=1\text{A}$   | -<br>-      | -<br>-            | 1.2<br>1.5  | V<br>V                               |
| $t_{rr}$               | * Reverse Recovery Time<br>( $di/dt=10\text{A}/\mu\text{s}$ ) | $I_F=0.2\text{A}$<br>$I_F=0.4\text{A}$<br>$I_F=1\text{A}$  | -<br>-<br>- | 800<br>1.0<br>1.4 | -<br>-<br>- | ns<br>$\mu\text{s}$<br>$\mu\text{s}$ |

\*Pulse Test : Pulse Width=5, Duty cycles  $\leq 10\%$

# Typical Characteristics

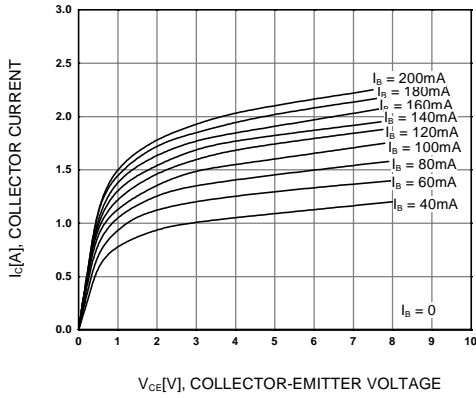


Figure 1. Static Characteristic

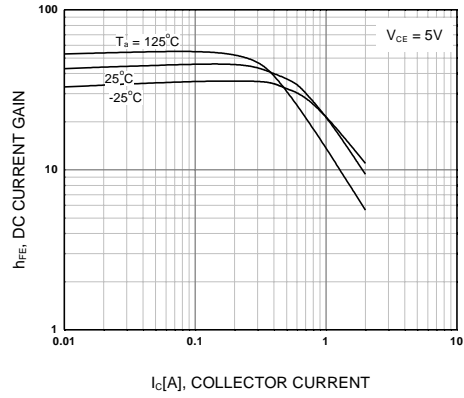


Figure 2. DC current Gain

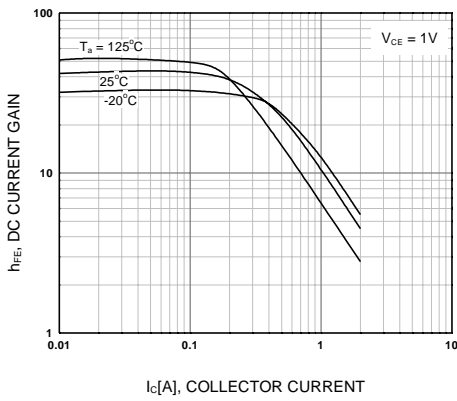


Figure 3. DC current Gain

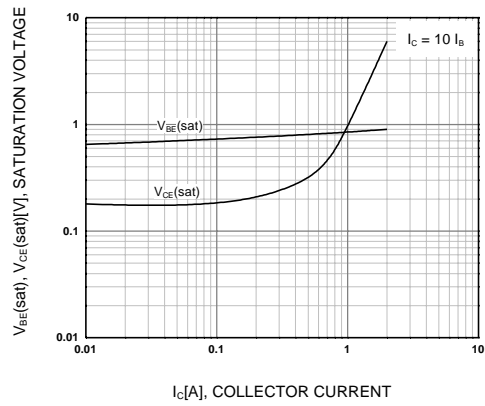


Figure 4. Collector-Emitter Saturation Voltage  
Base-Emitter Saturation Voltage

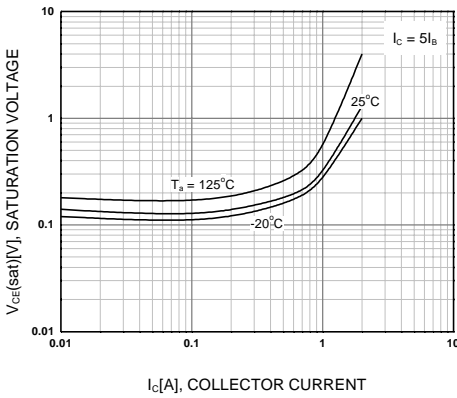


Figure 5. Collector-Emitter Saturation Voltage

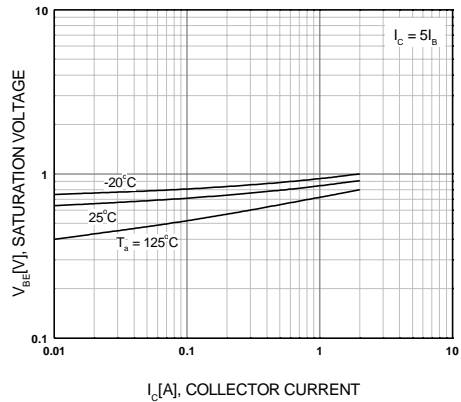


Figure 6. Base-Emitter Saturation Voltage

Typical Characteristics (Continued)

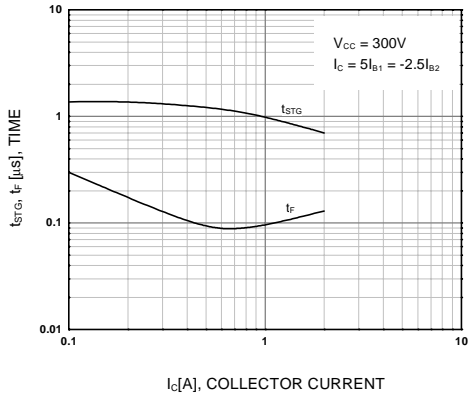


Figure 7. Switching Time

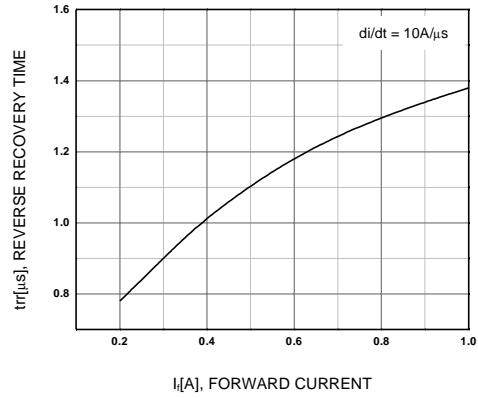


Figure 8. Forward Diode Voltage

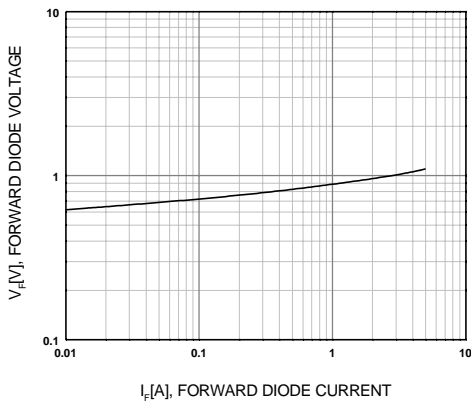


Figure 9. Reverse Recovery Time

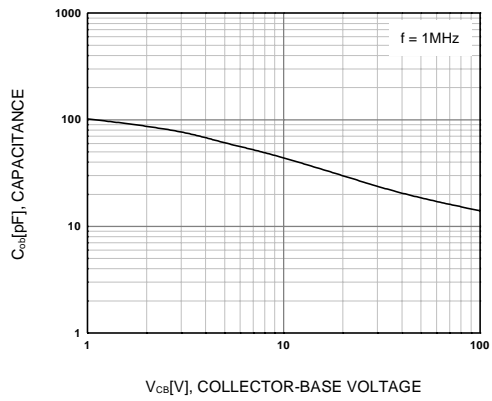


Figure 10. Collector Output Capacitance

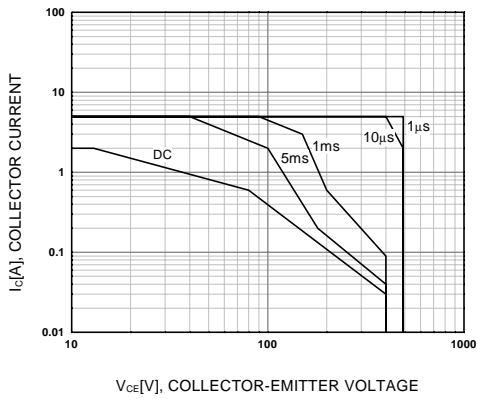


Figure 11. Safe Operating Area

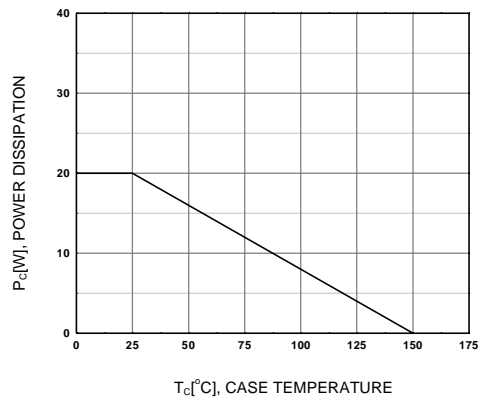
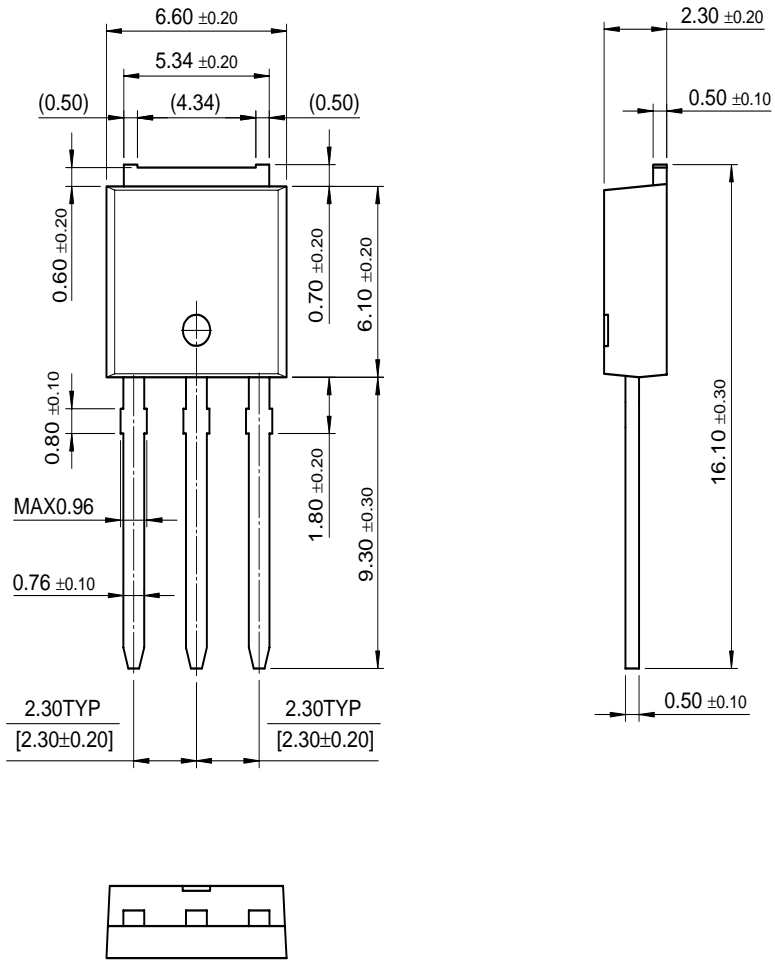


Figure 12. Power Derating

# Package Dimensions

## I-PAK



Dimensions in Millimeters

## TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

|                      |                     |                     |                 |
|----------------------|---------------------|---------------------|-----------------|
| ACEx™                | FAST®               | OPTOPLANAR™         | STAR*POWER™     |
| Bottomless™          | FASTr™              | PACMAN™             | Stealth™        |
| CoolFET™             | FRFET™              | POP™                | SuperSOT™-3     |
| CROSSVOLT™           | GlobalOptoisolator™ | Power247™           | SuperSOT™-6     |
| DenseTrench™         | GTO™                | PowerTrench®        | SuperSOT™-8     |
| DOMET™               | HiSeC™              | QFET™               | SyncFET™        |
| EcoSPARK™            | ISOPLANAR™          | QS™                 | TruTranslation™ |
| E <sup>2</sup> CMOS™ | LittleFET™          | QT Optoelectronics™ | TinyLogic™      |
| EnSigna™             | MicroFET™           | Quiet Series™       | UHC™            |
| FACT™                | MICROWIRE™          | SLIENT SWITCHER®    | UltraFET®       |
| FACT Quiet Series™   | OPTOLOGIC™          | SMART START™        | VCX™            |

STAR\*POWER is used under license

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

## LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

| Datasheet Identification | Product Status         | Definition  |
|--------------------------|------------------------|---|
| Advance Information      | Formative or In Design | This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.  |
| Preliminary              | First Production       | This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| No Identification Needed | Full Production        | This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.   |
| Obsolete                 | Not In Production      | This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.   |

Fairchild Semiconductor

SEARCH | [Parametric](#) | [Cross Reference](#)

space

Product Folders and

Applies

find products

[Home](#) >> [Find products](#) >>

[Products groups](#)

[Analog and Mixed](#)

[Signal](#)

[Discrete](#)

[Interface](#)

[Logic](#)

[Microcontrollers](#)

[Non-Volatile](#)

[Memory](#)

[Optoelectronics](#)

[Markets and](#)

[applications](#)

[New products](#)

[Product selection and](#)

[parametric search](#)

[Cross-reference](#)

[search](#)

KSC5302DI

NPN Silicon Transistor

Contents

[Features](#) | [Applications](#) | [Product status/pricing/packaging](#)

Features

- Built-in Free-wheeling Diode makes efficient anti-saturation operation suitable for half-bridge light ballast applications
- No need to interest an hFE value because of low variable storage-time spread even though corner spirit
- Low base drive requirement

[back to top](#)

Applications

**High Voltage & High Speed Power Switch**

[back to top](#)

Product status/pricing/packaging

| Product     | Product status  | Pricing* | Package type | Leads | Packing method |
|-------------|-----------------|----------|--------------|-------|----------------|
| KSC5302DITU | Full Production | \$0.45   | TO-251(IPAK) | 3     | RAIL           |

\* 1,000 piece Budgetary Pricing

[back to top](#)

[Home](#) | [Find products](#) | [Technical information](#) | [Buy products](#) | [Support](#) | [Company](#) | [Contact us](#) | [Site index](#) | [Privacy policy](#)

© Copyright 2002 Fairchild Semiconductor

Related Links

[Request samples](#)

[How to order products](#)

[Product Change Notices \(PCNs\)](#)

[Support](#)

[Distributor and field sales representatives](#)

[Quality and reliability](#)

[Design tools](#)

Datasheet

[Download this datasheet](#)

PDF

[e-mail this datasheet](#)

[E-]

This page [Print version](#)

