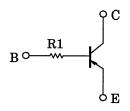
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process)

RN2210,RN2211

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- Complementary to RN1210, 1211

Equivalent Circuit



Absolute Maximum Ratings (Ta = 25°C)

		/ /
Symbol	Rating	Unit
Усво	-50	\ V
(V _{CEO})	-50	\/v
VEBO	-5	<u> </u>
// \nc	-100	mA
Pc	300/	mW
Тј	150	°C
T _{stg}	-55~150	°C
	VCBO VCEO VEBO PC Tj	V _{CBO} -50 V _{CEO} -50 V _{EBO} -5 V _{EBO} -5 T _j 150

4.2MAX.

4.2MAX.

0.55MAX.

1.27127

1.27127

2.3 SG OILECTPR

3. BASE

JEDEC —

JEITA —

TOSHIBA 2-4E1A

Weight: 0.13g (typ.)

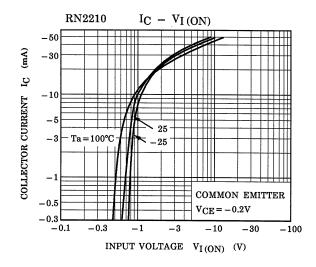
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

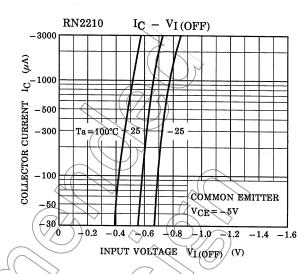
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

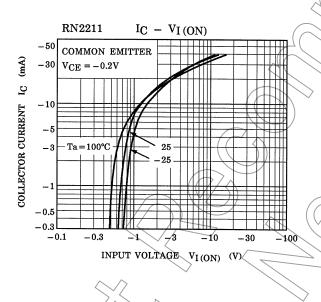
Electrical Characteristics (Ta = 25°C)

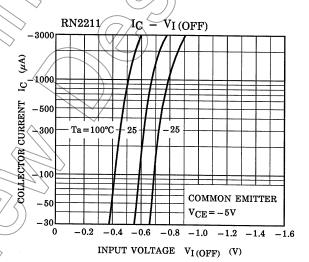
Characteristic		Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current		I _{CBO}	_	$V_{CB} = -50V$, $I_E = 0$	_	_	-100	nA	
Emitter cut-off current		I _{EBO}	_	$V_{EB} = -5V, I_C = 0$	_	_	-100	nA	
DC current gain		h _{FE}	_	$V_{CE} = -5V, I_{C} = -1mA$	120	_	400	_	
Collector-emitter saturation	n voltage	V _{CE} (sat)	_	$I_C = -5mA$, $I_B = -0.25mA$		-0.1	-0.3	V	
Translation frequency		f _T	_	$V_{CE} = -10V, I_{C} = -5mA$) 200	_	MHz	
Collector output capacitance		C _{ob}	_	V _{CB} = -10V, I _E = 0, f = 1MHz) 	3	6	pF	
Input resistor	RN2210	R1	_			3.29	4.7	6.11	kΩ
	RN2211	KI –		_	7	10	13	K12	



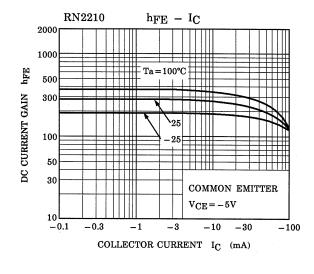


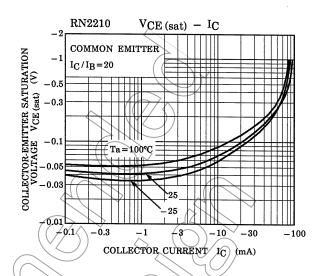


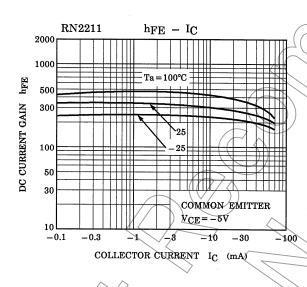


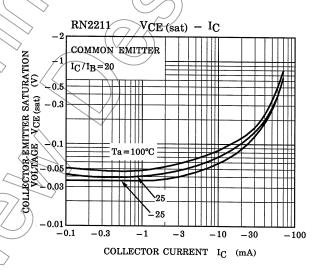


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