



PJD2N60 / PJU2N60

600V N-Channel Enhancement Mode MOSFET

TO-252 / TO-251

FEATURES

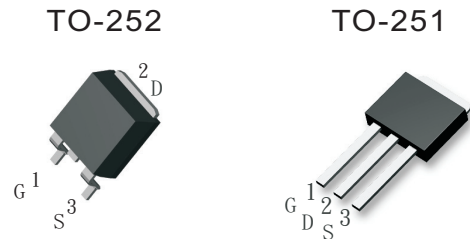
- 2A, 600V, $R_{DS(ON)}=4.6\Omega@V_{GS}=10V, I_D=1.0A$
- Low ON Resistance
- Fast Switching
- Low Gate Charge
- Fully Characterized Avalanche Voltage and Current
- Specially Designed for AC Adapter, Battery Charge and SMPS
- In compliance with EU RoHs 2002/95/EC Directives

MECHANICAL DATA

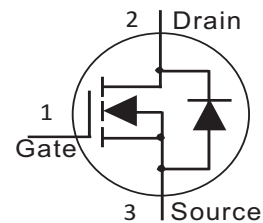
- Case: TO-252 / TO-251 Molded Plastic
- Terminals : Solderable per MIL-STD-750, Method 2026

ORDERING INFORMATION

TYPE	MARKING	PACKAGE	PACKING
PJD2N60	D2N60	TO-252	2500PCS/REEL
PJU2N60	U2N60	TO-251	80PCS/TUBE



INTERNAL SCHEMATIC DIAGRAM



Maximum RATINGS and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	Symbol	PJD2N60	PJU2N60	Units
Drain-Source Voltage	V_{DS}	600		V
Gate-Source Voltage	V_{GS}	± 30		V
Continuous Drain Current	I_D	2	2	A
Pulsed Drain Current ¹⁾	I_{DM}	8	8	A
Maximum Power Dissipation Derating Factor	P_D	44.6 0.36	43.5 0.35	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150		$^\circ\text{C}$
Avalanche Energy with Single Pulse $I_{AS}=2.0A, V_{DD}=50V, L=56mH$	E_{AS}	120		mJ
Junction-to-Case Thermal Resistance	$R_{\theta JC}$	2.80	2.85	$^\circ\text{C/W}$
Junction-to Ambient Thermal Resistance	$R_{\theta JA}$	50	100	$^\circ\text{C/W}$

Note : 1. Maximum DC current limited by the package



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ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Units
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	600	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	2.0	-	4.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =1A	-	4.0	4.6	Ω
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =600V, V _{GS} =0V	-	-	10	uA
Gate Body Leakage	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
Dynamic						
Total Gate Charge	Q _g	V _{DS} =480V, I _D =2A V _{GS} =10V	-	9.3	13	nC
Gate-Source Charge	Q _{gs}		-	2.0	-	
Gate-Drain Charge	Q _{gd}		-	3.3	-	
Turn-On Delay Time	t _{d(on)}	V _{DD} =300V, I _D =2A V _{GS} =10V, R _G =25Ω	-	10.8	18	ns
Turn-On Rise Time	t _r		-	10.4	16	
Turn-Off Delay Time	t _{d(off)}		-	23.6	32	
Turn-Off Fall Time	t _f		-	16.2	22	
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V f=1.0MHz	-	320	380	pF
Output Capacitance	C _{oss}		-	30	45	
Reverse Transfer Capacitance	C _{rss}		-	3	5.6	
Source-Drain Diode						
Max. Diode Forward Current	I _S	-	-	-	2.0	A
Max.Pulsed Source Current	I _{SM}	-	-	-	8.0	A
Diode Forward Voltage	V _{SD}	I _S =2A, V _{GS} =0V	-	-	1.4	V
Reverse Recovery Time	t _{rr}	V _{GS} =0V, I _F =2A di/dt=100A/us	-	230	-	ns
Reverse Recovery Charge	Q _{rr}		-	1.0	-	uC

NOTE : Plus Test: Pluse Width ≤ 300us, Duty Cycle ≤ 2%.



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Typical Characteristics Curves ($T_a=25^\circ\text{C}$, unless otherwise noted)

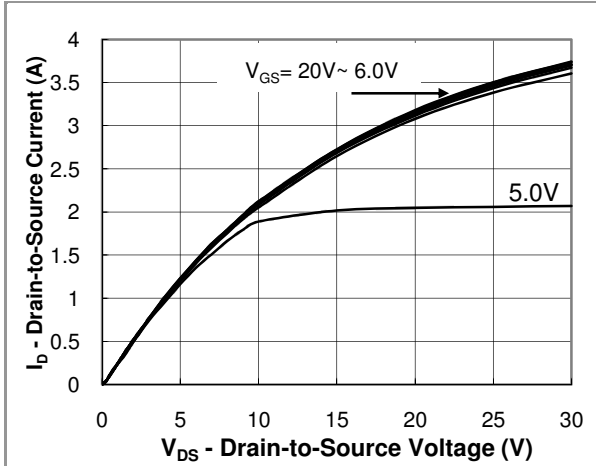


Fig.1 Output Characteristic

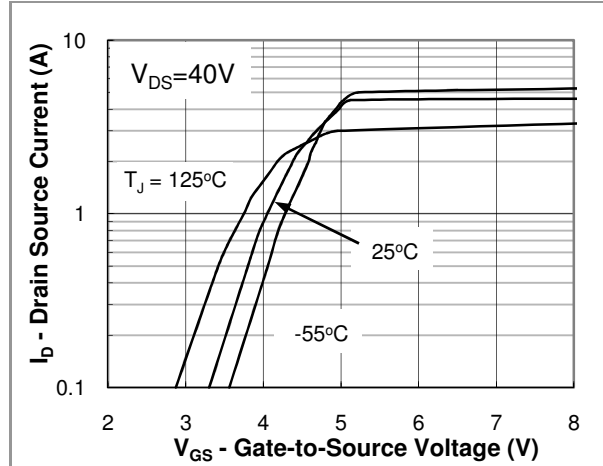


Fig.2 Transfer Characteristic

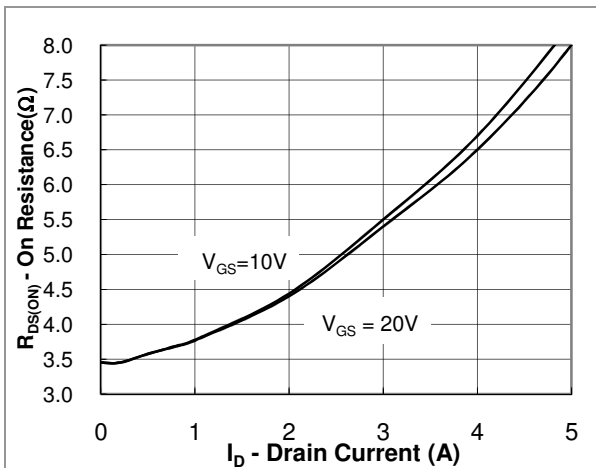


Fig.3 On Resistance vs Drain Current

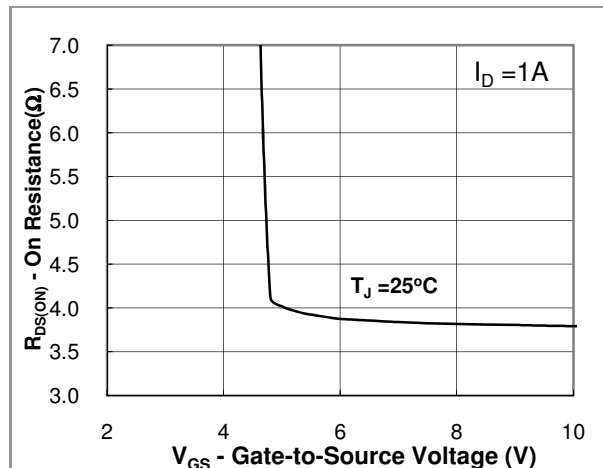


Fig.4 On Resistance vs Gate to Source Voltage

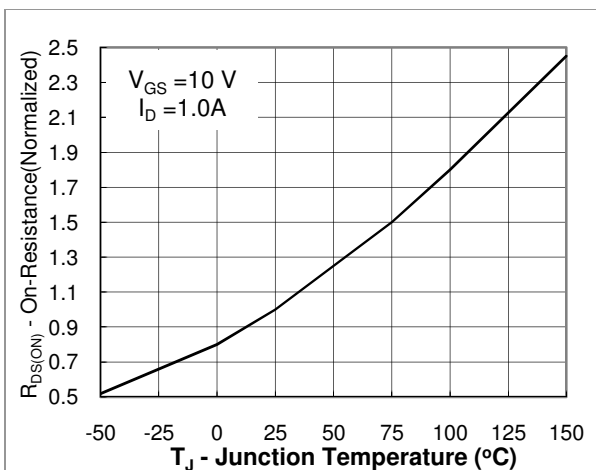


Fig.5 On Resistance vs Junction Temperature

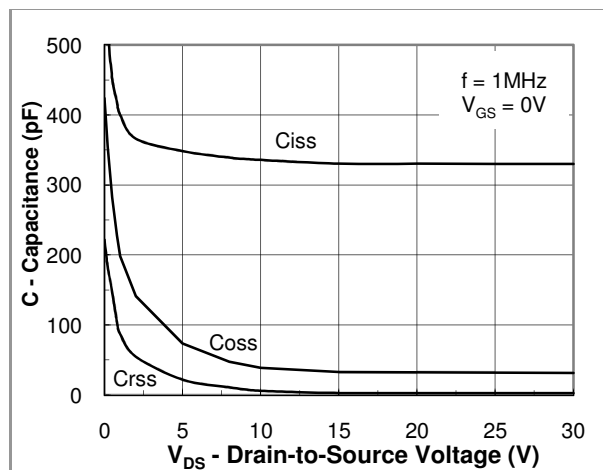


Fig.6 Capacitance



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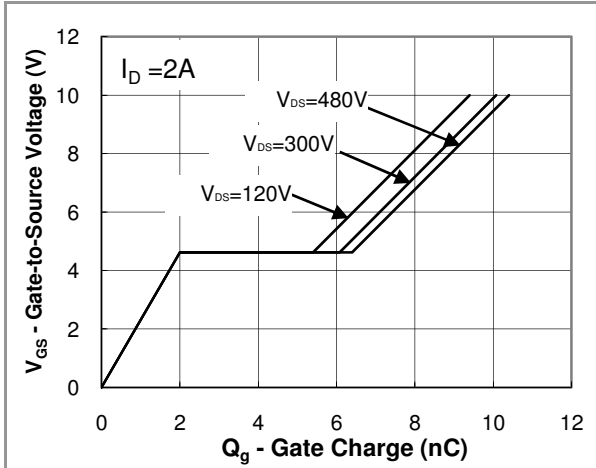


Fig. 7 Gate Charge Waveform

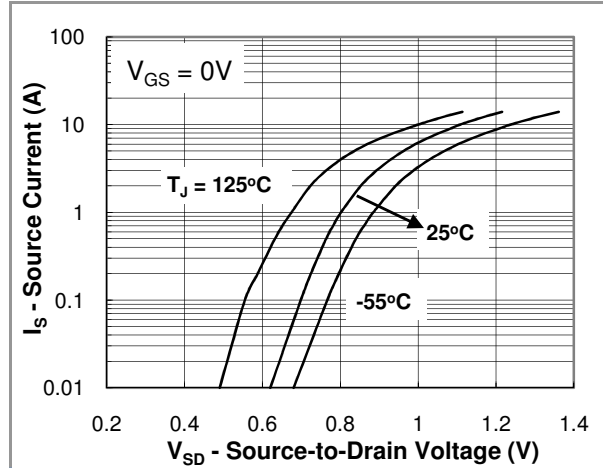


Fig.8 Source-Drain Diode Forward Voltage

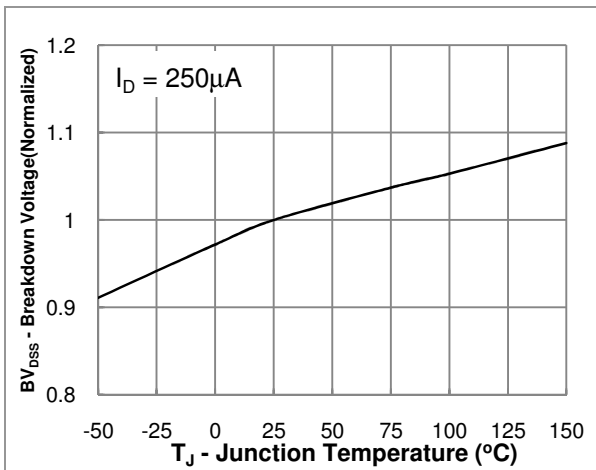


Fig.9 Breakdown Voltage vs Junction Temperature



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