EUREKA

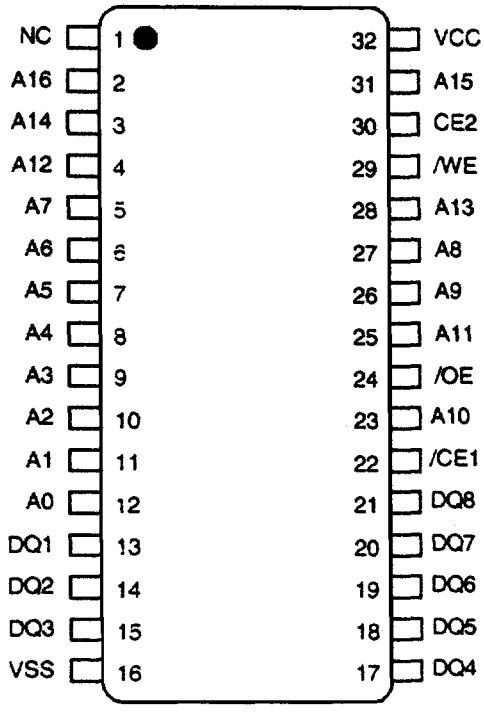
EK621024

128K x 8 Bit CMOS SRAM

Advance

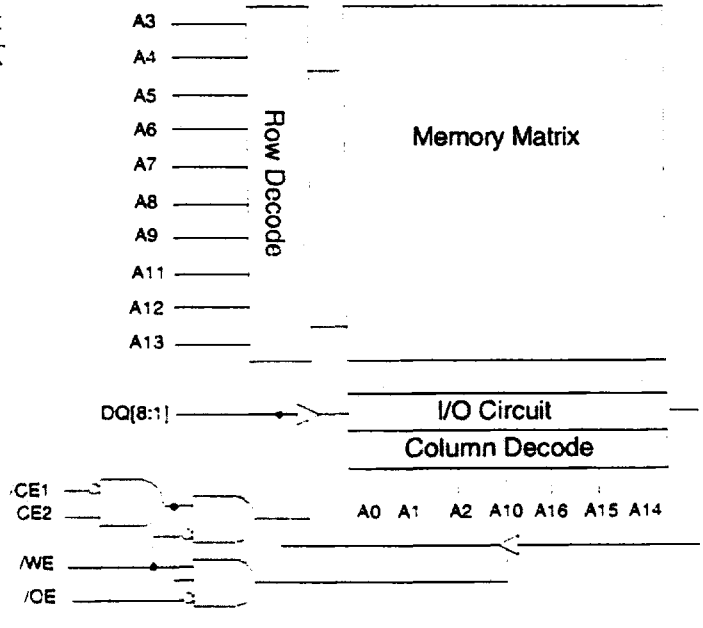
Features

- Access Times
  35ns, 55ns, 70ns, 100ns
- Single 5V ±10% Power Supply
- Industry Standard Pin Assignment
- Package Options
  450 mil 32 pin SOP
  300 mil 32 pin PDIP(Skinny)



Description

The EK621024 from Eureka is a one-megabit density static random access memory organized as 131,072 words by 8 bits. The device is offered in 450 mil SOP for surface mount applications and 300 mil DIP packages for thru-hole assembly. The pin assignment complies with the JEDEC standard pin assignment. T device is fabricated using high performance CMOS technology.



Symbol	Description
A0-A16	Address Inputs
DQ1-DQ8	Data Inputs/Outputs
/CE1, CE2	Chip Enables
/WE	Write Enable
/OE	Output Enable
NC	No Connect
Vcc	Power Supply
Vss	Ground

EUREKA MICROELECTRONICS, INC.
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Phone : 886-3-579-9255 FAX : 886-3-579-9253

Ordering Information

(Order by Complete Part Number)

EK 621024 X - Y

- Eureka Prefix
- Part Number
- Package Type
  - S = 450 mil SOP
  - K = 300 mil PDIP(Skinny)
- 35=35ns, 55=55ns, 70=70ns, 100=100ns

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