

F100364

Low Power 16-Bit Multiplexer

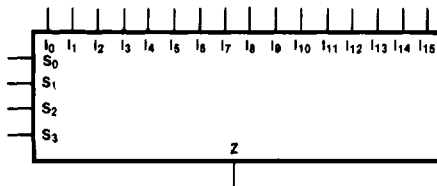
General Description

The F100364 is a 16-input multiplexer. Data paths are controlled by four Select lines (S_0 – S_3). Their decoding is shown in the truth table. Output data polarity is the same as the selected input data. All inputs have 50 k Ω pull-down resistors.

Features

- 35% power reduction of the F100164
- 2000V ESD protection
- Pin/function compatible with F100164
- Voltage compensated operating range = $-4.2V$ to $-5.7V$

Logic Symbol



TL/F/10265-1

Pin Names	Description
I_0 – I_{15}	Data Inputs
S_0 – S_3	Select Inputs
Z	Data Output

Connection Diagrams

