

## Bipolar Power Transistors

## General Purpose (Continued)

Type No.	V <sub>CEO(sus)</sub> V	V <sub>CES(sus)</sub> V	P <sub>T</sub> W	h <sub>FE</sub>		V <sub>CE(sat)</sub> -V		f <sub>T</sub> (Typ) MHz	JEDEC Package		
				I <sub>C</sub> A	V <sub>CE</sub> V	I <sub>C</sub> A	I <sub>B</sub> A				
D40D FAMILY (n-p-n)			-	Complementary to D41D Family							
D40D1	30	45	6.25	10 min.	1	2	0.5	0.5	0.05	200	TO-202
D40D2	30	45	6.25	20 min.	1	2	0.5	0.5	0.05		
D40D4	45	60	6.25	10 min.	1	2	0.5	0.5	0.05		
D40D5	45	60	6.25	10 min.	1	2	0.5	0.5	0.05		
D40D7	60	75	6.25	10 min.	1	2	1	0.5	0.05		
D40D8	60	75	6.25	10 min.	1	2	1	0.5	0.05		
D40E FAMILY (n-p-n)			-	Complementary to D41E Family							
D40E1	30	45	8	10 min.	1	2	1	1	0.1	230	TO-202
D40E5	60	70	8	10 min.	1	2	1	1	0.1		
D41D FAMILY (p-n-p)			-	Complementary to D40D Family							
D41D1	-30	-45	6.25	10 min.	-1	-2	-0.5	-0.5	-0.05	150	TO-202
D41D2	-30	-45	6.25	20 min.	-1	-2	-0.5	-0.5	-0.05		
D41D4	-45	-60	6.25	10 min.	-1	-2	-0.5	-0.5	-0.05		
D41D5	-45	-60	6.25	10 min.	-1	-2	-0.5	-0.5	-0.05		
D41D7	-60	-75	6.25	10 min.	-1	-2	-1	-0.5	-0.05		
D41D8	-60	-75	6.25	10 min.	-1	-2	-1	-0.5	-0.05		
D41E FAMILY (p-n-p)			-	Complementary to D40E Family							
D41E1	-30	-45	8	10 min.	-1	-2	-1	-1	-0.1	175	TO-202
D41E5	-60	-70	8	10 min.	-1	-2	-1	-1	-0.1		
D41E7	-80	-90	8	10 min.	-1	-2	-1	-1	-0.1		
D42C FAMILY (n-p-n)			-	Complementary to D43C Family							
D42C1	30	40	12.5	10 min.	1	1	0.5	1	0.1	50	TO-202
D42C2	30	40	12.5	20 min.	2	1	0.5	1	0.05		
D42C3	30	40	12.5	20 min.	2	1	0.5	1	0.05		
D42C4	45	55	12.5	10 min.	1	1	0.5	1	0.1		
D42C5	45	55	12.5	20 min.	2	1	0.5	1	0.05		
D42C6	45	55	12.5	20 min.	2	1	0.5	1	0.05		
D42C7	60	70	12.5	10 min.	1	1	0.5	1	0.1		
D42C8	60	70	12.5	20 min.	2	1	0.5	1	0.05		
D42C9	60	70	12.5	20 min.	2	1	0.5	1	0.05		
D42C10	80	90	12.5	10 min.	1	1	0.5	1	0.1		
D42C11	80	90	12.5	20 min.	2	1	0.5	1	0.05		
D42C12	80	90	12.5	20 min.	2	1	0.5	1	0.05		