Micro Commercial Components

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LLSD103A THRU LLSD103C

Schottky Barrier Switching Diode

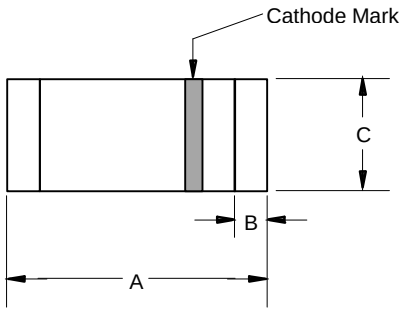
Features

- Low Reverse Recovery Time
- Low Reverse Capacitance
- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection

Mechanical Data

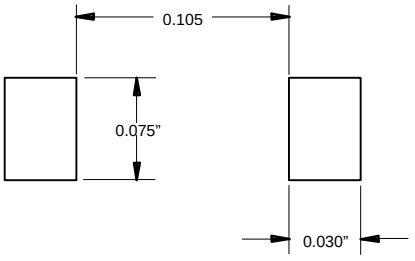
- Case: MiniMELF, Glass
- Terminals: Solderable per MIL-STD-202, Method 208
- Polarity: Indicated by Cathode Band
- Weight: 0.05 grams (approx.)

MINIMELF



DIMENSION					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	.134	.142	3.40	3.60	
B	.008	.016	0.20	0.40	
C	.055	.059	1.40	1.50	

SUGGESTED SOLDER PAD LAYOUT



Maximum Ratings @ 25°C Unless Otherwise Specified

Characteristic	Symbol	LLSD103A	LLSD103B	LLSD103C
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	40V	30V	20V
RMS Reverse Voltage	$V_{R(RMS)}$	28V	21V	14V
Forward Continuous Current(Note1)	I_{FM}	350mA		
Maximum Single cycle surge 60Hz sine wave	I_{FSM}	15A		
Power Dissipation(Note 1)	P_d	400mW		
Thermal Resistance(Note 1)	R	250K/W		
Operation/Storage Temp. Range	Tj, T_{STG}	-55 to 150 °C		

Electrical Characteristics @ 25°C Unless Otherwise Specified

Charateristic	Symbol	Min	Typ	Max	Test Cond.
Peak Reverse Current LLSD103A	I_{RM}	-----	-----	5.0uA	V_R=30V
Peak Reverse Current LLSD103B					V_R=20V
Peak Reverse Current LLSD103C					V_R=10V
Maximum Forward Voltage Drop	V_{FM}	-----	-----	0.37V	I_F=20mA
Maximum Forward Voltage Drop				0.60V	I_F=200mA
Junction Capacitance	Cj	-----	50	pF	V_R=0V, f=1.0MHz
Reverse Recovery Time	t_{rr}	-----	10	ns	I_F=I_R=50mA to 200mA, recover to 0.1 I_R

Note: 1. Valid provided that electrodes are kept at ambient temperature

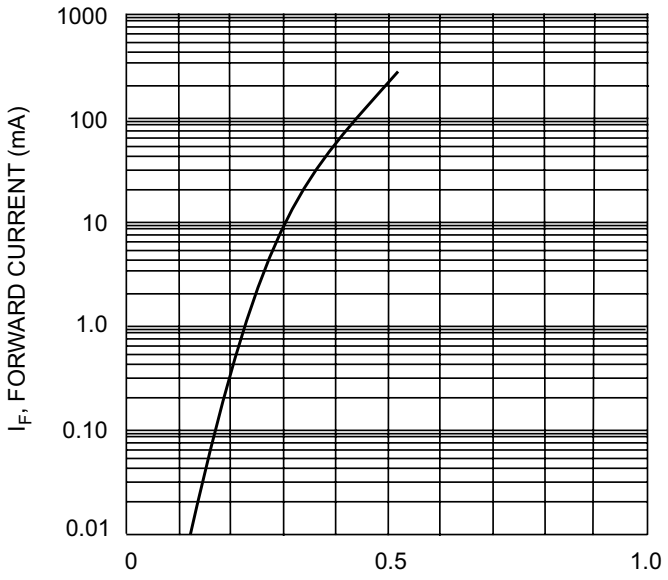


Fig. 1 Typical Forward Characteristics

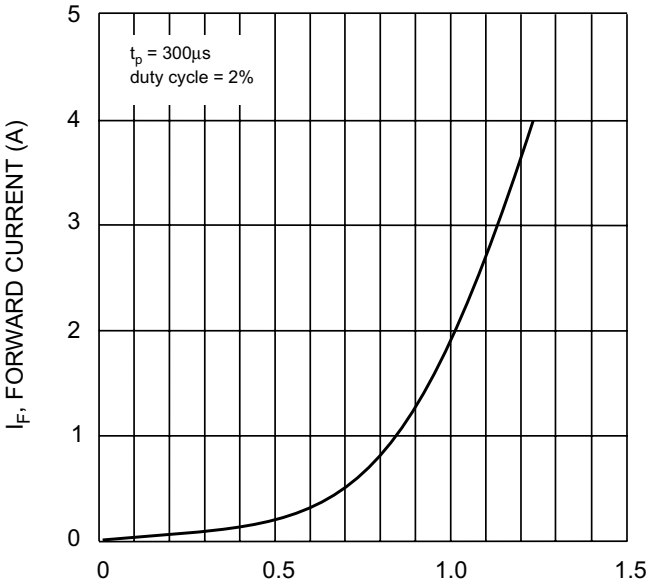


Fig. 2 Typical High Current Fwd Characteristics

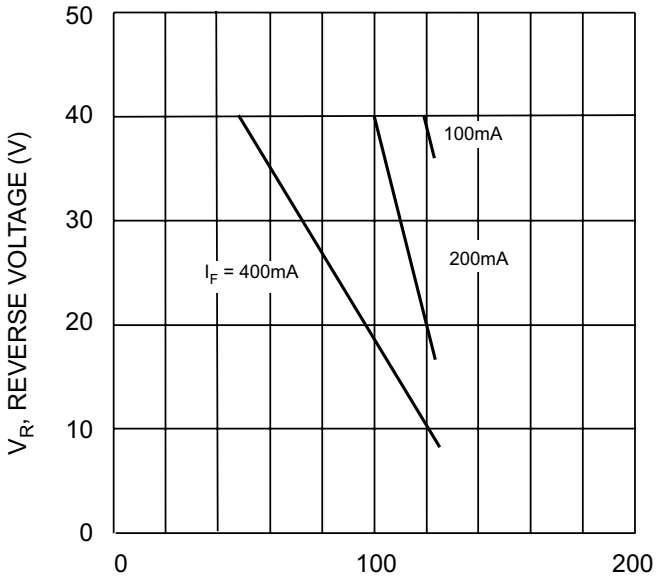


Fig. 3 Blocking Voltage Derating Curves

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