

TECHNICAL DATA
DATA SHEET 237, REV. A
Formerly Part Number SHD4312

SMALL SIGNAL TRANSISTOR

DESCRIPTION: A SINGLE NPN SMALL SIGNAL TRANSISTOR IN A CERAMIC LCC-3 PACKAGE.

MAXIMUM RATINGS

(ALL RATINGS ARE AT $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE SPECIFIED).

RATING	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector-Emitter Voltage (V_{CE0})	-	-	-	40	Vdc
Collector-Base Voltage (V_{CBO})	-	-	-	75	Vdc
Emitter-Base Voltage (V_{EBO})	-	-	-	6.0	Vdc
Collector Current-Continuous (I_C)	-	-	-	800	mAdc
Total Power Dissipation $P_D @ T_C = 25^\circ\text{C}$ Derate above 25°C	-	-	-	1.0 6.0	W mW/ $^\circ\text{C}$
Thermal Resist. Junction to Case $R\theta_{JC}$	-	-	-	168	$^\circ\text{C}/\text{W}$
Operating Junction and Storage Temp. (T_J & T_{stg})	-	-65	-	+200	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS

(ALL ELECTRICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$)

OFF CHARACTERISTICS						
Collector-Emitter Breakdown Voltage $V_{(BR)CEO(1)}$	$I_C = 10\text{mAdc}, I_B = 0$	40	-	-	Vdc	
Collector-Base Breakdown Voltage $V_{(BR)CBO}$	$I_C = 10\mu\text{Adc}, I_E = 0$	75	-	-	Vdc	
Emitter-Base Breakdown Voltage $V_{(BR)EBO}$	$I_E = 10\mu\text{Adc}, I_C = 0$	6.0	-	-	Vdc	
Collector Cutoff Current (I_{CBO})	$V_{CB} = 60\text{Vdc}, I_E = 0$	-	-	0.01	μAdc	
		-	-	10	μAdc	
Collector Cutoff Current (I_{CEX})	$V_{CE} = 60\text{Vdc}, I_C = 0,$ $V_{EB(off)} = 3.0\text{Vdc}$	-	-	10	nAdc	
ON CHARACTERISTICS						
DC Current Gain (h_{FE}) $V_{CE} = 10\text{Vdc}$	$I_C = 0.1\text{ mAdc}$	35	-	-	-	
	$I_C = 1.0\text{ mAdc}$	50	-	-	-	
	$I_C = 10\text{ mAdc (1)}$	75	-	-	-	
	$I_C = 150\text{ mAdc (1)}$	100	-	-	300	
	$I_C = 500\text{ mAdc (1)}$	40	-	-	-	

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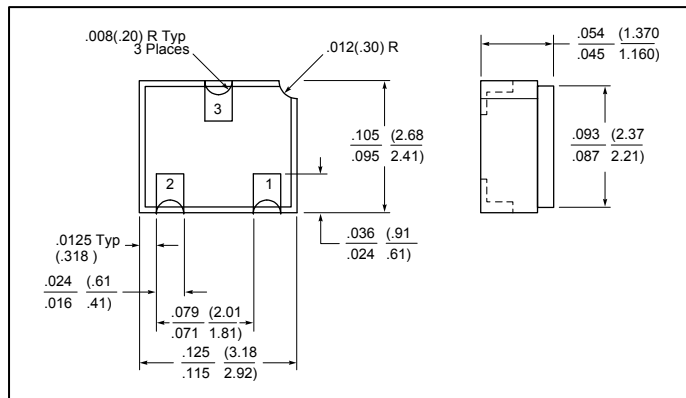
SMALL-SIGNAL CHARACTERISTICS					
Current Gain, Bandwidth (2) (f_T)	$V_{CE} = 20V_{dc}, I_C = 20mAdc,$ $f = 100MHz$	300	-	-	MHz
Output Capacitance (C_{obo})	$V_{CB} = 10V_{dc}, I_E = 0,$ $f = 1.0 MHz$	-	-	8.0	pF

RATING	CONDITIONS	MIN.	TYP.	MAX.	UNITS
SMALL-SIGNAL CHARACTERISTICS (Contiued)					
Input Capacitance (C_{ibo})	$V_{EB} = 0.5 V_{dc}, I_C = 0,$ $f = 1.0 MHz$	-	-	25	pF
Delay Time (t_d)	$(I_C = 150 mAdc, I_B = 15 mAdc, V_{CC} = 30V_{dc},$ $V_{BE(off)} = -0.5V_{dc})$	-	-	10	ns
Rise Time (t_r)		-	-	25	
Storage Time (t_s)	$(I_C = 150 mAdc, I_{B1} = I_{B2} = 15 mAdc, V_{CC} = 30V_{dc})$	-	-	225	ns
Fall Time (t_f)		-	-	60	

(1) Pulsed. Pulse Width $\leq 300 \mu s$, Duty Cycle $\leq 2.0\%$.

(2) $f_T = |h_{fe}| \cdot f_{test}$

MECHANICAL DIMENSIONS - in inches / mm



LCC-3

PINOUTS

TYPE	PIN 1	PIN 2	PIN 3
PNP Transistor	BASE	EMITTER	COLLECTOR

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