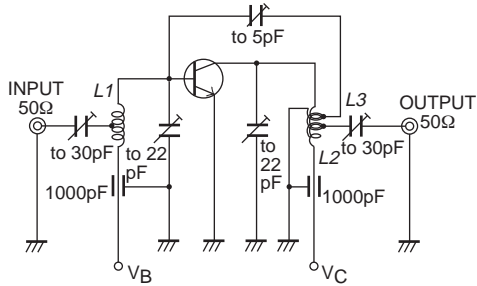


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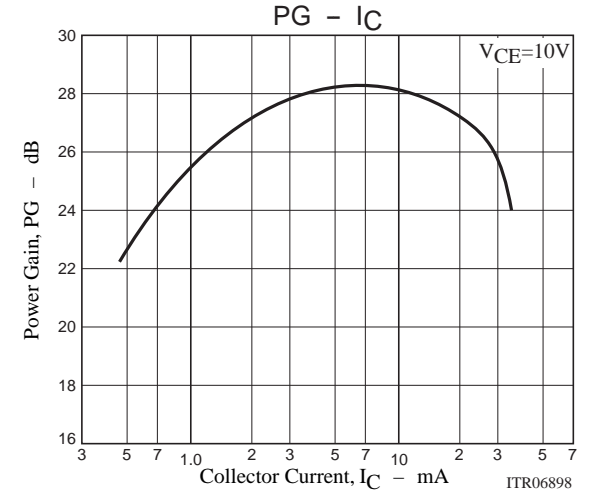
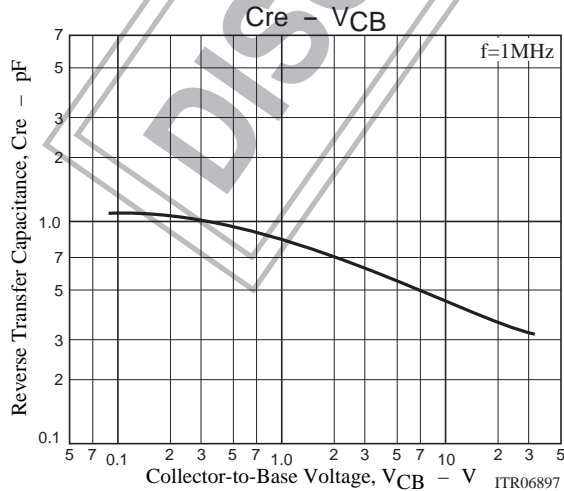
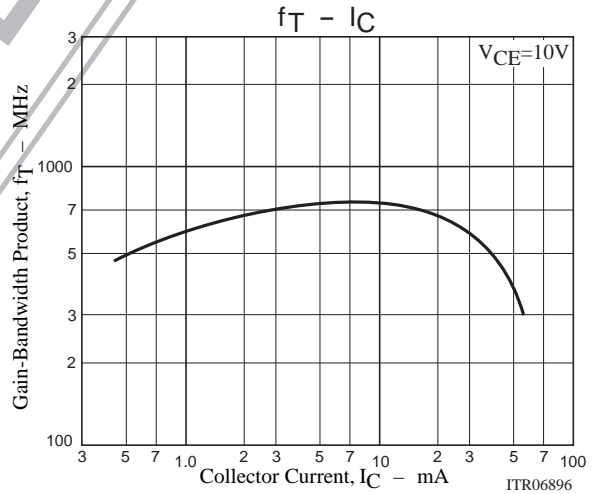
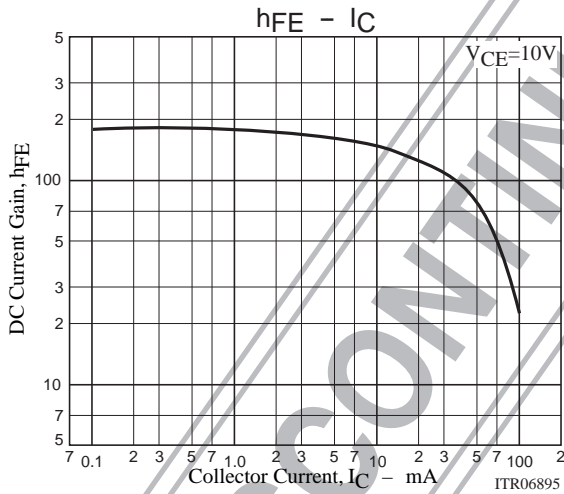
Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=10mA, I_B=1mA$			0.2	V
Base-to-Collector Time Constant	$r_{bb}C_c$	$V_{CE}=10V, I_C=5mA, f=31.9MHz$			23	ps
Power Gain	PG	$V_{CE}=10V, I_C=10mA, f=100MHz$		28		dB

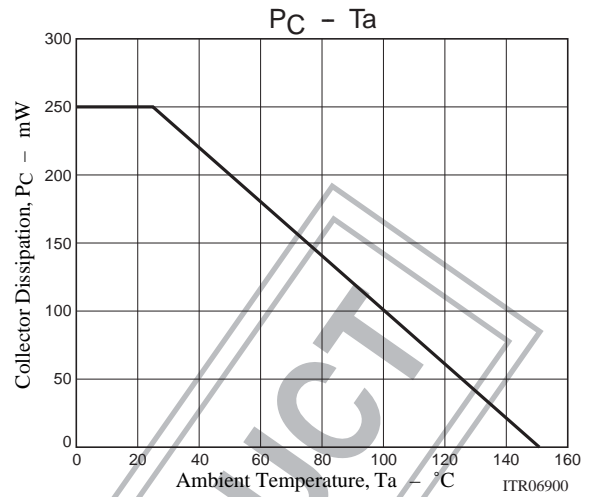
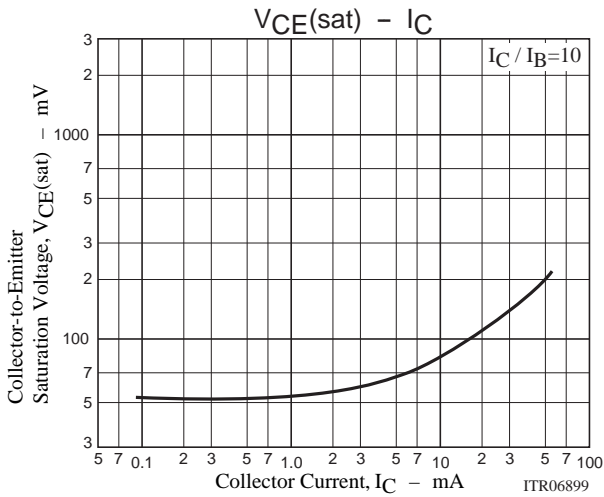
PG Test Circuit



- L1 : 1mmϕ plated wire, 10mmϕ 5T, pitch 15mm, tap : 2T from base side.
- L2 : 1mmϕ plated wire, 10mmϕ 7T, pitch 10mm, tap : from VC side.
- L3 : 1mmϕ enamel wire, 10mmϕ 3T, pitch 10mm



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