

RD74HV1G00

High–Voltage 2–input NAND gate

REJ03D0885-0100 Rev.1.00 Feb 20, 2007

Description

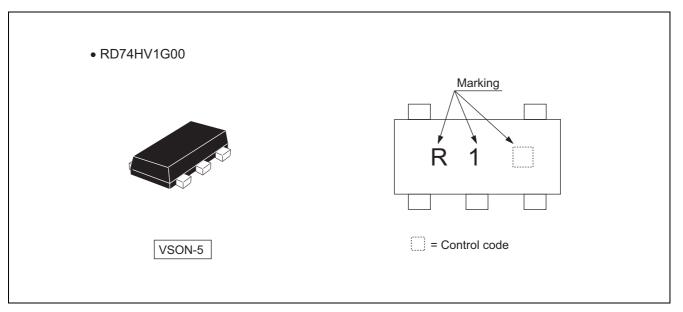
The RD74HV1G00 has two–input NAND gate in a 5 pin package. Supports the wide power supply voltage and can use it for the other use as a general–purpose driver.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Wide supply voltage range : 4.5 to 30 V
- Operating temperature range : -40 to $+85^{\circ}C$
- All inputs V_{IH} (Min.) = 3.5 V, V_{IL} (Max.) = 0.8 V (@V_{CC} = 10 V to 30 V)
- Output current : I_0 short (Typ.) = ± 70 mA (@V_{CC} = 15 V)
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
RD74HV1G00VSE	VSON-5 pin	PUSN0005KA-A (TNP-5DV)	VS	E (3,000 pcs / Reel)

Outline and Article Indication



These products designed for general and industrial use. It is not supported for special quality or reliability demanded use such as automotive or life support or something like that.



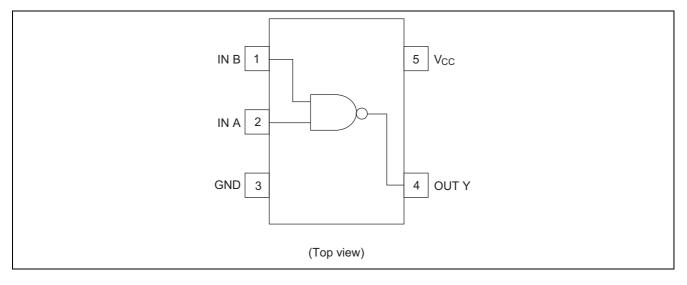
Function Table

Inp	outs	Output Y		
A	В	Output 1		
L	L	Н		
Н	L	н		
L	Н	н		
Н	Н	L		

H : High level

L : Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	0 to 30	V	
Input voltage range *1	VI	–0.5 to V _{CC} + 0.5	V	
Output voltage range *1, 2	Vo	–0.5 to V _{CC} + 0.5	V	
Input clamp current	I _{IK}	±50	mA	$V_1 < 0 \text{ or } V_1 > V_{CC}$
Output clamp current	Ι _{ΟΚ}	±75	mA	$V_{\rm O}$ < 0 or $V_{\rm O}$ > $V_{\rm CC}$
Continuous output current	Ι _Ο	±100	mA	$V_{\rm O}$ = 0 to $V_{\rm CC}$
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±100	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 30 V maximum.

3. The maximum package power dissipation was calculated using a junction temperature of 150°C.



Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	4.5	30	V	
Input voltage range	VI	0	V _{CC}	V	
Input / Output voltage range	V _{I/O}	0	V _{CC}	V	
		—	-2.5		V _{CC} = 10 V
		—	-5		V _{CC} = 15 V
	I _{OH}	_	-10	- mA	V _{CC} = 25 V
		_	-15		V _{CC} = 30 V
Output current	I _{OL}	_	2.5		V _{CC} = 10 V
		_	5		V _{CC} = 15 V
		_	10		V _{CC} = 25 V
		_	15		V _{CC} = 30 V
	Δt / Δv	0	100		V _{CC} < 5 V
Input transition rise or fall rate		0	20	ns / V	15 V > V _{CC} ≥ 5 V
		0	10	1	30 V ≥ V _{CC} ≥ 15 V
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristic

							$(Ta = -40 \text{ to } 85^{\circ}C)$
Item	Symbol	V _{cc} (V) *	Min	Тур	Max	Unit	Test condition
		10	3.5	_	_		
	V	15	3.5				
	V _{IH}	25	3.5				
Input voltage		30	3.5			v	
Input voltage		105		_	0.8	v	
	V	15	_		0.8		
	VIL	25	_		0.8		
		30	_	_	0.8		
		10	9.0	_	—		I _{OH} = –2.5 mA
	V _{OH}	15	13.5	—	—	V	I _{OH} = –5 mA
		25	22.5	—	—		I _{OH} = –10 mA
Output voltage		30	27.0	_	—		I _{OH} = –15 mA
Oulput vollage	V _{OL}	10	—	—	1.0		I _{OL} = 2.5 mA
		15	—	—	1.5		I _{OL} = 5 mA
		25	—	—	2.5		I _{OL} = 10 mA
		30	—	—	3.0		I _{OL} = 15 mA
Output current	I _{OH} short	15	-46	-70	-95	mA	$V_0 = 0V$
	I_{OL} short	15	46	70	95		$V_{O} = V_{CC}$
Input current	I _{IN}	V _{CC}		—	±1	μA	$V_{IN} = V_{CC}$ or GND
		10		—	0.5		
Quiescent supply current	Icc	15		—	1.0	μA	$V_{IN} = V_{CC}$ or GND
Quiescent supply current		25		—	2.0	μΑ	
		30	_	—	2.0		
Supply current	I _{SUPP}	10			2	- mA	V_{CC} = 10 V , VIN = 4.5 V
		30	_	—	10		$V_{\rm CC}$ = 30 V , VIN = 4.5 V
Input capacitance	CIN	V _{CC}	—	2.5	—	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

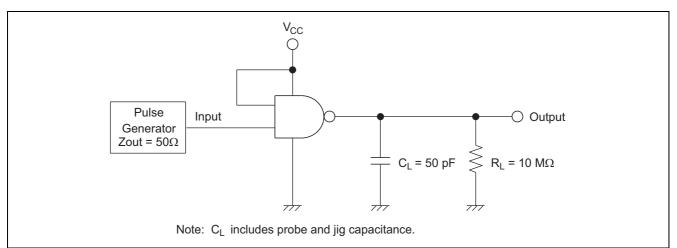


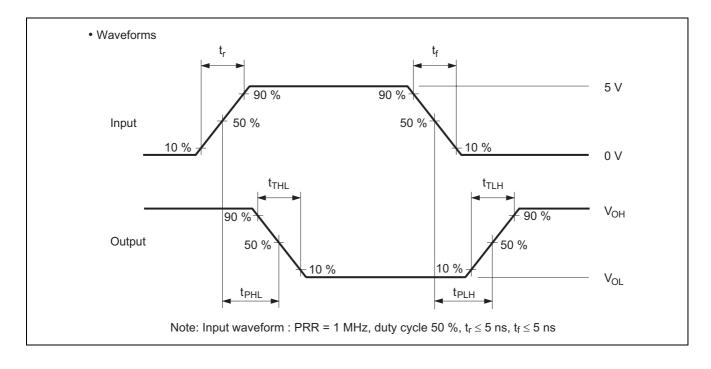
Switching Characteristics

$(C_L = 50 \text{ pF}, t_r = t_f = 5 \text{ ns})$

ltem	Symbol	Vcc (V)	Ta = -40 to 85°C			Unit	FROM	то
		VCC (V)	Min	Тур	Max	Unit	(Input)	(Output)
	t _{PLH} t _{PHL}	10	15	—	75	ns	A or B	Y
		15	10	—	55			
Propagation delay time		20	10	_	45			
		25	10	_	35			
		30	10	—	35			
Output rise / fall time	t _{TLH}	10	8	—	30	ns	A or B	Y
		15	7	_	25			
		20	6	—	20			
		25	5	_	17			
		30	5	_	15			

Test Circuit







Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]		
P-USON5-1.2x1.6-0.50	PUSN0005KA-A	TNP-5D/TNP-5DV	0.002g		
					Reference Symbol Dimension in Millimeters Min Nom Max D 1.55 1.6 1.65 E 1.1 1.2 1.3 A — — 0.6 A1 — — — b 0.15 0.2 0.3 b1 — — — B — 0.5 — Lp — — — X — — — ZD — — — ZD — — — HE 1.55 1.6 1.65



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