

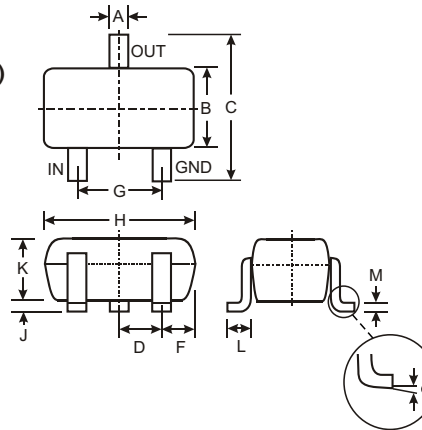
### Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDTC)
- Built-In Biasing Resistors, R1≠R2
- Available in Lead Free/RoHS Compliant Version (Note 2)

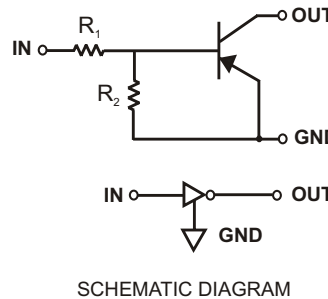
### Mechanical Data

- Case: SOT-323
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Diagram
- Terminals: Solderable per MIL-STD-202, Method 208
- Also Available in Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe). Please see Ordering Information, Note 4, on Page 2
- Marking: Date Code and Type Code, See Page 3
- Type Code: See Table Below
- Ordering Information (See Page 2)
- Weight: 0.006 grams (approximate)

P/N	R1 (NOM)	R2 (NOM)	Type Code
DDTA113ZUA	1KΩ	10KΩ	P02
DDTA123YUA	2.2KΩ	10KΩ	P05
DDTA123JUA	2.2KΩ	47KΩ	P06
DDTA143XUA	4.7KΩ	10KΩ	P09
DDTA143FUA	4.7KΩ	22KΩ	P10
DDTA143ZUA	4.7KΩ	47KΩ	P11
DDTA114YUA	10KΩ	47KΩ	P14
DDTA114WUA	10KΩ	4.7KΩ	P15
DDTA124XUA	22KΩ	47KΩ	P18
DDTA144VUA	47KΩ	10KΩ	P21
DDTA144WUA	47KΩ	22KΩ	P22



SOT-323		
Dim	Min	Max
A	0.25	0.40
B	1.15	1.35
C	2.00	2.20
D	0.65 Nominal	
E	0.30	0.40
G	1.20	1.40
H	1.80	2.20
J	0.0	0.10
K	0.90	1.00
L	0.25	0.40
M	0.10	0.18
α	0°	8°
All Dimensions in mm		



### Maximum Ratings @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage, (3) to (1)	V <sub>CC</sub>	-50	V
Input Voltage, (2) to (1)	V <sub>IN</sub>	DDTA113ZUA: +5 to -10 DDTA123YUA: +5 to -12 DDTA123JUA: +5 to -12 DDTA143XUA: +7 to -20 DDTA143FUA: +6 to -30 DDTA143ZUA: +5 to -30 DDTA114YUA: +6 to -40 DDTA114WUA: +10 to -30 DDTA124XUA: +10 to -40 DDTA144VUA: +15 to -40 DDTA144WUA: +10 to -40	V
Output Current	I <sub>O</sub>	DDTA113ZUA: -100 DDTA123YUA: -100 DDTA123JUA: -100 DDTA143XUA: -100 DDTA143FUA: -100 DDTA143ZUA: -100 DDTA114YUA: -70 DDTA114WUA: -100 DDTA124XUA: -50 DDTA144VUA: -30 DDTA144WUA: -30	mA
Output Current	I <sub>C</sub> (Max)	-100	mA
Power Dissipation	P <sub>d</sub>	200	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	R <sub>θJA</sub>	625	°C/W
Operating and Storage and Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Note: 1. Mounted on FR4 PC Board with recommended pad layout at <http://www.diodes.com/datasheets/ap02001.pdf>.  
2. No purposefully added lead.

**Electrical Characteristics** @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition	
Input Voltage	DDTA113ZUA DDTA123YUA DDTA123JUA DDTA143XUA DDTA143FUA DDTA143ZUA DDTA114YUA DDTA114WUA DDTA124XUA DDTA144VUA DDTA144WUA	V <sub>I(off)</sub>	-0.3 -0.3 -0.5 -0.3 -0.3 -0.5 -0.3 -0.8 -0.4 -1.0 -0.8	—	—	—	V	V <sub>CC</sub> = 5V, I <sub>O</sub> = 100μA
	DDTA113ZUA DDTA123YUA DDTA123JUA DDTA143XUA DDTA143FUA DDTA143ZUA DDTA114YUA DDTA114WUA DDTA124XUA DDTA144VUA DDTA144WUA	V <sub>I(on)</sub>	—	—	-3.0 -3.0 -1.1 -2.5 -1.3 -1.3 -1.4 -3.0 -2.5 -5.0 -4.0	—	V	V <sub>O</sub> = -0.3V, I <sub>O</sub> = -20mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -20mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -5mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -20mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -3mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -5mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -1mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -2mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -2mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -2mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -2mA
Output Voltage		V <sub>O(on)</sub>	—	-0.1	-0.3	V	I <sub>O</sub> /I <sub>I</sub> = -5mA/-0.25mA DDTA123JUA I <sub>O</sub> /I <sub>I</sub> = -5mA/-0.25mA DDTA143ZUA I <sub>O</sub> /I <sub>I</sub> = -5mA/-0.25mA DDTA114YUA I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA All Others	
Input Current	DDTA113ZUA DDTA123YUA DDTA123JUA DDTA143XUA DDTA143FUA DDTA143ZUA DDTA114YUA DDTA114WUA DDTA124XUA DDTA144VUA DDTA144WUA	I <sub>I</sub>	—	—	-7.2 -3.8 -3.6 -1.8 -1.8 -1.8 -0.88 -0.88 -0.36 -0.16 -0.16	mA	V <sub>I</sub> = -5V	
Output Current		I <sub>O(off)</sub>	—	—	-0.5	μA	V <sub>CC</sub> = -50V, V <sub>I</sub> = 0V	
DC Current Gain	DDTA113ZUA DDTA123YUA DDTA123JUA DDTA143XUA DDTA143FUA DDTA143ZUA DDTA114YUA DDTA114WUA DDTA124XUA DDTA144VUA DDTA144WUA	G <sub>I</sub>	-33 -33 -80 -30 -68 -80 -68 -24 -68 -33 -56	—	—	—	V <sub>O</sub> = -5V, I <sub>O</sub> = -10mA	
Input Resistor Tolerance		ΔR <sub>1</sub>	-30	—	+30	%	—	
Resistance Ratio Tolerance		ΔR <sub>2</sub> /R <sub>1</sub>	-20	—	+20	%	—	
Gain-Bandwidth Product*		f <sub>T</sub>	—	250	—	MHz	V <sub>CE</sub> = -10V, I <sub>E</sub> = 5mA, f = 100MHz	

\* Transistor - For Reference Only

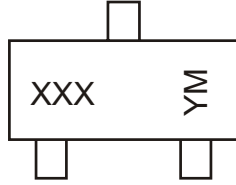
**Ordering Information** (Note 3)

Device	Packaging	Shipping
DDTA113ZUA-7	SOT-323	3000/Tape & Reel
DDTA123YUA-7	SOT-323	3000/Tape & Reel
DDTA123JUA-7	SOT-323	3000/Tape & Reel
DDTA143XUA-7	SOT-323	3000/Tape & Reel
DDTA143FUA-7	SOT-323	3000/Tape & Reel
DDTA143ZUA-7	SOT-323	3000/Tape & Reel
DDTA114YUA-7	SOT-323	3000/Tape & Reel
DDTA114WUA-7	SOT-323	3000/Tape & Reel
DDTA124XUA-7	SOT-323	3000/Tape & Reel
DDTA144VUA-7	SOT-323	3000/Tape & Reel
DDTA144WUA-7	SOT-323	3000/Tape & Reel

Notes: 3. For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

4. For Lead Free/RoHS Compliant version part number, please add "-F" suffix to the part number above. Example: DDTA144WUA-7-F.

**Marking Information**



XXX = Product Type Marking Code, See Table on Page 1  
 YM = Date Code Marking  
 Y = Year ex: N = 2002  
 M = Month ex: 9 = September

Date Code Key

<b>Year</b>	<b>2002</b>	<b>2003</b>	<b>2004</b>	<b>2005</b>	<b>2006</b>	<b>2007</b>	<b>2008</b>	<b>2009</b>
<b>Code</b>	N	P	R	S	T	U	V	W

<b>Month</b>	<b>Jan</b>	<b>Feb</b>	<b>March</b>	<b>Apr</b>	<b>May</b>	<b>Jun</b>	<b>Jul</b>	<b>Aug</b>	<b>Sep</b>	<b>Oct</b>	<b>Nov</b>	<b>Dec</b>
<b>Code</b>	1	2	3	4	5	6	7	8	9	O	N	D

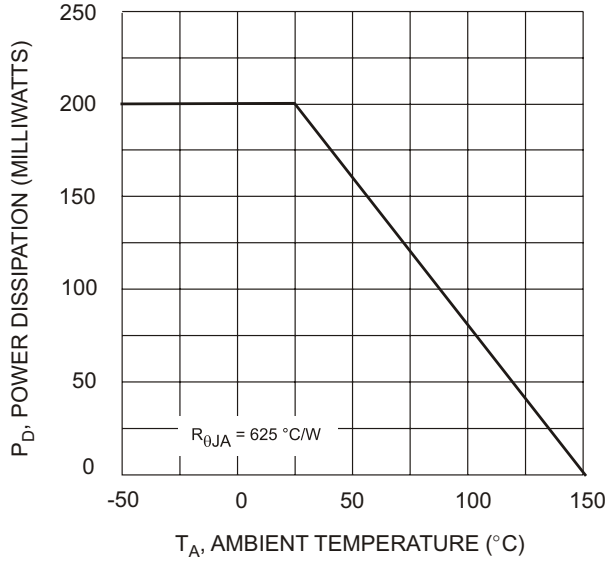


Fig. 1 Derating Curve

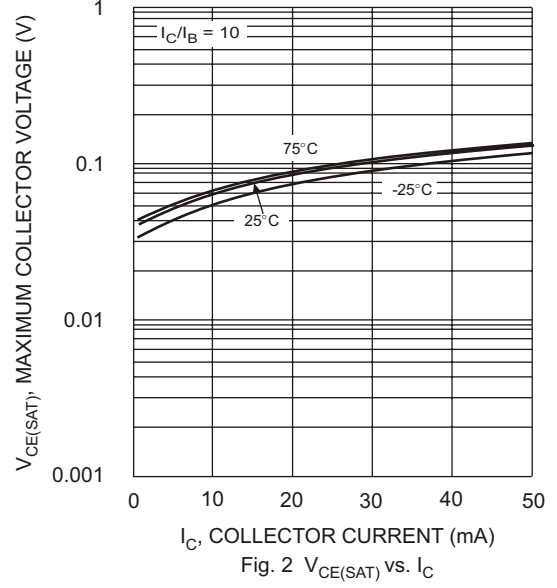


Fig. 2  $V_{CE(SAT)}$  vs.  $I_C$

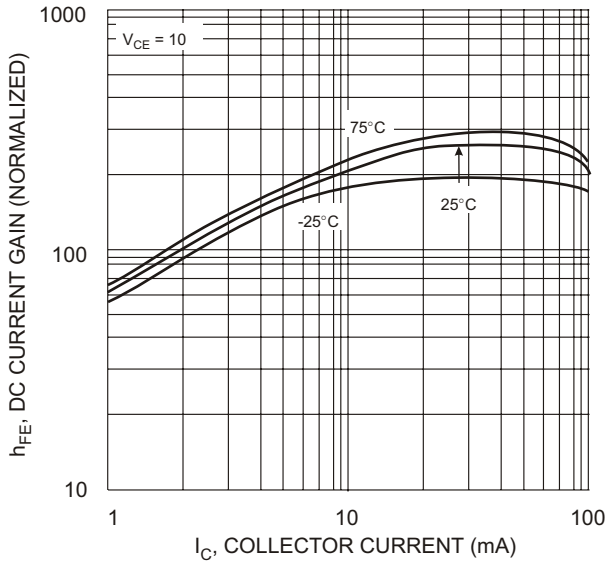


Fig. 3 DC CURRENT GAIN

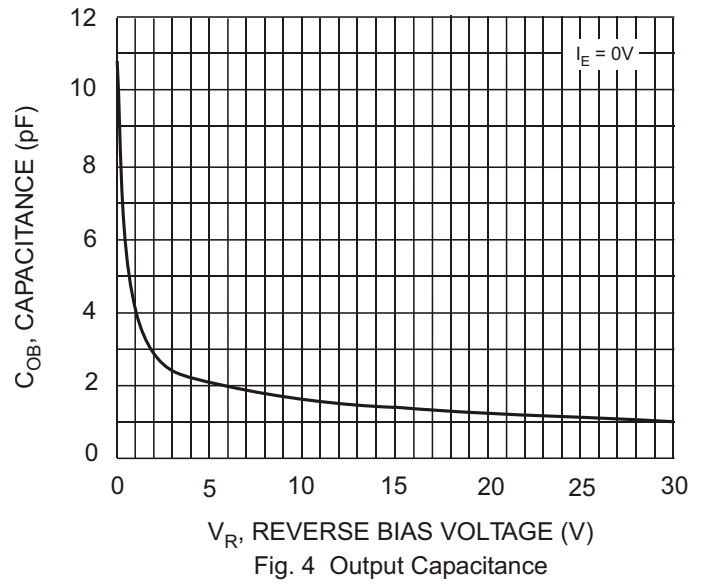


Fig. 4 Output Capacitance

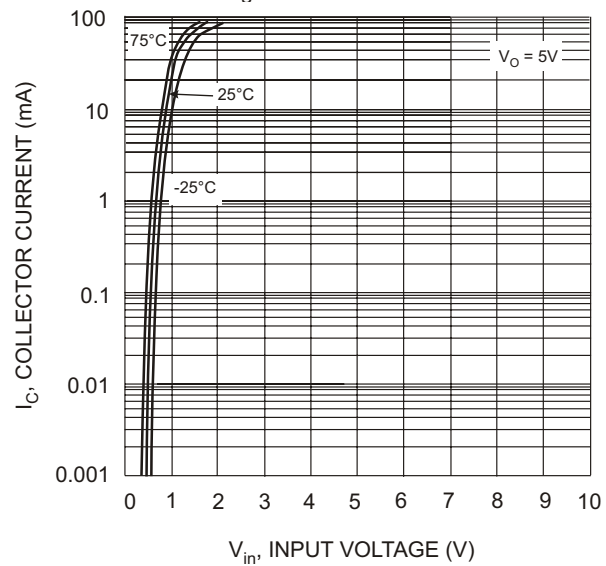


Fig. 5 Collector Current Vs. Input Voltage

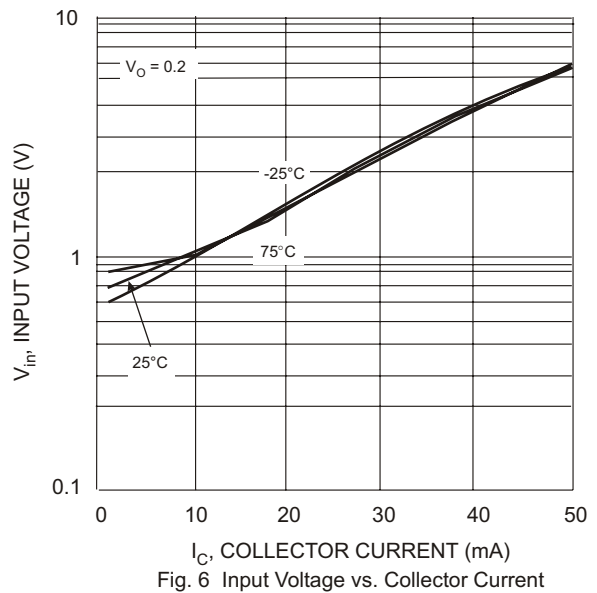


Fig. 6 Input Voltage vs. Collector Current