

# **HD74HC05**

# Hex Inverters (with open drain outputs)

REJ03D0535-0200 (Previous ADE-205-407) Rev.2.00 Oct 06, 2005

#### **Features**

High Speed Operation: t<sub>pd</sub> = 8 ns typ (C<sub>L</sub> = 50 pF)
 High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage:  $V_{CC} = 2$  to 6 V

• Low Input Current: 1 µA max

• Low Quiescent Supply Current:  $I_{CC}$  (static) = 1  $\mu$ A max (Ta = 25°C)

Protection diode inclusion between output and V<sub>CC</sub>

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC05P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74HC05FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74HC05RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)
HD74HC05TELL	TSSOP-14 pin	PTSP0014JA-B (TTP-14DV)	Т	ELL (2,000 pcs/reel)

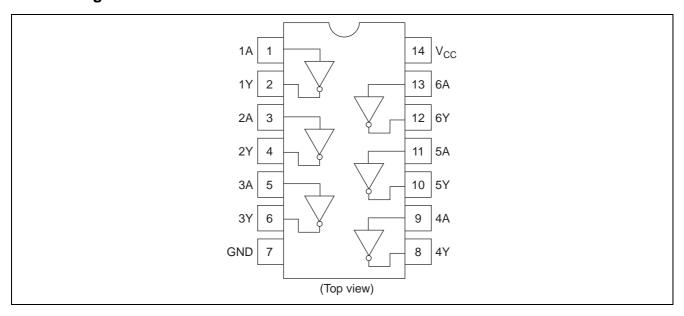
Note: Please consult the sales office for the above package availability.

### **Function Table**

Input	Output
A	Υ
L	Н
Н	L

H: High levelL: Low level

## **Pin Arrangement**



## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V
Input / Output voltage	Vin, Vout	-0.5 to V <sub>CC</sub> +0.5	V
Input / Output diode current	I <sub>IK</sub> , I <sub>OK</sub>	±20	mA
Output current	I <sub>0</sub>	±25	mA
V <sub>CC</sub> , GND current	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA
Power dissipation	P <sub>T</sub>	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

## **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V <sub>CC</sub>	2 to 6	V	
Input / Output voltage	V <sub>IN</sub> , V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	Та	-40 to 85	°C	
		0 to 1000		$V_{CC} = 2.0 \text{ V}$
Input rise / fall time*1	$t_r, t_f$	0 to 500	ns	$V_{CC} = 4.5 \text{ V}$
		0 to 400		$V_{CC} = 6.0 \text{ V}$

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

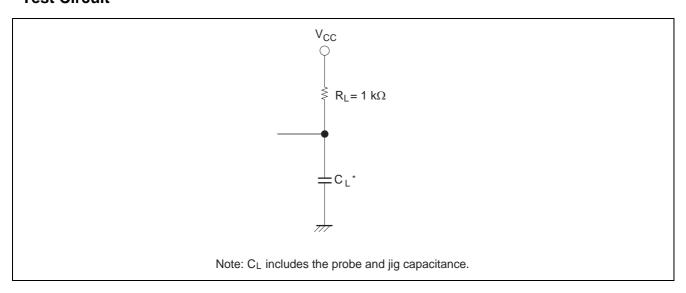
## **Electrical Characteristics**

			Т	a = 25°	С	Ta = -40 to+85°C				
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Condi	tions
Input voltage	V <sub>IH</sub>	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15	_			
		6.0	4.2	_	_	4.2	_			
	V <sub>IL</sub>	2.0		_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35			
		6.0	_	_	1.8	_	1.8			
Output voltage	V <sub>OL</sub>	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL} \mid I_{OI}$	L = 20 μA
		4.5	_	0.0	0.1	_	0.1			
		6.0	_	0.0	0.1	_	0.1			
		4.5	_	_	0.26	_	0.33		Ioi	L = 4 mA
		6.0	_	_	0.26	_	0.33		Ioi	<sub>L</sub> = 5.2 mA
Off-state output	lo(off)	6.0	_	_	±0.5	_	±5.0	μΑ	Vin = V <sub>IH</sub> or V <sub>IL</sub> ,	
current									Vout = $V_{CC}$ or GND	
Input current	lin	6.0			±0.1		±1.0	μΑ	Vin = $V_{CC}$ or GND Vin = $V_{CC}$ or GND, lout = 0 $\mu$ A	
Quiescent supply current	Icc	6.0		_	1.0	_	10	μΑ		

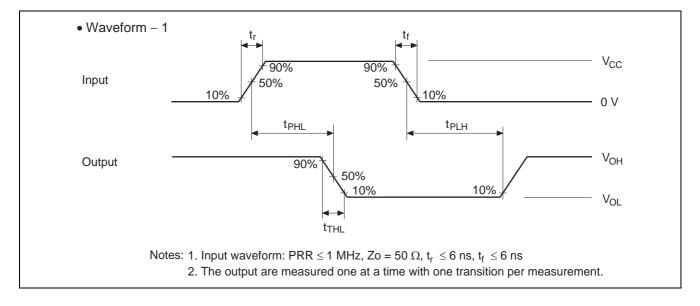
# Switching Characteristics ( $C_L = 50 \text{ pF}$ , Input $t_r = t_f = 6 \text{ ns}$ )

			Ta = 25°C		$Ta = -40 \text{ to } +85^{\circ}C$				
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t <sub>PLH</sub>	2.0	_	_	90	_	115	ns	
time		4.5	_	10	18	_	23		
		6.0	_	_	15	_	20		
	t <sub>PHL</sub>	2.0	_	_	90	_	112	ns	
		4.5	_	6	18	_	22		
		6.0	_	_	15	_	18		
Output fall time	t <sub>THL</sub>	2.0	_	_	75	_	95	ns	
		4.5	_	5	15	_	19		
		6.0	_	_	13	_	16		
Input capacitance	Cin	_	_	5	10	_	10	pF	

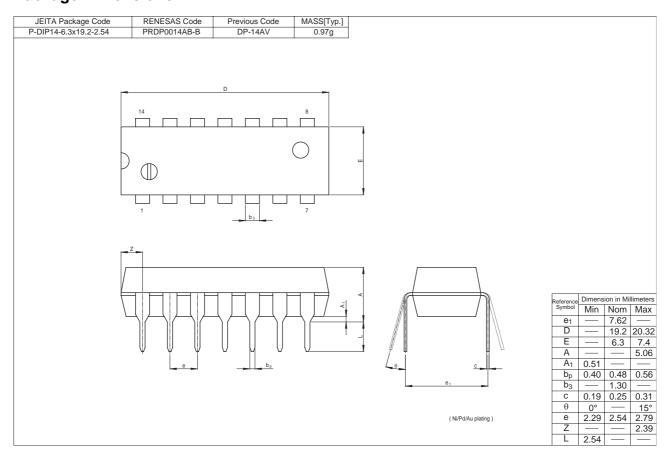
## **Test Circuit**

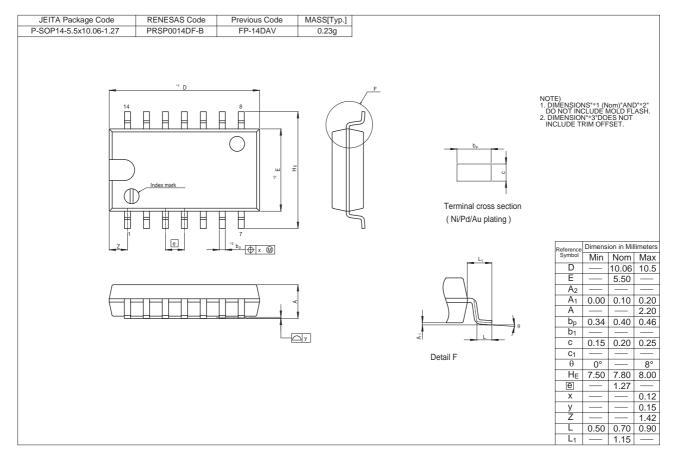


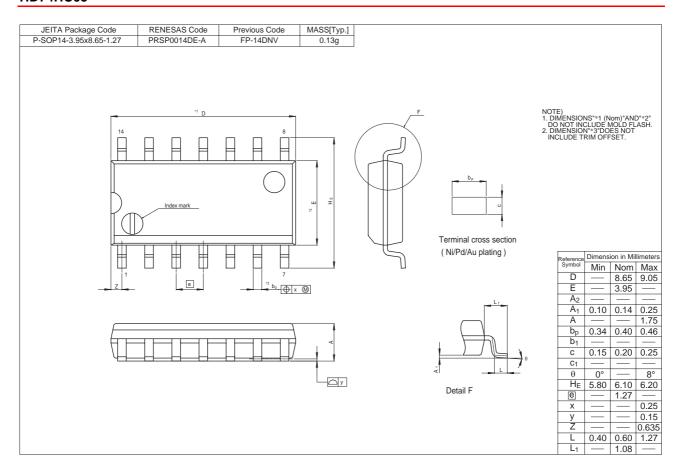
#### Waveforms

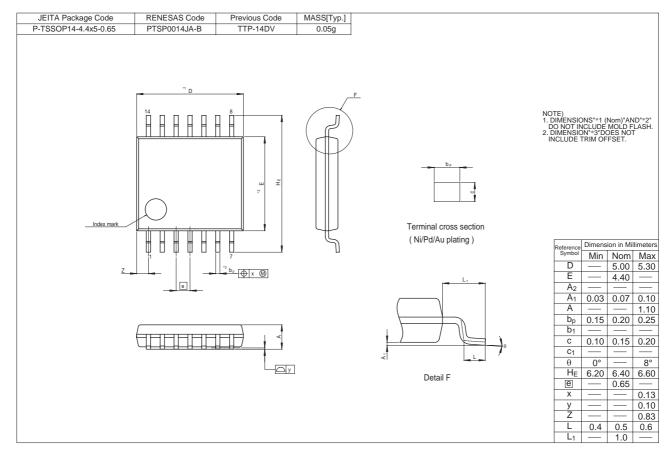


## **Package Dimensions**









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