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RTT Series Thick Film Chip Resistors Product Specification

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1 Scope:

- 1.1 This specification is applicable to lead free and halogen free of RoHS directive for RTT series thick film chip resistors
- 1.2 The product is for general electronic purpose.

2 Explanation Of Part Numbers:

(EX)

	<u>0</u>	2			
Туре	Size	Nor	ninal Resistance	Resistance Tolerance	Packaging(Refer to IE-SP-055)
RTT series	01(0201) 5% 02(0402) 03(0603) 03(0603) 05(0805)	2% 5% (3-Digit)	EX. 10Ω=100 4.7Ω=4R7 JUMPER=000	$B = \pm 0.1\%$ $D = \pm 0.5\%$	Q1 : 1 mm Pitch Carrier Tape 20000 pcs QE : 1 mm Pitch Carrier Tape 150000 pcs TH : 2 mm Pitch Carrier Tape 10000 pcs H0 : 2 mm Pitch Carrier Tape 15000 pcs H1 : 2 mm Pitch Carrier Tape 20000 pcs H2 : 2 mm Pitch Carrier Tape 20000 pcs H3 : 2 mm Pitch Carrier Tape 30000 pcs H4 : 2 mm Pitch Carrier Tape 40000 pcs
Thick Film Chip Resistors	06(1206) 12(1210) 18(1812) 20(2010) 25(2512)	0.1% 0.5% 1% (4-Digit)	EX. 10.2Ω=10R2 10KΩ=1002 JUMPER=0000	- F=± 1% G=± 2% J=± 5%	 H5 : 2 mm Pitch Carrier Tape 50000 pcs H6 : 2 mm Pitch Carrier Tape 60000 pcs TP : 4 mm Pitch Carrier Tape 5000 pcs P2 : 4 mm Pitch Carrier Tape 10000 pcs P3 : 4 mm Pitch Carrier Tape 15000 pcs P4 : 4 mm Pitch Carrier Tape 20000 pcs TE : 4 mm Pitch Carrier Tape 4000 pcs E6 : 8 mm Pitch Carrier Tape 2000 pcs BA : Bulk Case

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3 General Specifications:

Туре	Rated Power at	Max. Working	Max. Overload	T.C.R (ppm/		Resistanc	e Range			PER ted rent	JUM Resis Va	tance		
	70℃	Voltage	Voltage	ິ 🗘	B(±0.1%) E-24 ∖ E-96	D(±0.5%) E-24	F(±1%) E-24 丶 E-96	G(±2%) ∖ J(±5%) E-24	J (±5%)	F (±1%)	J (±5%)	F (±1%)		
RTT01	W	25V	50V	-200 +400		$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	0.5A	0.5A	50mΩ	35mΩ		
(0201)	20			±200	$47\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega\!\leq\!R\!\leq\!10M\Omega$	$10\Omega\!\leq\!R\!\leq\!10M\Omega$	$10\Omega\!\leq\!R\!\leq\!10M\Omega$			MAX.	MAX.		
RTT02	W	50V	100V	±100	$100\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega{\le}R{\le}1M\Omega$	$10\Omega\!\leq\!R\!\leq\!22M\Omega$	$10\Omega{\leq}R{\leq}22M\Omega$	1A	1.5A	50mΩ	20mΩ		
(0402)	16	500	1000	±200		$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	IA	I.5A	MAX.	MAX.		
RTT03	W	75V	150V	±100	$100\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega{\le}R{\le}1M\Omega$	$10\Omega\!\leq\!R\!\leq\!22M\Omega$	$10\Omega{\le}R{\le}22M\Omega$	1A		50mΩ	20mΩ		
(0603)	3) 10 VV	750	750	750	1500	±200		$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	1A	2A	MAX.	MAX.
RTT05	W	450)/	300V	±100	$100\Omega{\leq}R{\leq}1.5M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}27M\Omega$	$10\Omega{\leq}R{\leq}27M\Omega$		2.5A	50mΩ MAX.	20mΩ MAX.		
(0805)	8	150V	3007	±200		$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	2A					
RTT06	W	200V	400V	±100	$10\Omega{\leq}R{\leq}1M\Omega$	$10\Omega{\le}R{\le}10M\Omega$	$10\Omega{\leq}R{\leq}27M\Omega$	$10\Omega{\le}R{\le}27M\Omega$	2A	3.5A	50mΩ	20mΩ		
(1206)	4		400 v	±200	$3\Omega\!\leq\!R\!<\!10\Omega$	$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	$1\Omega\!\leq\!R\!<\!10\Omega$	ZA	3.3A	MAX.	MAX.		
RTT12	1	0001/	400V	±100	$100\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega{\le}R{\le}10M\Omega$	$10\Omega{\leq}R{\leq}27M\Omega$	$10\Omega{\le}R{\le}27M\Omega$		4A	50mΩ	20mΩ		
(1210)	<u>1</u> 2	V 200V	2000	4007	±200			$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\le}R{<}10\Omega$	2A	4A	MAX.	MAX.	
RTT18	<u>3</u> _W	0001/	400)/	±100	$100\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega{\le}R{\le}10M\Omega$	$10\Omega{\leq}R{\leq}20M\Omega$	$10\Omega{\le}R{\le}20M\Omega$		5 A	50mΩ	20mΩ		
(1812)	4	200V	400V	±200			$1\Omega{\leq}R{<}10\Omega$	$1\Omega\!\leq\!R\!<\!10\Omega$	2A	5A	MAX.	MAX.		
RTT20	<u>3</u> W	0001	400)/	±100	$100\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}20M\Omega$	$10\Omega{\leq}R{\leq}20M\Omega$		5 A	50mΩ	20mΩ		
(2010)	4	200V	400V	±200			$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	2A	5A	MAX.	MAX.		
RTT25	4144	0001	400)/	±100	$100\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}20M\Omega$	$10\Omega{\leq}R{\leq}20M\Omega$		7.0	50mΩ	20mΩ		
(2512)	1W	200V	400V	±200			$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	2A	7A	MAX.	MAX.		
Oper	ating Tem	perature	Range		•	−55 °C ~ +	155°C (0201: −	− 55 °C ~ +125°	C)					

3.1 Power Derating Curve:

-	Derating Curve:	
Туре	RTT01 (0201)	Other
Operating Temperature Range	− 55 °C ~ + 125 °C	−55°C ~ +155°C
Explain	For resistors operated in ambient temperatures above 70° C, power rating shall be derated in accordance with figure below.	For resistors operated in ambient temperatures above 70° C, power rating shall be derated in accordance with figure below.
Figure	(%) How	100 70 80 60 40 155 20 40 155 20 40 60 40 155 20 40 100 40 120 140 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40 40
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3.2 Voltage Rating :

Rated Voltage: The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following

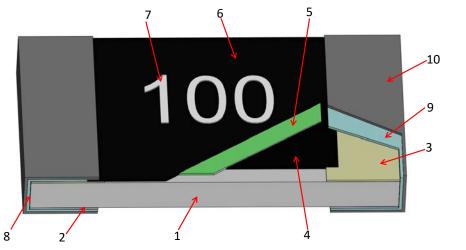
$$E = \sqrt{R \times P}$$

E= Rated voltage (v) P= Power rating (w) R= Nominal resistance(Ω)

4 Dimensions:

-						Unit:mm
Туре	Dimension Size Code	L	W	н	L1	L2
RTT01	0201	0.60±0.03	0.30±0.03	0.23±0.03	0.10±0.05	0.15±0.05
RTT02	0402	1.00±0.10	0.50±0.05	0.30±0.05	0.20±0.10	0.25±0.10
RTT03	0603	1.60±0.10	0.80±0.10	0.45±0.10	0.30±0.15	0.30±0.15
RTT05	0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.15
RTT06	1206	3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.35±0.15
RTT18	1812	4.40±0.20	3.15±0.20	0.47±0.20	0.60±0.20	0.60±0.20
RTT12	1210	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
RTT20	2010	5.00±0.20	2.50±0.20	0.55±0.10	0.60±0.20	0.60±0.20
RTT25	2512	6.30±0.20	3.20±0.20	0.55±0.10	0.60±0.20	0.60±0.20

5 Structure Graph:



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7	Marking
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating

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	Reliabilit	-				
6	5.1 Electri	ical Performance Test				
	Item	Conditions	Spec Resistors	cifications		
		(<i>R</i> 2- <i>R</i> 1)	Refer to item 3. gene		Jumper NA	
			specifications	iai	11/1	
-	Comporaturo	R1: Resistance at room temperature				
Ċ	Coefficient of	R2: Resistance at -55 $^{\circ}$ or +125 $^{\circ}$				
		T1: Room temperature				
		T2: Temperature -55℃ or +125℃				
		Refer to JIS-C5201-1 4.8				
	Chart Times		0.1%、0.5%、1%:∆I		Refer to item	
	Short Time Overload	load for about 30 minutes, then measure its resistance variance rate. (Rated voltage refer to item 3. general specifications)	2% \ 5%:∆F		 general specifications 	
	Oventidau	Tale. (Naled Vollage Telef to item 5. general specifications)			specifications	
		Refer to JIS-C5201-1 4.13				
		Put the resistor in the fixture, add 100 VDC in + ,- terminal for 60	$\geq 10^{9}\Omega$			
		sec then measured the insulation resistance between electrodes				
		and insulating enclosure or between electrodes and base material. Refer to JIS-C5201-1 4.6				
		Insulating plate				
	Insulation Resistance	Metal block measuring <u>Point A</u> Metal plate measuring point B				
	Resistance					
		Base material Specimen Pressurizing by spring Insulating enclosure surface R0.5mm				
		Put the resistor in the fixture, add VAC (see SPEC below) in +,-	No short or burned or	n the appea	rance.	
	Dielectric	terminal for.				
	Withstand	RTT05 \ 06 \ 12 \ 18 \ 20 \ 25 apply 500 VAC 1 minute.				
	Voltage	RTT01 \ 02 \ 03 apply 300 VAC 1 minute.				
		Refer to JIS-C5201-1 4.7				
		Put the tested resistor in chamber under temperature 25±2°C and	∆R%=±5.0%		Refer to item	
		load 2.5 times rated DC voltage for 1 sec on, 25 sec off,			3. general	
		10000^{+400}_{0} test cycles, then it be left at no-load for 1 hour, then			specifications	
		measure its resistance variance rate.				
	Intermittent	Jumper : Applied Maximum overload current				
	Overload	Type RTT01 RTT02 RTT03 RTT05 RTT06 RTT12 RTT18 RTT20 RTT25				
		Jumper (0201) (0402) (0603) (0805) (1206) (1210) (1812) (2010) (2512)				
		±5% 1.25A 2.5A 5A 5A 5A 5A 5A ±1% 1.25A 3.75A 5A 6.25A 8.75A 10A 12.5A 12.5A 17.5A				
		Refer to JIS-C5201-1 4.13				
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Terminal Strength Resistance to Solvent	Conditions Test 1 : The resistor mounted on the board applied 5N pushing proce on the sample rear for 10 sec. (RTT01:3N) The resistor mounted on the board slowly add force on the ample rear until the sample termination is breakdown. Refer to JIS-C5201-1 4.16 The tested resistor be immersed into isopropyl alcohol of 20~25°C or 5 minutes, then the resistor is left in the room for 48 hrs, and	Resistors Test 1 : No evidence of mechanical o Test 2 : RTT01≧3N Other Type≧5N	Jumper lamage.
Terminal Strength Resistance to Solvent	brce on the sample rear for 10 sec. (RTT01:3N) Test 2 : The resistor mounted on the board slowly add force on the ample rear until the sample termination is breakdown. Refer to JIS-C5201-1 4.16 The tested resistor be immersed into isopropyl alcohol of 20~25°C or 5 minutes, then the resistor is left in the room for 48 hrs, and	Test 2 : RTT01≧3N	lamage.
T Resistance to Solvent	he tested resistor be immersed into isopropyl alcohol of $20~25^{\circ}$ C or 5 minutes, then the resistor is left in the room for 48 hrs, and		
Resistance to ^{fc} Solvent	or 5 minutes, then the resistor is left in the room for 48 hrs, and		
Solvent			Refer to iter
Solvent	neasured its resistance variance rate.	Type RTT01 Other	 general specificatior
п		$\triangle R\% \triangle R\% = \pm 1.0\% \triangle R\% = \pm 0.5\%$	specification
ĸ	Refer to JIS-C5201-1 4.29		
P of di te Solderability T fc	Preconditioning Put the tested resistor in the apparatus of PCT, at a temperature f 105° , humidity of 100% RH, and pressure of 1.22×105 Pa for a uration of 4 hours. Then after left the tested resistor in room emperature for 2 hours or more. Test method: The resistor be immersed into solder pot in temperature $235 \pm 5^{\circ}$ or 2 sec, then the resistor is left as placed under microscope to bserved its solder area.	Solder coverage over 95%	
R	tefer to JIS-C5201-1 4.17		
		Test item 1:	Refer to iten
-	he tested resistor be immersed into molten solder of 260+5/-0 $^\circ\mathrm{C}$	(1).Variance rate on resistance	3. general
fc	or 10 seconds. Then the resistor is left in the room for 1 hour.	∆R%=±1.0%	specificatior
e	Test method 2 (Solder pot test):	Test item 2:	
	The tested resistor be immersed into molten solder of $260+5/-0^{\circ}$	(1).Solder coverage over 95%.	
	or 30 seconds. Then the resistor is left as placed under	(2).The underlying material	
Resistance to m	nicroscope to observe its solder area.	(such as ceramic) shall not be visible at the crest corner	
Soldering Heat	Toot mathed 2 (Floatria internation	area of the electrode.	
G	DTest method 3 (Electric iron test): Preheating temperature : 350±10℃		
		Test item 3: (1) Variance rate on resistance	
P st	Preheating the electric iron on electrode termination, as after that tep placed the iron over 60 min. and measured its resistance ariance rate.	(1).Variance rate on resistance △R%=±1.0%	
R	Refer to JIS-C5201-1 4.18		
<u>F</u> =			

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	Item	Conditions	Specifications Resistors	Jumper
			△R%=±1.0%	Refer to item 3. general specifications
s	Joint Strength of Solder	45 45 Chip resistor		
		Pressurtze		
		Refer to JIS-C5201-1 4.33	<u> </u>	<u> </u>
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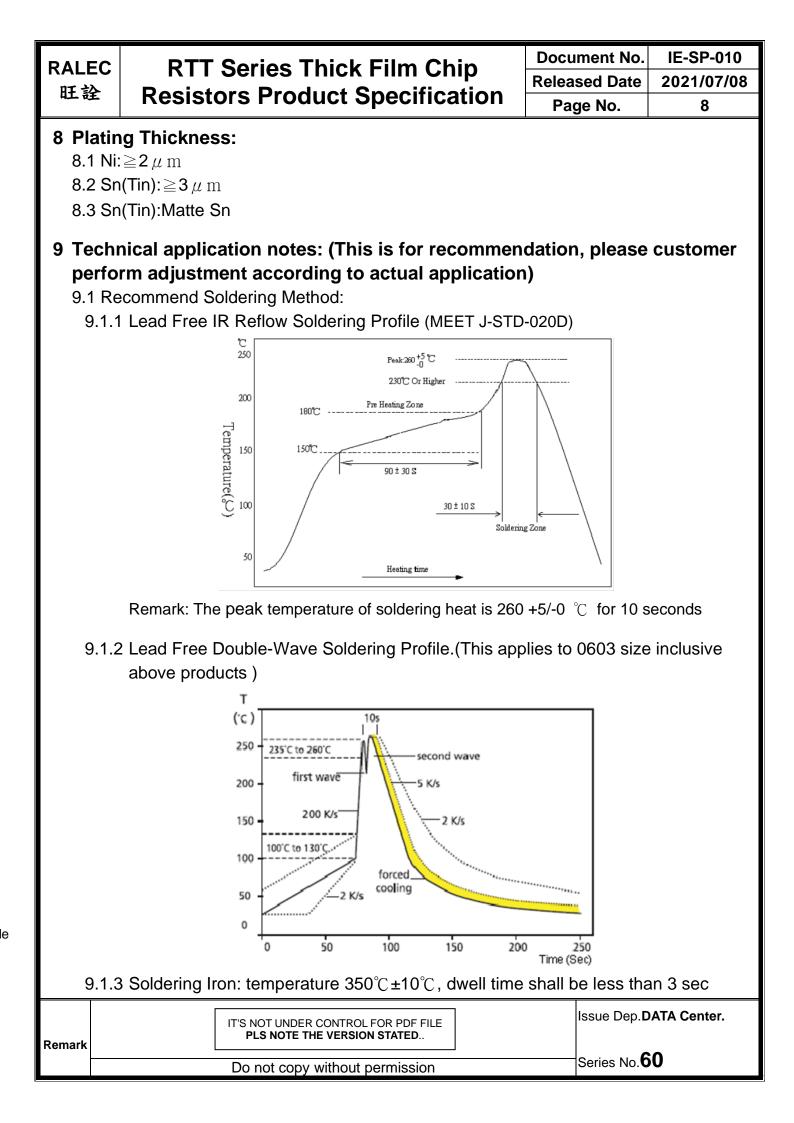
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6.3 Environmental Test

Item	m Conditions			Specifications	
Item	Conditions			stors	Jumper
Put tested resistor in chamber under temperature 155±5℃ for 1000 +48/-0 hours. Then leaving the tested resistor in room Resistance to Dry Heat RTT01 for 125±3℃) Refer to JIS-C5201-1 4.25			0.1%、0.5%、1' 2%、5	%:∆R%=±1.0% %:∆R%=±2.0%	Refer to item 3. general specifications
	Put the tested resistor in the chamber which shown in the following table sha consecutively. Then leaving the tested temperature for 1 hours, and measure rate.	0.1%	%:△R%=±0.5% %:△R%=±1.0%	Refer to item 3. general specifications	
Thermal Shock	Testing Condi	tion			
SHUCK	Lowest Temperature	-55±5℃			
	Highest Temperature	125±5 ℃			
	Temperature-retaining time Refer to MIL-STD 202 Method 107	15 minutes each			
Loading Life in Moisture	Put the tested resistor in the chamber relative humidity 90~95% and load the minutes on, 30 minutes off, total 1000 tested resistor in room temperature fo its resistance variance rate. Refer to JIS-C5201-1 4.24	Type RTT01 1%: △R%=±1.0% 5%: △R%=±3.0%	2%、5%:	Refer to item 3. general specifications	
Load Life	Put the tested resistor in chamber und and load the rated voltage for 90 minut total 1000 hours. Then leaving the test temperature for 60 minutes, and meas variance rate. Refer to JIS-C5201-1 4.25	utes on, 30 minutes off, sted resistor in room	Type RTT01 1%: △R%=±1.0% 5%: △R%=±3.0%	2%、5%:	Refer to item 3. general specifications

7 Measurement Point:

7 IVIE	easurement Point:			
	Bottom electrode			Unit : mm
		DIM TYPE	Α	В
		RTT01	0.44±0.05	0.22±0.05
		RTT02	0.80±0.05	0.24±0.05
		RTT03	1.35±0.05	0.35±0.05
		RTT05	1.80±0.05	0.35±0.05
		RTT06	2.90±0.05	0.35±0.05
		RTT12	2.90±0.05	0.35±0.05
	 Current Terminal 	RTT18	3.70±0.05	0.60±0.05
		RTT20	4.50±0.05	1.15±0.05
	 Voltage Terminal 	RTT25	5.90±0.05	1.60±0.05
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9.2 Recommend Land Pattern Design (For Reflow Soldering)

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.

			l	Jnit:mm
	DIM TYPE	A	В	С
	RTT01	0.3	1.0	0.4
	RTT02	0.5	1.5	0.6
	RTT03	0.8	2.1	0.9
	RTT05	1.2	3.0	1.3
	RTT06	2.2	4.2	1.6
В	RTT12	2.2	4.2	2.8
	RTT18	3.1	5.9	3.0
	RTT20	3.5	6.1	2.8
	RTT25	3.8	8.0	3.5

9.3 Environment Precautions:

This specification product is for general electronic use, RALEC will not be responsible for any damage, cost or loss caused by using this specification product in any special environment. If other applications need to confirm with RALEC.

If consumer intends to use our Company product in special environment or condition (including but not limited to those mentioned below), then will need to make individual recognition of product features and reliability accordingly.

- (a) Used in high temperature and humidity environment;
- (b) Exposed to sea breeze or other corrosive gas, such as Cl2 \ H2S \ NH3 \ SO2 and NO2;
- (c) Used in non-verified liquids including water, oil, chemical and organic solvents;
- (d) Using non-verified resin or other coating material to seal or coat our Company product;
- (e) After soldering, it is necessary to use water-soluble detergents to clean residual solder
- (f) fluxes, even though no-clean fluxes are recommended.

9.4 Momentary Overload Precautions:

The product might be out of function when momentary overloaded. Please make sure to avoid momentary overloading while using and preserving.

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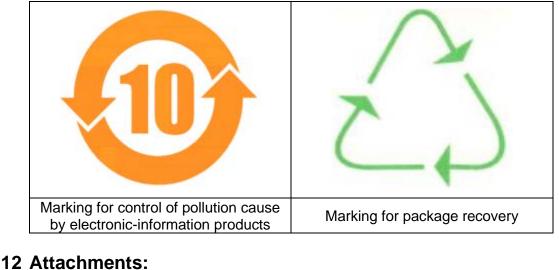
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- 9.5 Operation and Processing Precautions:
 - (a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.
 - (b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
 - (c) Make sure the power rating is under the limit when using the resistor. When power rating is over the limit, the resister will be overloaded. There might be machinery damage due to the climbing temperature.
 - (d) If the resister will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.
 - (e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of it's fail-safe design to ensure the system safety.

10 Stock period:

- 10.1 The temperature condition must be controlled at 25±5℃, the R.H. must be controlled at 60±15%. The stock can maintain quality level in two years.
- 10.2 Please avoid the mentioned harsh environment below when storing to ensure product performance and its' weldability. Places exposed to sea breeze or other corrosive gas, such as Cl₂ 、 H₂S 、 NH₃ 、 SO₂ and NO₂.
- 10.3 When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

11 The carton packaged for electronic-information products is made by the symbol as follows: (For china)



12 Attachments:

12.1 Document Revise Record (QA-QR-027)

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