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Octal Bi-directional Transceiver with 3-State Input/Output

RENESAS

ADE-205-384 (Z) 1st. Edition Sep. 2000

Description

The HD74AC245/HD74ACT245 contains eight non-inverting bidirectional buffers with 3-state outputs and is intended for bus-oriented applications. Current sinking capability is 24 mA at both the A and B ports. The Transmit/Receive (T/\overline{R}) input determines the direction of data flow through the bidirectional transceiver. Transmit (active-High) enables data from A ports to B ports; Receive (active-Low) enables data from B ports to A ports. The Output Enable input, when High, disables, both A and B ports by placing them in a High Z condition.

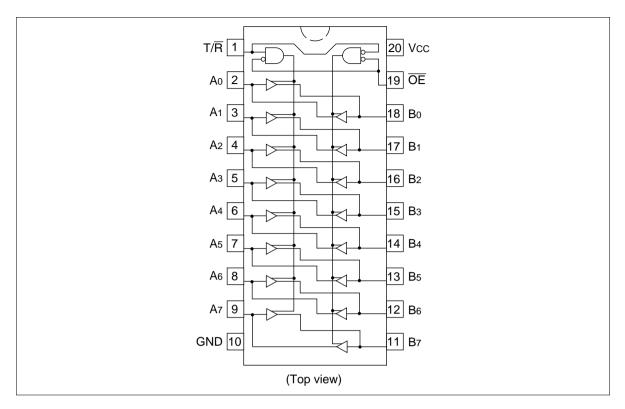
Features

- Noninverting Buffers
- Bi-directional Data Path
- A and B Outputs Source/Sink 24 mA
- HD74ACT245 has TTL-Compatible Inputs

Pin Names

- \overline{OE} Output Enable Input T/\overline{R} Transmit/Receive Input
- A_0 to A_7 Side A 3-State Inputs or 3-State Outputs
- B_0 to B_7 Side B 3-State Inputs or 3-State Outputs

Pin Arrangement



Truth Tables

Inputs

| OE | T/R | Outputs | |
|----|-----|---------------------|--|
| L | L | Bus B Data to Bus A | |
| L | Н | Bus A Data to Bus B | |
| Н | Х | High Z State | |

H : High Voltage Level

L : Low Voltage Level

X : Immaterial

DC Characteristics (unless otherwise specified)

| Item | Symbol | Max | Unit | Condition |
|---|------------------|-----|------|---|
| Maximum quiescent supply current | I _{cc} | 80 | μΑ | $V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 V$, Ta = Worst case |
| Maximum quiescent supply current | I _{cc} | 8.0 | μΑ | $V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$, Ta = 25°C |
| Maximum additional I _{cc} /input (HD74ACT245) | I _{CCT} | 1.5 | mA | $V_{IN} = V_{CC} - 2.1 \text{ V}, V_{CC} = 5.5 \text{ V},$ Ta = Worst case |

AC Characteristics: HD74AC245

| | | | Ta = + C _⊾ = 50 | | | Ta = −40°C to +85°C C _L = 50 pF | | |
|---------------------|------------------|-----------------------------------|-------------------------------|-----|------|---|------|------|
| ltem | Symbol | V _{cc} (V)* ¹ | Min | Тур | Мах | Min | Max | Unit |
| Propagation delay | t _{PLH} | 3.3 | 1.0 | 5.0 | 8.5 | 1.0 | 9.0 | ns |
| Data to output | | 5.0 | 1.0 | 3.5 | 6.5 | 1.0 | 7.0 | |
| Propagation delay | t _{PHL} | 3.3 | 1.0 | 5.0 | 8.5 | 1.0 | 9.0 | ns |
| Data to output | | 5.0 | 1.0 | 3.5 | 6.0 | 1.0 | 7.0 | |
| Output enable time | t _{zH} | 3.3 | 1.0 | 7.0 | 11.5 | 1.0 | 12.5 | ns |
| | | 5.0 | 1.0 | 5.0 | 8.5 | 1.0 | 9.0 | |
| Output enable time | t _{zL} | 3.3 | 1.0 | 7.5 | 12.0 | 1.0 | 13.5 | ns |
| | | 5.0 | 1.0 | 5.5 | 9.0 | 1.0 | 9.5 | |
| Output disable time | t _{HZ} | 3.3 | 1.0 | 6.5 | 12.0 | 1.0 | 12.5 | ns |
| | | 5.0 | 1.0 | 5.5 | 9.0 | 1.0 | 10.0 | |
| Output disable time | t _{LZ} | 3.3 | 1.0 | 7.0 | 11.5 | 1.0 | 13.0 | ns |
| | | 5.0 | 1.0 | 5.5 | 9.0 | 1.0 | 10.0 | _ |

Note: 1. Voltage Range 3.3 is $3.3 \text{ V} \pm 0.3 \text{ V}$ Voltage Range 5.0 is $5.0 \text{ V} \pm 0.5 \text{ V}$

RENESAS

AC Characteristics: HD74ACT245

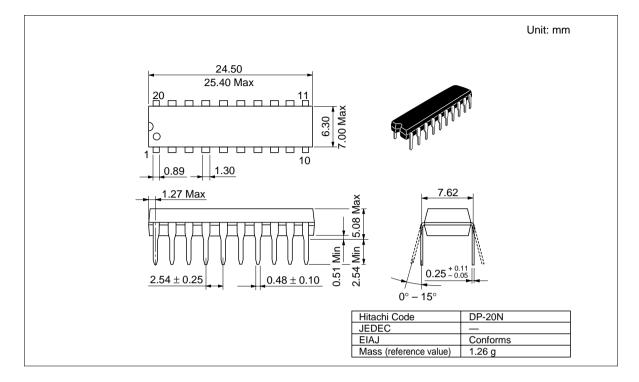
| | | V _{cc} (V)* ¹ | Ta = +25°C C _∟ = 50 pF | | | Ta = −40°C to +85°C C _L = 50 pF | | |
|-------------------------------------|------------------|-----------------------------------|--------------------------------------|-----|------|---|------|------|
| Item | Symbol | | Min | Тур | Max | Min | Max | Unit |
| Propagation delay Data to output | t _{PLH} | 5.0 | 1.0 | 4.0 | 7.5 | 1.0 | 8.0 | ns |
| Propagation delay Data to output | t _{PHL} | 5.0 | 1.0 | 4.0 | 8.0 | 1.0 | 9.0 | ns |
| Output enable time | t _{zH} | 5.0 | 1.0 | 5.0 | 10.0 | 1.0 | 11.0 | ns |
| Output enable time | t _{zL} | 5.0 | 1.0 | 5.5 | 10.0 | 1.0 | 12.0 | ns |
| Output disable time | t _{HZ} | 5.0 | 1.0 | 5.5 | 10.0 | 1.0 | 11.0 | ns |
| Output disable time | t _{LZ} | 5.0 | 1.0 | 5.0 | 10.0 | 1.0 | 11.0 | ns |

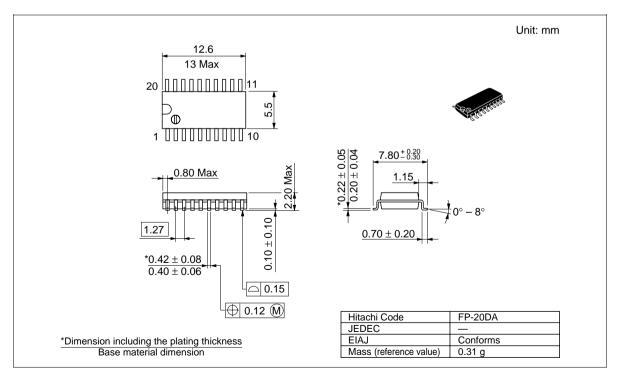
Note: 1. Voltage Range 5.0 is 5.0 V \pm 0.5 V

Capacitance

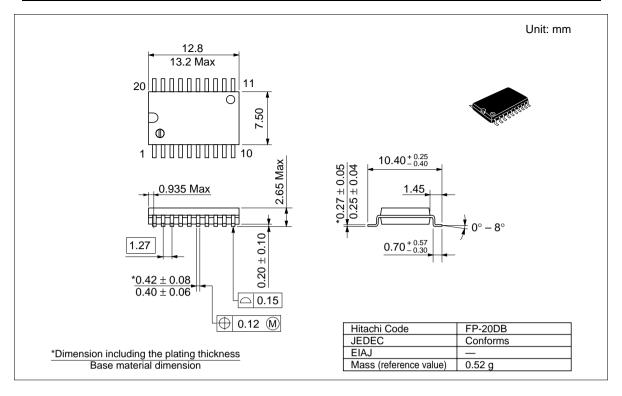
| Item | Symbol | Тур | Unit | Condition |
|-------------------------------|-----------------|------|------|------------------|
| Input capacitance | CIN | 4.5 | pF | $V_{cc} = 5.5 V$ |
| Input/output capacitance | CI/O | 15.0 | pF | $V_{cc} = 5.5 V$ |
| Power dissipation capacitance | C _{PD} | 45.0 | pF | $V_{cc} = 5.0 V$ |

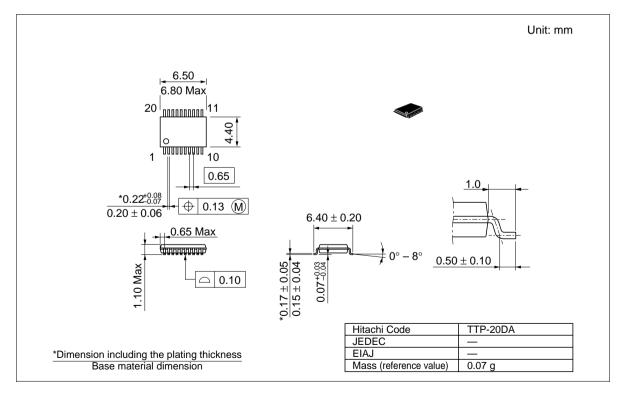
Package Dimensions











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