SN54BCT373, SN74BCT373 OCTAL D-TYPE TRANSPARENT LATCHES WITH 3-STATE OUTPUTS

D3145. SEPTEMBER 1988—REVISED OCTOBER 1988

- 8-Latches in a Single Package
- 3-State Bus-Driving True Outputs
- Full Parallel Access for Loading
- State of the Art BICMOS Design Significantly Reduces Icc
- Comparable Speed and Improved Power Performance Relative to 54F/74F373
- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- ESD Protection Exceeds 2000 V per MIL-STD-883C. Method 3015
- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers, and Standard Plastic Ceramic 300-mil DIPs

description

These 8-bit latches feature three-state outputs designed specifically for driving highly capacitive or relatively low-impedance loads. They are particularly suitable for implementing buffer registers, I/O ports, bidirectional bus drivers, and working registers.

The eight latches of the 'BCT373 are transparent D-type latches. While the enable (C) is high, the Q outputs will follow the data (D) included in the enable is taken low, the Q outputs will be latched at the levels that were set up at the D inputs.

A buffered output-control (\overline{OC}) input can be used to place the eight outputs in either a normal logic state (high or low logic levels) or a high-impedance state. In the high-impedance state the outputs neither load nor drive the bus lines significantly. The high-impedance third state and increased drive provide the capability to drive the bus lines in a bus-organized system without need for interface components.

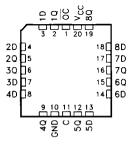
The output control (OC) does not affect the internal operations of the latches. Old data can be retained or new data can be entered while the outputs are off.

The SN54BCT373 is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74BCT373 is characterized for operation from 0°C to 70°C.

SN54BCT373 ... J PACKAGE SN74BCT373 ... DW OR N PACKAGE (TOP VIEW)

oc [1	U 20	þν _{cα}
1Q [2	19] 8Q
10[3	18	8D
2D 🗌	4	17	70
20 [5	16] 7Q
3Q 🗌	6	15] 6Q
3D 🗌	7	14	☐ 6D
4D 🗌	8	13] 5D
40 🗆	9	12] 5Q
GND [10	11	рс

SN54BCT373 ... FK PACKAGE (TOP VIEW)



FUNCTION TABLE

	INPUTS	OUTPUT	
<u>56</u>	ENABLE C	D	Q
L	н	н	н
L	н	L	L
L	L	X	Q ₀
н	×	X	z

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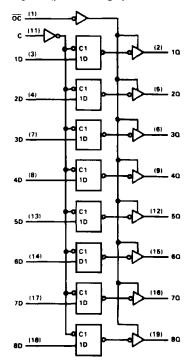
logic symbol†

ᅈᄱ	EN			
c (11)	C1		l	
1D (3)	1D	D	∽몫	(2) 10
2D (4)	U		<u> </u>	(5) 20
3D (7)	-		┪	(6) 3Q
4D (8)			一.	(9) 40
5D (13)	\vdash		1	(12) 5Q
6D (14)			┪	(15) 60
7D (17)			⊣	(16) 70
8D (18)			⇉	(19) 80

† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for DW, J, and N packages.

logic diagram (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage, VCC	-0.5 V to 7 V
Input voltage (see Note 1)	~0.5 V to 7 V
Voltage applied to any output in the disabled or power-off state	-0.5 V to 7 V
Voltage applied to any output in the high state	-0.5 V to VCC
Input clamp current	30 mA
Current into any output in the low state: SN54BCT373	96 mA
SN74BCT373	128 mA
Operating free-air temperature range: SN54BCT373	-55°C to 125°C
SN74BCT373	0°C to 70°C
Storage temperature range	-65°C to 150°C

^{\$} Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input negative-voltage rating may be exceeded if the input clamp current rating is observed.



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recommended operating conditions

		SI	154BCT37	3	18	UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX	0
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	٧
۷ін	High-level input voltage	2			2			· ·
٧IL	Low-level input voltage			8.0			8.0	>
İIK	Input clamp current			-18			18	mA
ЮН	High-level output current			-12			15	mA
lOL	Low-level output current			48			64	mA
TA	Operating free-air temperature	- 55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	PARAMETER TEST CONDITIONS			SN	154 B CT37	'3	SN	74BCT37	73	UNIT
AMAINE TEN				MIN	TYP	MAX	MIN	TYP	MAX	U.I.
VIK	V _{CC} = 4.5 V,	I _I = -18 mA				- 1.2			1.2	V
			IOH = -3 mA	2.4	3.3		2.4	3.3		
∨он	V _{CC} = 4.5 V		IOH = -12 mA	2	3.2					٧
			IOH = -15 mA				2	3.1		
VOL	V _{CC} = 4.5 V		IOL = 48 mA		0.38	0.55				v
1	100		IOL = 64 mA					0.42	0.55	•
lozh	V _{CC} = 5.5 V,	V _O = 2.7 V				50			50	μА
IOZL	$V_{CC} = 5.5 V$,	VO = 0.5 V				50			~ 50	μΑ
Ŋ	VCC = 5.5 V,	V _I = 5.5 V				0.4			0.4	mA
ΊΗ	$V_{CC} = 5.5 V$,	V _I = 2.7 V				20			20	μΑ
Iμ	V _{CC} = 5.5 V,	V _I = 0.5 V				-0.6			- 0.6	mA
los‡	V _{CC} = 5.5 V,	VO = 0		- 100		225	100		- 225	mA
ICCL	V _{CC} = 5.5 V				37	60		37	60	mA
Іссн	V _{CC} = 5.5 V				2	5		2	5	mA
Iccz	$V_{CC} = 5.5V$				5	8		5	8	mA
Ci	V _{CC} = 5 V,	$V_1 = 2.5 \text{ V or } 0.5 \text{ V}$			6	Ţ		6		ρF
Co	V _{CC} = 5 V,	$V_0 = 2.5 \text{V} \text{ or } 0.5 \text{V}$			11			11		pF

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25 ^{\circ}\text{C}$.

timing requirements

		V _{CC} = 5V, T _A = 25°C		1		UNIT		
1		'BCT	373	SN54B	CT373	SN74B	CT373]
1		MIN	MAX	MIN	MAX	MIN	MAX	
tsu	Setup time, Data before enable C ↓	2		2		2		ns
th	Hold time, Data after enable C↓	5.5		5.5		5.5		ns
tw	Pulse duration, Enable C high	7.5		7.5		7.5		ns

For conditions shown as MIN or MAX, use the appropriate value specified under Recommended Operating Conditions.



^{*} Not more than one output should be shorted at a time and the duration of the short circuit should not exceed one second.

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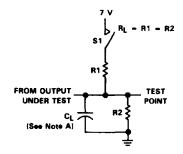
switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	VCC = 5 V, CL = 50 pF, R1 = 500 Ω, R2 = 500 Ω, TA = 25°C			R1 = R2 =	5 V to 5.5 V 50 pF, 500 Ω, 500 Ω, 1 to MAX [†]	•	UNIT	
				'BCT373		SN541	BCT373	SN748	3CT373	
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	1
t₽LH	D	Any Q	2	5.9	7.7	1.5	10.1	2	9.3	ns
t _{PHL}	D	Any Q	2	6.7	8.5	1	10.3	1.5	9.5	ns
tPLH	С	Any Q	2	6.2	8.2	2	10.1	2	9.3	ns
tPHL .	С	Any Q	2	5.9	7.8	2	9.2	2	8.8	ns
tPZH	ÖČ	Any Q	1	7.8	9.6	1	12.3	1	11.8	ns
[†] PZL	<u> </u>	Any Q	1	8.2	10.2	1	12.5	1	12	ns
tPHZ	<u> </u>	Any Q	1	4.9	6.6	1	7.4	1	7	ns.
tPLZ	ÖÖ	Any Q	1	5	6.7	1	8.1	1	7.4	ns

[†] For conditions shown as MIN or MAX, use the appropriate value specified under Recommended Operating Conditions.



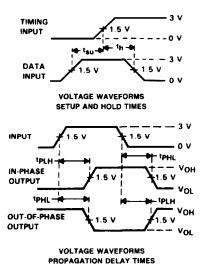
PARAMETER MEASUREMENT INFORMATION

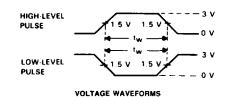


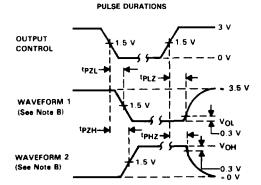
SWITCH POSITION TABLE

TEST	S1
tPLH	Open
tPHL	Open
tPZH	Open
tPZL	Closed
tPHZ	Open
†PLZ	Closed

LOAD CIRCUIT 1 ALL OUTPUTS EXCEPT FOR ERROR FLAG







VOLTAGE WAVEFORMS ENABLE AND DISABLE TIMES, THREE-STATE OUTPUTS

NOTES: A. C. includes probe and jig capacitance.

- B. Input pulses are supplied by generators having the following characteristics: PRR < 10 MHz, $Z_0 = 50 \Omega$, $t_f = 2.5$ ns. $t_f = 2.5$ ns.
- C. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- D. The outputs are measured one at a time with one input transition per measurement.

FIGURE 1. SWITCHING CHARACTERISTICS

